

ANNA UNIVERSITY, CHENNAI

AFFILIATED INSTITUTIONS

R - 2008

B.E. ELECTRONICS AND INSTRUMENTATION ENGINEERING

II - VIII SEMESTERS CURRICULA AND SYLLABI

SEMESTER II

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	HS2161	<u>Technical English – II*</u>	3	1	0	4
2.	MA2161	<u>Mathematics – II*</u>	3	1	0	4
3.	PH2161	<u>Engineering Physics – II*</u>	3	0	0	3
4.	CY2161	<u>Engineering Chemistry – II*</u>	3	0	0	3
5. a	ME2151	<u>Engineering Mechanics</u> (For non-circuit branches)	3	1	0	4
5. b	EE2151	<u>Circuit Theory</u> (For branches under Electrical Faculty)	3	1	0	4
5. c	EC2151	<u>Electric Circuits and Electron Devices</u> (For branches under I & C Faculty)	3	1	0	4
6. a	GE2151	<u>Basic Electrical & Electronics Engineering</u> (For non-circuit branches)	4	0	0	4
6. b	GE2152	<u>Basic Civil & Mechanical Engineering</u> (For circuit branches)	4	0	0	4
PRACTICAL						
7.	GE2155	<u>Computer Practice Laboratory-II*</u>	0	1	2	2
8.	GS2165	<u>Physics & Chemistry Laboratory - II*</u>	0	0	3	2
9. a	ME2155	<u>Computer Aided Drafting and Modeling Laboratory</u> (For non-circuits branches)	0	1	2	2
9. b	EE2155	<u>Electrical Circuits Laboratory</u> (For branches under Electrical Faculty)	0	0	3	2
9. c	EC2155	<u>Circuits and Devices Laboratory</u> (For branches under I & C Faculty)	0	0	3	2
TOTAL : 28 CREDITS						
10.	-	<u>English Language Laboratory</u> ⁺	0	0	2	-

* Common to all B.E. / B.Tech. Programmes

+ Offering English Language Laboratory as an additional subject (with no marks) during 2nd semester may be decided by the respective Colleges affiliated to Anna University Chennai.

A. CIRCUIT BRANCHES

I Faculty of Electrical Engineering

1. B.E. Electrical and Electronics Engineering
2. B.E. Electronics and Instrumentation Engineering
3. B.E. Instrumentation and Control Engineering

II Faculty of Information and Communication Engineering

1. B.E. Computer Science and Engineering
2. B.E. Electronics and Communication Engineering
3. B.E. Bio Medical Engineering
4. B.Tech. Information Technology

B. NON – CIRCUIT BRANCHES

I Faculty of Civil Engineering

1. B.E. Civil Engineering

II Faculty of Mechanical Engineering

1. B.E. Aeronautical Engineering
2. B.E. Automobile Engineering
3. B.E. Marine Engineering
4. B.E. Mechanical Engineering
5. B.E. Production Engineering

III Faculty of Technology

1. B.Tech. Chemical Engineering
2. B.Tech. Biotechnology
3. B.Tech. Polymer Technology
4. B.Tech. Textile Technology
5. B.Tech. Textile Technology (Fashion Technology)
6. B.Tech. Petroleum Engineering
7. B.Tech. Plastics Technology

SEMESTER III

(Applicable to the students admitted from the Academic year 2008 – 2009 onwards)

THEORY			L	T	P	C
1.	MA 2211	<u>Transforms and Partial Differential Equations</u>	3	1	0	4
2.	GE 2211	<u>Environmental Science and Engineering</u>	3	0	0	3
3.	EI 2201	<u>Electrical Machines</u>	3	1	0	4
4.	EI 2203	<u>Electronic Devices and Circuits</u>	3	0	0	3
5.	EE 2204	<u>Data Structures and Algorithms</u>	3	1	0	4
6.	EI 2202	<u>Electrical Measurements</u>	3	1	0	4
PRACTICAL						
1.	EE 2207	<u>Electron Devices and Circuits Laboratory</u>	0	0	3	2
2.	EE 2209	<u>Data Structures and Algorithms Laboratory</u>	0	0	3	2
3.	EI 2208	<u>Electrical Machines Laboratory</u>	0	0	3	2
TOTAL			18	4	9	28

SEMESTER IV

(Applicable to the students admitted from the Academic year 2008 – 2009 onwards)

THEORY			L	T	P	C
1.	EE 2253	<u>Control Systems</u>	3	1	0	4
2.	EI 2251	<u>Industrial Instrumentation - I</u>	3	0	0	3
3.	EI 2252	<u>Transducer Engineering</u>	3	0	0	3
4.	EI 2253	<u>Digital logic Circuits</u>	3	1	0	4
5.	EE 2254	<u>Linear Integrated Circuits and Applications</u>	3	0	0	3
6.	EI 2254	<u>Applied Thermodynamics</u>	3	1	0	4
PRACTICAL						
1.	EI 2257	<u>Transducers and Measurements Laboratory</u>	0	0	3	2
2.	EI 2258	<u>Thermodynamics Laboratory</u>	0	0	3	2
3.	EE 2258	<u>Linear and Digital Integrated circuits Laboratory</u>	0	0	3	2
TOTAL			18	3	9	27

SEMESTER V

(Applicable to the students admitted from the Academic year 2008 – 2009 onwards)

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	EC2312	<u>Microprocessor and Microcontroller</u>	3	0	0	3
2.	EC2315	<u>Communication Engineering</u>	3	0	0	3
3.	CS2311	<u>Object Oriented Programming</u>	3	0	0	3
4.	EI2301	<u>Industrial Electronics</u>	3	0	0	3
5.	EI2302	<u>Analytical Instruments</u>	3	0	0	3
6.	EI2303	<u>Industrial Instrumentation – II</u>	3	0	0	3
PRACTICAL						
1.	EC2313	<u>Microprocessor and Microcontroller Lab</u>	0	0	3	2
2.	CS2312	<u>Object Oriented Programming Laboratory</u>	0	0	3	2
3.	GE2321	<u>Communication Skills Laboratory</u>	0	0	4	2
4.	EI2304	<u>Industrial Instrumentation Laboratory</u>	0	0	3	2
TOTAL			18	0	13	26

SEMESTER VI

(Applicable to the students admitted from the Academic year 2008 – 2009 onwards)

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	EI2351	<u>Modern Electronic Instrumentation</u>	3	0	0	3
2.	EI2352	<u>Process Control</u>	3	1	0	4
3.	EI2353	<u>Digital System Design</u>	3	0	0	3
4.	EC2361	<u>Digital Signal Processing</u>	3	1	0	4
5.	CS2364	<u>Embedded System</u>	3	0	0	3
6.	EI2311	<u>Biomedical Instrumentation</u>	3	0	0	3
PRACTICAL						
1.	EI2355	<u>Communication and DSP Laboratory</u>	0	0	3	2
2.	EI2356	<u>Process Control System Lab</u>	0	0	3	2
3.	EI2357	<u>Virtual Instrumentation Lab</u>	0	0	3	2
TOTAL			18	2	9	26

SEMESTER VII

(Applicable to the students admitted from the Academic year 2008 – 2009 onwards)

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	EI2401	<u>Industrial Data Networks</u>	3	0	0	3
2.	EI2402	<u>Logic and Distributed Control System</u>	3	0	0	3
3.	EI2403	<u>VLSI Design</u>	3	0	0	3
4.	EI2404	<u>Fibre Optics and Laser Instruments</u>	3	0	0	3
5.	E1	<u>Elective – I</u>	3	0	0	3
6.	E2	<u>Elective – II</u>	3	0	0	3
PRACTICAL						
1.	EI2405	<u>VLSI Lab</u>	0	0	3	2
2.	EI2406	<u>Instrumentation System Design Laboratory</u>	0	0	3	2
3.	EI2407	<u>Comprehension</u>	0	0	2	1
TOTAL			18	0	8	23

SEMESTER VIII

(Applicable to the students admitted from the Academic year 2008 – 2009 onwards)

SL. No.	COURSE CODE	COURSE TITLE	L	T	P	C
THEORY						
1.	MG2351	<u>Principles of Management</u>	3	0	0	3
2.	E3	<u>Elective – III</u>	3	0	0	3
3.	E4	<u>Elective – IV</u>	3	0	0	3
PRACTICAL						
1.	EI2451	<u>Project Work</u>	0	0	12	6
TOTAL			9	0	12	15

B.E ELECTRONICS AND INSTRUMENTATION ENGINEERING

LIST OF ELECTIVES - R 2008

ELECTIVE I

SL.NO	CODE NO.	COURSE TITLE	L	T	P	C
1.	CS2351	<u>Artificial Intelligence</u>	3	0	0	3
2.	CS2071	<u>Computer Architecture</u>	3	0	0	3
3.	CS2411	<u>Operating Systems</u>	3	0	0	3
4.	CS2070	<u>Visual Languages and Applications</u>	3	0	0	3

ELECTIVE II

5.	EI2021	<u>Power Plant Instrumentation</u>	3	0	0	3
6.	EI2022	<u>Instrumentation in Petrochemical Industries</u>	3	0	0	3
7.	EI2023	<u>Micro Electro Mechanical Systems</u>	3	0	0	3
8.	GE2023	<u>Fundamentals of NanoScience</u>	3	0	0	3

ELECTIVE III

9.	EC2055	<u>Digital Image Processing</u>	3	0	0	3
10.	EC2056	<u>Advanced Communication Engineering</u>	3	0	0	3
11.	EC2057	<u>Advanced Digital Signal Processing</u>	3	0	0	3
12.	EE2023	<u>Robotics and Automation</u>	3	0	0	3

ELECTIVE IV

13.	GE2022	<u>Total Quality Management</u>	3	0	0	3
14.	GE2025	<u>Professional Ethics In Engineering</u>	3	0	0	3
15.	IC2401	<u>Digital Control System</u>	3	0	0	3
16.	CS2461	<u>Applied Soft Computing</u>	3	0	0	3

AIM:

To encourage students to actively involve in participative learning of English and to help them acquire Communication Skills.

OBJECTIVES:

1. To help students develop listening skills for academic and professional purposes.
2. To help students acquire the ability to speak effectively in English in real-life situations.
3. To inculcate reading habit and to develop effective reading skills.
4. To help students improve their active and passive vocabulary.
5. To familiarize students with different rhetorical functions of scientific English.
6. To enable students write letters and reports effectively in formal and business situations.

UNIT I**12**

Technical Vocabulary - meanings in context, sequencing words, Articles- Prepositions, intensive reading& predicting content, Reading and interpretation, extended definitions, Process description

SUGGESTED ACTIVITIES:

1. Exercises on word formation using the prefix 'self' - Gap filling with preposition.
2. Exercises - Using sequence words.
3. Reading comprehension exercise with questions based on inference – Reading headings and predicting the content – Reading advertisements and interpretation.
5. Writing extended definitions – Writing descriptions of processes – Writing paragraphs based on discussions – Writing paragraphs describing the future.

UNIT II**12**

Phrases / Structures indicating use / purpose – Adverbs-Skimming – Non-verbal communication - Listening – correlating verbal and non-verbal communication -Speaking in group discussions – Formal Letter writing – Writing analytical paragraphs.

SUGGESTED ACTIVITIES:

Reading comprehension exercises with questions on overall content – Discussions analyzing stylistic features (creative and factual description) - Reading comprehension exercises with texts including graphic communication - Exercises in interpreting non-verbal communication.

1. Listening comprehension exercises to categorise data in tables.
2. Writing formal letters, quotations, clarification, complaint – Letter seeking permission for Industrial visits– Writing analytical paragraphs on different debatable issues.

UNIT III**12**

Cause and effect expressions – Different grammatical forms of the same word - Speaking – stress and intonation, Group Discussions - Reading – Critical reading - Listening, - Writing – using connectives, report writing – types, structure, data collection, content, form, recommendations .

SUGGESTED ACTIVITIES:

Exercises combining sentences using cause and effect expressions – Gap filling exercises using the appropriate tense forms – Making sentences using different grammatical forms of the same word. (Eg: object –verb / object – noun)

1. Speaking exercises involving the use of stress and intonation – Group discussions– analysis of problems and offering solutions.

2. Reading comprehension exercises with critical questions, Multiple choice question.
3. Sequencing of jumbled sentences using connectives – Writing different types of reports like industrial accident report and survey report – Writing recommendations.

UNIT IV

12

Numerical adjectives – Oral instructions – Descriptive writing – Argumentative paragraphs – Letter of application - content, format (CV / Bio-data) - Instructions, imperative forms - Checklists, Yes/No question form – E-mail communication.

SUGGESTED ACTIVITIES:

1. Rewriting exercises using numerical adjectives.
2. Reading comprehension exercises with analytical questions on content – Evaluation of content.
3. Listening comprehension – entering information in tabular form, intensive listening exercise and completing the steps of a process.
4. Speaking - Role play – group discussions – Activities giving oral instructions.
5. Writing descriptions, expanding hints – Writing argumentative paragraphs – Writing formal letters – Writing letter of application with CV/Bio-data – Writing general and safety instructions – Preparing checklists – Writing e-mail messages.

UNIT V

9

Speaking - Discussion of Problems and solutions - Creative and critical thinking – Writing an essay, Writing a proposal.

SUGGESTED ACTIVITIES:

1. Case Studies on problems and solutions
2. Brain storming and discussion
3. Writing Critical essays
4. Writing short proposals of 2 pages for starting a project, solving problems, etc.
5. Writing advertisements.

TOTAL : 60 PERIODS

TEXT BOOK:

1. Chapters 5 – 8. Department of Humanities & Social Sciences, Anna University, 'English for Engineers and Technologists' Combined Edition (Volumes 1 & 2), Chennai: Orient Longman Pvt. Ltd., 2006. Themes 5 – 8 (Technology, Communication, Environment, Industry)

REFERENCES:

1. P. K. Dutt, G. Rajeevan and C.L.N Prakash, 'A Course in Communication Skills', Cambridge University Press, India 2007.
2. Krishna Mohan and Meera Banerjee, 'Developing Communication Skills', Macmillan India Ltd., (Reprinted 1994 – 2007).
3. Edgar Thorpe, Showick Thorpe, 'Objective English', Second Edition, Pearson Education, 2007.

EXTENSIVE READING:

1. Robin Sharma, 'The Monk Who Sold His Ferrari', Jaico Publishing House, 2007

NOTE:

The book listed under Extensive Reading is meant for inculcating the reading habit of the students. They need not be used for testing purposes.

UNIT I ORDINARY DIFFERENTIAL EQUATIONS 12

Higher order linear differential equations with constant coefficients – Method of variation of parameters – Cauchy's and Legendre's linear equations – Simultaneous first order linear equations with constant coefficients.

UNIT II VECTOR CALCULUS 12

Gradient Divergence and Curl – Directional derivative – Irrotational and solenoidal vector fields – Vector integration – Green's theorem in a plane, Gauss divergence theorem and Stokes' theorem (excluding proofs) – Simple applications involving cubes and rectangular parallelepipeds.

UNIT III ANALYTIC FUNCTIONS 12

Functions of a complex variable – Analytic functions – Necessary conditions, Cauchy – Riemann equation and Sufficient conditions (excluding proofs) – Harmonic and orthogonal properties of analytic function – Harmonic conjugate – Construction of analytic functions – Conformal mapping : $w = z+c$, cz , $1/z$, and bilinear transformation.

UNIT IV COMPLEX INTEGRATION 12

Complex integration – Statement and applications of Cauchy's integral theorem and Cauchy's integral formula – Taylor and Laurent expansions – Singular points – Residues – Residue theorem – Application of residue theorem to evaluate real integrals – Unit circle and semi-circular contour(excluding poles on boundaries).

UNIT V LAPLACE TRANSFORM 12

Laplace transform – Conditions for existence – Transform of elementary functions – Basic properties – Transform of derivatives and integrals – Transform of unit step function and impulse functions – Transform of periodic functions.

Definition of Inverse Laplace transform as contour integral – Convolution theorem (excluding proof) – Initial and Final value theorems – Solution of linear ODE of second order with constant coefficients using Laplace transformation techniques.

TOTAL : 60 PERIODS**TEXT BOOKS:**

1. Bali N. P and Manish Goyal, "Text book of Engineering Mathematics", 3rd Edition, Laxmi Publications (p) Ltd., (2008).
2. Grewal. B.S, "Higher Engineering Mathematics", 40th Edition, Khanna Publications, Delhi, (2007).

REFERENCES:

1. Ramana B.V, "Higher Engineering Mathematics", Tata McGraw Hill Publishing Company, New Delhi, (2007).
2. Glyn James, "Advanced Engineering Mathematics", 3rd Edition, Pearson Education, (2007).
3. Erwin Kreyszig, "Advanced Engineering Mathematics", 7th Edition, Wiley India, (2007).
4. Jain R.K and Iyengar S.R.K, "Advanced Engineering Mathematics", 3rd Edition, Narosa Publishing House Pvt. Ltd., (2007).

UNIT I CONDUCTING MATERIALS**9**

Conductors – classical free electron theory of metals – Electrical and thermal conductivity – Wiedemann – Franz law – Lorentz number – Draw backs of classical theory – Quantum theory – Fermi distribution function – Effect of temperature on Fermi Function – Density of energy states – carrier concentration in metals.

UNIT II SEMICONDUCTING MATERIALS**9**

Intrinsic semiconductor – carrier concentration derivation – Fermi level – Variation of Fermi level with temperature – electrical conductivity – band gap determination – extrinsic semiconductors – carrier concentration derivation in n-type and p-type semiconductor – variation of Fermi level with temperature and impurity concentration – compound semiconductors – Hall effect – Determination of Hall coefficient – Applications.

UNIT III MAGNETIC AND SUPERCONDUCTING MATERIALS**9**

Origin of magnetic moment – Bohr magneton – Dia and para magnetism – Ferro magnetism – Domain theory – Hysteresis – soft and hard magnetic materials – anti – ferromagnetic materials – Ferrites – applications – magnetic recording and readout – storage of magnetic data – tapes, floppy and magnetic disc drives.

Superconductivity : properties - Types of super conductors – BCS theory of superconductivity(Qualitative) - High T_c superconductors – Applications of superconductors – SQUID, cryotron, magnetic levitation.

UNIT IV DIELECTRIC MATERIALS**9**

Electrical susceptibility – dielectric constant – electronic, ionic, orientational and space charge polarization – frequency and temperature dependence of polarisation – internal field – Claussius – Mosotti relation (derivation) – dielectric loss – dielectric breakdown – uses of dielectric materials (capacitor and transformer) – ferroelectricity and applications.

UNIT V MODERN ENGINEERING MATERIALS**9**

Metallic glasses: preparation, properties and applications.

Shape memory alloys (SMA): Characteristics, properties of NiTi alloy, application, advantages and disadvantages of SMA

Nanomaterials: synthesis –plasma arcing – chemical vapour deposition – sol-gels – electrodeposition – ball milling - properties of nanoparticles and applications.

Carbon nanotubes: fabrication – arc method – pulsed laser deposition – chemical vapour deposition - structure – properties and applications.

TOTAL : 45 PERIODS**TEXT BOOKS:**

1. Charles Kittel 'Introduction to Solid State Physics', John Wiley & sons, 7th edition, Singapore (2007)
2. Charles P. Poole and Frank J.Ownen, 'Introduction to Nanotechnology', Wiley India(2007) (for Unit V)

REFERENCES:

1. Rajendran, V, and Marikani A, 'Materials science' Tata McGraw Hill publications, (2004) New delhi.
2. Jayakumar, S. 'Materials science', R.K. Publishers, Coimbatore, (2008).
3. Palanisamy P.K, 'Materials science', Scitech publications(India) Pvt. LTd., Chennai, second Edition(2007)
4. M. Arumugam, 'Materials Science' Anuradha publications, Kumbakonam, (2006).

CY2161

ENGINEERING CHEMISTRY – II

**L T P C
3 0 0 3**

AIM

To impart a sound knowledge on the principles of chemistry involving the different application oriented topics required for all engineering branches.

OBJECTIVES

- The student should be conversant with the principles electrochemistry, electrochemical cells, emf and applications of emf measurements.
- Principles of corrosion control
- Chemistry of Fuels and combustion
- Industrial importance of Phase rule and alloys
- Analytical techniques and their importance.

UNIT I ELECTROCHEMISTRY

9

Electrochemical cells – reversible and irreversible cells – EMF – measurement of emf – Single electrode potential – Nernst equation (problem) – reference electrodes – Standard Hydrogen electrode – Calomel electrode – Ion selective electrode – glass electrode and measurement of pH – electrochemical series – significance – potentiometer titrations (redox - Fe^{2+} vs dichromate and precipitation – Ag^+ vs Cl^- titrations) and conduct metric titrations (acid-base – HCl vs, NaOH) titrations,

UNIT II CORROSION AND CORROSION CONTROL

9

Chemical corrosion – Pilling – Bedworth rule – electrochemical corrosion – different types – galvanic corrosion – differential aeration corrosion – factors influencing corrosion – corrosion control – sacrificial anode and impressed cathodic current methods – corrosion inhibitors – protective coatings – paints – constituents and functions – metallic coatings – electroplating (Au) and electroless (Ni) plating.

UNIT III FUELS AND COMBUSTION

9

Calorific value – classification – Coal – proximate and ultimate analysis metallurgical coke – manufacture by Otto-Hoffmann method – Petroleum processing and fractions – cracking – catalytic cracking and methods-knocking – octane number and cetane number – synthetic petrol – Fischer Tropsch and Bergius processes – Gaseous fuels- water gas, producer gas, CNG and LPG, Flue gas analysis – Orsat apparatus – theoretical air for combustion.

UNIT IV PHASE RULE AND ALLOYS**9**

Statement and explanation of terms involved – one component system – water system – condensed phase rule – construction of phase diagram by thermal analysis – simple eutectic systems (lead-silver system only) – alloys – importance, ferrous alloys – nichrome and stainless steel – heat treatment of steel, non-ferrous alloys – brass and bronze.

UNIT V ANALYTICAL TECHNIQUES**9**

Beer-Lambert's law (problem) – UV-visible spectroscopy and IR spectroscopy – principles – instrumentation (problem) (block diagram only) – estimation of iron by colorimetry – flame photometry – principle – instrumentation (block diagram only) – estimation of sodium by flame photometry – atomic absorption spectroscopy – principles – instrumentation (block diagram only) – estimation of nickel by atomic absorption spectroscopy.

TOTAL: 45 PERIODS**TEXT BOOKS:**

1. P.C.Jain and Monica Jain, "Engineering Chemistry" Dhanpat Rai Pub, Co., New Delhi (2002).
2. S.S.Dara "A text book of Engineering Chemistry" S.Chand & Co.Ltd., New Delhi (2006).

REFERENCES:

1. B.Sivasankar "Engineering Chemistry" Tata McGraw-Hill Pub.Co.Ltd, New Delhi (2008).
2. B.K.Sharma "Engineering Chemistry" Krishna Prakasan Media (P) Ltd., Meerut (2001).

ME2151**ENGINEERING MECHANICS****L T P C
3 1 0 4****OBJECTIVE**

At the end of this course the student should be able to understand the vectorial and scalar representation of forces and moments, static equilibrium of particles and rigid bodies both in two dimensions and also in three dimensions. Further, he should understand the principle of work and energy. He should be able to comprehend the effect of friction on equilibrium. He should be able to understand the laws of motion, the kinematics of motion and the interrelationship. He should also be able to write the dynamic equilibrium equation. All these should be achieved both conceptually and through solved examples.

UNIT I BASICS & STATICS OF PARTICLES**12**

Introduction – Units and Dimensions – Laws of Mechanics – Lame's theorem, Parallelogram and triangular Law of forces – Vectors – Vectorial representation of forces and moments – Vector operations: additions, subtraction, dot product, cross product – Coplanar Forces – Resolution and Composition of forces – Equilibrium of a particle – Forces in space – Equilibrium of a particle in space – Equivalent systems of forces – Principle of transmissibility – Single equivalent force.

UNIT II	EQUILIBRIUM OF RIGID BODIES	12
Free body diagram – Types of supports and their reactions – requirements of stable equilibrium – Moments and Couples – Moment of a force about a point and about an axis – Vectorial representation of moments and couples – Scalar components of a moment – Varignon’s theorem – Equilibrium of Rigid bodies in two dimensions – Equilibrium of Rigid bodies in three dimensions – Examples		
UNIT III	PROPERTIES OF SURFACES AND SOLIDS	12
Determination of Areas and Volumes – First moment of area and the Centroid of sections – Rectangle, circle, triangle from integration – T section, I section, - Angle section, Hollow section by using standard formula – second and product moments of plane area – Rectangle, triangle, circle from integration – T section, I section, Angle section, Hollow section by using standard formula – Parallel axis theorem and perpendicular axis theorem – Polar moment of inertia – Principal moments of inertia of plane areas – Principal axes of inertia – Mass moment of inertia – Derivation of mass moment of inertia for rectangular section, prism, sphere from first principle – Relation to area moments of inertia.		
UNIT IV	DYNAMICS OF PARTICLES	12
Displacements, Velocity and acceleration, their relationship – Relative motion – Curvilinear motion – Newton’s law – Work Energy Equation of particles – Impulse and Momentum – Impact of elastic bodies.		
UNIT V	FRICITION AND ELEMENTS OF RIGID BODY DYNAMICS	12
Frictional force – Laws of Coloumb friction – simple contact friction – Rolling resistance – Belt friction. Translation and Rotation of Rigid Bodies – Velocity and acceleration – General Plane motion.		

TOTAL: 60 PERIODS

TEXT BOOK:

1. Beer, F.P and Johnson Jr. E.R. “Vector Mechanics for Engineers”, Vol. 1 Statics and Vol. 2 Dynamics, McGraw-Hill International Edition, (1997).

REFERENCES:

1. Rajasekaran, S, Sankarasubramanian, G., “Fundamentals of Engineering Mechanics”, Vikas Publishing House Pvt. Ltd., (2000).
2. Hibbeler, R.C., “Engineering Mechanics”, Vol. 1 Statics, Vol. 2 Dynamics, Pearson Education Asia Pvt. Ltd., (2000).
3. Palanichamy, M.S., Nagam, S., “Engineering Mechanics – Statics & Dynamics”, Tata McGraw-Hill, (2001).
4. Irving H. Shames, “Engineering Mechanics – Statics and Dynamics”, IV Edition – Pearson Education Asia Pvt. Ltd., (2003).
5. Ashok Gupta, “Interactive Engineering Mechanics – Statics – A Virtual Tutor (CDROM)”, Pearson Education Asia Pvt., Ltd., (2002).

UNIT I	BASIC CIRCUITS ANALYSIS	12
	Ohm's Law – Kirchoffs laws – DC and AC Circuits – Resistors in series and parallel circuits – Mesh current and node voltage method of analysis for D.C and A.C. circuits.	
UNIT II	NETWORK REDUCTION AND NETWORK THEOREMS FOR DC AND AC CIRCUITS:	12
	Network reduction: voltage and current division, source transformation – star delta conversion. Thevenins and Novton & Theorem – Superposition Theorem – Maximum power transfer theorem – Reciprocity Theorem.	
UNIT III	RESONANCE AND COUPLED CIRCUITS	12
	Series and paralled resonance – their frequency response – Quality factor and Bandwidth - Self and mutual inductance – Coefficient of coupling – Tuned circuits – Single tuned circuits.	
UNIT IV	TRANSIENT RESPONSE FOR DC CIRCUITS	12
	Transient response of RL, RC and RLC Circuits using Laplace transform for DC input and A.C. with sinusoidal input.	
UNIT V	ANALYSING THREE PHASE CIRCUITS	12
	Three phase balanced / unbalanced voltage sources – analysis of three phase 3-wire and 4-wire circuits with star and delta connected loads, balanced & un balanced – phasor diagram of voltages and currents – power and power factor measurements in three phase circuits.	

TOTAL :60 PERIODS

TEXT BOOKS:

1. William H. Hayt Jr, Jack E. Kemmerly and Steven M. Durbin, "Engineering Circuits Analysis", Tata McGraw Hill publishers, 6th edition, New Delhi, (2002).
2. Sudhakar A and Shyam Mohan SP, "Circuits and Network Analysis and Synthesis", Tata McGraw Hill, (2007).

REFERENCES:

1. Paranjothi SR, "Electric Circuits Analysis," New Age International Ltd., New Delhi, (1996).
2. Joseph A. Edminister, Mahmood Nahri, "Electric circuits", Schaum's series, Tata McGraw-Hill, New Delhi (2001).
3. Chakrabati A, "Circuits Theory (Analysis and synthesis), Dhanpath Rai & Sons, New Delhi, (1999).
4. Charles K. Alexander, Mathew N.O. Sadik, "Fundamentals of Electric Circuits", Second Edition, McGraw Hill, (2003).

UNIT I CIRCUIT ANALYSIS TECHNIQUES**12**

Kirchoff's current and voltage laws – series and parallel connection of independent sources – R, L and C – Network Theorems – Thevenin, Superposition, Norton, Maximum power transfer and duality – Star-delta conversion.

UNIT II TRANSIENT RESONANCE IN RLC CIRCUITS**12**

Basic RL, RC and RLC circuits and their responses to pulse and sinusoidal inputs – frequency response – Parallel and series resonances – Q factor – single tuned and double tuned circuits.

UNIT III SEMICONDUCTOR DIODES**12**

Review of intrinsic & extrinsic semiconductors – Theory of PN junction diode – Energy band structure – current equation – space charge and diffusion capacitances – effect of temperature and breakdown mechanism – Zener diode and its characteristics.

UNIT IV TRANSISTORS**12**

Principle of operation of PNP and NPN transistors – study of CE, CB and CC configurations and comparison of their characteristics – Breakdown in transistors – operation and comparison of N-Channel and P-Channel JFET – drain current equation – MOSFET – Enhancement and depletion types – structure and operation – comparison of BJT with MOSFET – thermal effect on MOSFET.

**UNIT V SPECIAL SEMICONDUCTOR DEVICES
(Qualitative Treatment only)****12**

Tunnel diodes – PIN diode, varactor diode – SCR characteristics and two transistor equivalent model – UJT – Diac and Triac – Laser, CCD, Photodiode, Phototransistor, Photoconductive and Photovoltaic cells – LED, LCD.

TOTAL : 60 PERIODS**TEXT BOOKS:**

1. Joseph A. Edminister, Mahmood, Nahri, "Electric Circuits" – Shaum series, Tata McGraw Hill, (2001)
2. S. Salivahanan, N. Suresh kumar and A. Vallavanraj, "Electronic Devices and Circuits", Tata McGraw Hill, 2nd Edition, (2008).
3. David A. Bell, "Electronic Devices and Circuits", Oxford University Press, 5th Edition, (2008).

REFERENCES:

1. Robert T. Paynter, "Introducing Electronics Devices and Circuits", Pearson Education, 7th Edition, (2006).
2. William H. Hayt, J.V. Jack, E. Kemmeby and Steven M. Durbin, "Engineering Circuit Analysis", Tata McGraw Hill, 6th Edition, 2002.
3. J. Millman & Halkins, Satyabranta Jit, "Electronic Devices & Circuits", Tata McGraw Hill, 2nd Edition, 2008.

GE2151 **BASIC ELECTRICAL AND ELECTRONICS ENGINEERING** **L T P C**
(Common to branches under Civil, Mechanical and Technology faculty) **3 0 0 3**

UNIT I **ELECTRICAL CIRCUITS & MEASUREMENTS** **12**

Ohm's Law – Kirchoff's Laws – Steady State Solution of DC Circuits – Introduction to AC Circuits – Waveforms and RMS Value – Power and Power factor – Single Phase and Three Phase Balanced Circuits.

Operating Principles of Moving Coil and Moving Iron Instruments (Ammeters and Voltmeters), Dynamometer type Watt meters and Energy meters.

UNIT II **ELECTRICAL MECHANICS** **12**

Construction, Principle of Operation, Basic Equations and Applications of DC Generators, DC Motors, Single Phase Transformer, single phase induction Motor.

UNIT III **SEMICONDUCTOR DEVICES AND APPLICATIONS** **12**

Characteristics of PN Junction Diode – Zener Effect – Zener Diode and its Characteristics – Half wave and Full wave Rectifiers – Voltage Regulation.

Bipolar Junction Transistor – CB, CE, CC Configurations and Characteristics – Elementary Treatment of Small Signal Amplifier.

UNIT IV **DIGITAL ELECTRONICS** **12**

Binary Number System – Logic Gates – Boolean Algebra – Half and Full Adders – Flip-Flops – Registers and Counters – A/D and D/A Conversion (single concepts)

UNIT V **FUNDAMENTALS OF COMMUNICATION ENGINEERING** **12**

Types of Signals: Analog and Digital Signals – Modulation and Demodulation: Principles of Amplitude and Frequency Modulations.

Communication Systems: Radio, TV, Fax, Microwave, Satellite and Optical Fibre (Block Diagram Approach only).

TOTAL : 60 PERIODS

TEXT BOOKS:

1. V.N. Mittle "Basic Electrical Engineering", Tata McGraw Hill Edition, New Delhi, 1990.
2. R.S. Sedha, "Applied Electronics" S. Chand & Co., 2006.

REFERENCES:

1. Muthusubramanian R, Salivahanan S and Muraleedharan K A, "Basic Electrical, Electronics and Computer Engineering", Tata McGraw Hill, Second Edition, (2006).
2. Nagsarkar T K and Sukhija M S, "Basics of Electrical Engineering", Oxford press (2005).
3. Mehta V K, "Principles of Electronics", S.Chand & Company Ltd, (1994).
4. Mahmood Nahvi and Joseph A. Edminister, "Electric Circuits", Schaum' Outline Series, McGraw Hill, (2002).
5. Premkumar N, "Basic Electrical Engineering", Anuradha Publishers, (2003).

GE2155

COMPUTER PRACTICE LABORATORY – II

**L T P C
0 1 2 2**

LIST OF EXPERIMENTS

- | | |
|---|-----------|
| 1. UNIX COMMANDS | 15 |
| Study of Unix OS - Basic Shell Commands - Unix Editor | |
| 2. SHELL PROGRAMMING | 15 |
| Simple Shell program - Conditional Statements - Testing and Loops | |
| 3. C PROGRAMMING ON UNIX | 15 |
| Dynamic Storage Allocation-Pointers-Functions-File Handling | |

TOTAL : 45 PERIODS

HARDWARE / SOFTWARE REQUIREMENTS FOR A BATCH OF 30 STUDENTS

HARDWARE:

- 1 UNIX Clone Server
- 33 Nodes (thin client or PCs)
- Printer – 3 Nos.

SOFTWARE :

- OS – UNIX Clone (33 user license or License free Linux)
- Compiler - C

GS2165

PHYSICS LABORATORY – II

**L T P C
0 0 3 2**

LIST OF EXPERIMENTS

1. Determination of Young's modulus of the material – non uniform bending.
2. Determination of Band Gap of a semiconductor material.
3. Determination of specific resistance of a given coil of wire – Carey Foster Bridge.
4. Determination of viscosity of liquid – Poiseuille's method.
5. Spectrometer dispersive power of a prism.
6. Determination of Young's modulus of the material – uniform bending.
7. Torsional pendulum – Determination of rigidity modulus.

- **A minimum of FIVE experiments shall be offered.**
- **Laboratory classes on alternate weeks for Physics and Chemistry.**
- **The lab examinations will be held only in the second semester.**

LIST OF EXPERIMENTS

1. Conduct metric titration (Simple acid base)
2. Conduct metric titration (Mixture of weak and strong acids)
3. Conduct metric titration using BaCl_2 vs Na_2SO_4
4. Potentiometric Titration (Fe^{2+} / KMnO_4 or $\text{K}_2\text{Cr}_2\text{O}_7$)
5. PH titration (acid & base)
6. Determination of water of crystallization of a crystalline salt (Copper sulphate)
7. Estimation of Ferric iron by spectrophotometry.

- A minimum of FIVE experiments shall be offered.
- Laboratory classes on alternate weeks for Physics and Chemistry.
- The lab examinations will be held only in the second semester.

List of Exercises using software capable of Drafting and Modeling

1. Study of capabilities of software for Drafting and Modeling – Coordinate systems (absolute, relative, polar, etc.) – Creation of simple figures like polygon and general multi-line figures.
2. Drawing of a Title Block with necessary text and projection symbol.
3. Drawing of curves like parabola, spiral, involute using Bspline or cubic spline.
4. Drawing of front view and top view of simple solids like prism, pyramid, cylinder, cone, etc, and dimensioning.
5. Drawing front view, top view and side view of objects from the given pictorial views (eg. V-block, Base of a mixie, Simple stool, Objects with hole and curves).
6. Drawing of a plan of residential building (Two bed rooms, kitchen, hall, etc.)
7. Drawing of a simple steel truss.
8. Drawing sectional views of prism, pyramid, cylinder, cone, etc,
9. Drawing isometric projection of simple objects.
10. Creation of 3-D models of simple objects and obtaining 2-D multi-view drawings from 3-D model.

Note: Plotting of drawings must be made for each exercise and attached to the records written by students.

List of Equipments for a batch of 30 students:

1. Pentium IV computer or better hardware, with suitable graphics facility -30 No.
2. Licensed software for Drafting and Modeling. – 30 Licenses
3. Laser Printer or Plotter to print / plot drawings – 2 No.

EE2155

ELECTRICAL CIRCUIT LABORATORY
(Common to EEE, EIE and ICE)

L T P C
0 0 3 2

LIST OF EXPERIMENTS

1. Verification of ohm's laws and kirchoff's laws.
2. Verification of Thevemin's and Norton's Theorem
3. Verification of superposition Theorem
4. Verification of maximum power transfer theorem.
5. Verification of reciprocity theorem
6. Measurement of self inductance of a coil
7. Verification of mesh and nodal analysis.
8. Transient response of RL and RC circuits for DC input.
9. Frequency response of series and parallel resonance circuits.
10. Frequency response of single tuned coupled circuits.

TOTAL: 45 PERIODS

EC2155

CIRCUITS AND DEVICES LABORATORY

L T P C
0 0 3 2

1. Verification of KVL and KCL
2. Verification of Thevenin and Norton Theorems.
3. Verification of superposition Theorem.
4. Verification of Maximum power transfer and reciprocity theorems.
5. Frequency response of series and parallel resonance circuits.
6. Characteristics of PN and Zener diode
7. Characteristics of CE configuration
8. Characteristics of CB configuration
9. Characteristics of UJT and SCR
10. Characteristics of JFET and MOSFET
11. Characteristics of Diac and Triac.
12. Characteristics of Photodiode and Phototransistor.

TOTAL : 45 PERIODS

10. ENGLISH LANGUAGE LABORATORY (Optional) L T P C
0 0 2 -
5

1. LISTENING:

Listening & answering questions – gap filling – Listening and Note taking- Listening to telephone conversations

2. SPEAKING:

Pronouncing words & sentences correctly – word stress – Conversation practice. **5**

CLASSROOM SESSION 20

1. Speaking: Introducing oneself, Introducing others, Role play, Debate - Presentations: Body language, gestures, postures.
Group Discussions etc
2. Goal setting – interviews – stress time management – situational reasons

EVALUATION

(1) Lab Session – 40 marks

- Listening – 10 marks
- Speaking – 10 marks
- Reading – 10 marks
- Writing – 10 marks

(2) Classroom Session – 60 marks

- Role play activities giving real life context – 30 marks
- Presentation – 30 marks

NOTE ON EVALUATION

1. Examples for role play situations:
 - a. Marketing engineer convincing a customer to buy his product.
 - b. Telephone conversation – Fixing an official appointment / Enquiry on availability of flight or train tickets / placing an order. etc.
2. Presentations could be just a Minute (JAM activity) or an Extempore on simple topics or visuals could be provided and students could be asked to talk about it.

REFERENCES:

1. Hartley, Peter, Group Communication, London: Routledge, (2004).
2. Doff, Adrian and Christopher Jones, Language in Use – (Intermediate level), Cambridge University Press, (1994).
3. Gammidge, Mick, Speaking Extra – A resource book of multi-level skills activities , Cambridge University Press, (2004).
4. Craven, Miles, Listening Extra - A resource book of multi-level skills activities, Cambridge, Cambridge University Press, (2004).
5. Naterop, Jean & Rod Revell, Telephoning in English, Cambridge University Press, (1987).

LAB REQUIREMENTS

1. Teacher – Console and systems for students
2. English Language Lab Software
3. Tape Recorders.

OBJECTIVES

- To create an awareness on the various environmental pollution aspects and issues.
- To give a comprehensive insight into natural resources, ecosystem and biodiversity.
- To educate the ways and means to protect the environment from various types of pollution.
- To impart some fundamental knowledge on human welfare measures.

UNIT I INTRODUCTION TO ENVIRONMENTAL STUDIES AND NATURAL RESOURCES**10**

Definition, scope and importance – need for public awareness – forest resources: use and over-exploitation, deforestation, case studies. Timber extraction, mining, dams and their ground water, floods, drought, conflicts over water, dams-benefits and problems – mineral resources: use effects on forests and tribal people – water resources: use and over-utilization of surface and exploitation, environmental effects of extracting and using mineral resources, case studies – food resources: world food problems, changes caused by agriculture and overgrazing, effects of modern agriculture, fertilizer-pesticide problems, water logging, salinity, case studies – energy resources: growing energy needs, renewable and non renewable energy sources, use of alternate energy sources. Case studies – land resources: land as a resource, land degradation, man induced landslides, soil erosion and desertification – role of an individual in conservation of natural resources – equitable use of resources for sustainable lifestyles.

Field study of local area to document environmental assets – river / forest / grassland / hill / mountain.

UNIT II ECOSYSTEMS AND BIODIVERSITY**14**

Concept of an ecosystem – structure and function of an ecosystem – producers, consumers and decomposers – energy flow in the ecosystem – ecological succession – food chains, food webs and ecological pyramids – introduction, types, characteristic features, structure and function of the (a) forest ecosystem (b) grassland ecosystem (c) desert ecosystem (d) aquatic ecosystems (ponds, streams, lakes, rivers, oceans, estuaries) – introduction to biodiversity – definition: genetic, species and ecosystem diversity – biogeographical classification of India – value of biodiversity: consumptive use, productive use, social, ethical, aesthetic and option values – biodiversity at global, national and local levels – India as a mega-diversity nation – hot-spots of biodiversity – threats to biodiversity: habitat loss, poaching of wildlife, man-wildlife conflicts – endangered and endemic species of India – conservation of biodiversity: in-situ and ex-situ conservation of biodiversity.

Field study of common plants, insects, birds

Field study of simple ecosystems – pond, river, hill slopes, etc.

UNIT III ENVIRONMENTAL POLLUTION**8**

Definition – causes, effects and control measures of: (a) air pollution (b) water pollution (c) soil pollution (d) marine pollution (e) noise pollution (f) thermal pollution (g) nuclear hazards – solid waste management: causes, effects and control measures of urban and industrial wastes – role of an individual in prevention of pollution – pollution case studies – disaster management: floods, earthquake, cyclone and landslides.

Field study of local polluted site – urban / rural / industrial / agricultural

UNIT IV SOCIAL ISSUES AND THE ENVIRONMENT 7
 From unsustainable to sustainable development – urban problems related to energy – water conservation, rain water harvesting, watershed management – resettlement and rehabilitation of people; its problems and concerns, case studies – environmental ethics: issues and possible solutions – climate change, global warming, acid rain, ozone layer depletion, nuclear accidents and holocaust, case studies. – wasteland reclamation – consumerism and waste products – environment protection act – air (prevention and control of pollution) act – water (prevention and control of pollution) act – wildlife protection act – forest conservation act – issues involved in enforcement of environmental legislation – public awareness.

UNIT V HUMAN POPULATION AND THE ENVIRONMENT 6
 Population growth, variation among nations – population explosion – family welfare programme – environment and human health – human rights – value education – hiv / aids – women and child welfare – role of information technology in environment and human health – case studies.

TOTAL : 45 PERIODS

TEXT BOOKS:

1. Gilbert M.Masters, "Introduction to Environmental Engineering and Science", Pearson Education Pvt., Ltd., Second Edition, ISBN 81-297-0277-0, 2004.
2. Miller T.G. Jr., "Environmental Science", Wadsworth Publishing Co.

REFERENCES:

1. Bharucha Erach, "The Biodiversity of India", Mapin Publishing Pvt. Ltd., Ahmedabad India.
2. Trivedi R.K., "Handbook of Environmental Laws, Rules, Guidelines, Compliances and Standards", Vol. I and II, Enviro Media.
3. Cunningham, W.P.Cooper, T.H.Gorhani, "Environmental Encyclopedia", Jaico Publ., House, Mumbai, 2001.
4. Wager K.D. "Environmental Management", W.B. Saunders Co., Philadelphia, USA, 1998.
5. Townsend C., Harper J and Michael Begon, "Essentials of Ecology", Blackwell Science.
6. Trivedi R.K. and P.K. Goel, "Introduction to Air Pollution", Techno-Science Publications.

EI2201 ELECTRICAL MACHINES L T P C
(Common to EIE & ICE) 3 1 0 4

AIM

To impart basic knowledge on Electrical machines, principles and its behavior.

OBJECTIVES

At the end of this course, student would have been exposed to:

- Theory of structures, operating principle, characteristics, and applications of D.C and A.C rotating machines and transformers in detail.
- Introductory knowledge on Special Machines.

UNIT I D.C. MACHINES 12
 Construction of D.C. Machines - Principle and theory of operation of D.C. generator - EMF equation - Characteristics of D.C. generators - Armature reaction – Commutation - Principle of operation of

- UNIT I SEMICONDUCTOR DIODE AND BJT 9**
 PN Junction – Current components in a PN diode – Junction capacitance – Junction diode switching time – Zener diode – Varactor diode – Tunnel diode – Schottky diode – Transistor Structure – Basic Transistor operation – Transistor characteristics and parameters – The transistor as a switch, as an amplifier – Transistor bias circuits:- Voltage divider bias circuits, base bias circuits, emitter bias circuits, collector feedback bias circuits – DC load line – AC load line- bias stabilization, thermal runaway and thermal stability.
- UNIT II FET, UJT and SCR 9**
 JFET characteristics and parameters – JFET biasing, self bias, voltage divider bias – Q point, stability over temperature – MOSFET D-MOSFET, E-MOSFET – MOSFET characteristics and parameters – MOSFET biasing, zero bias, voltage divider bias method, drain feedback bias – Characteristics and applications of UJT, SCR, DIAC, TRIAC.
- UNIT III AMPLIFIERS 9**
 CE, CC and CB amplifiers - Small signal low frequency transistor amplifier circuits - h parameter representation of a transistor - Analysis of single stage transistor amplifier using parameters voltage gain, current gain, input impedance and output impedance-frequency response - RC coupled amplifier.
 Classification of Power amplifiers:- Class A, B, AB and C Power amplifiers-Push-Pull and Complementary Symmetry Push-Pull amplifiers - Design of power output, efficiency and cross-over distortion.
- UNIT IV FEEDBACK AMPLIFIERS AND OSCILLATORS 9**
 Advantages of negative feedback - Voltage/current, series/shunt feedback-Positive feedback - Condition for oscillators - Phase shift - Wein Bridge – Hartley - Colpitts and crystal oscillators.
- UNIT V PULSE CIRCUITS AND POWER SUPPLIES 9**
 RC wave shaping circuits - Diode clampers and clippers – Multivibrators -Schmitt triggers - UJT - Saw tooth oscillators - Single and polyphase rectifiers and analysis of filter circuits - Design of zener and transistor series voltage regulators - Switched mode power supplies.

L = 45 TOTAL : 45 PERIODS

TEXT BOOKS:

1. Millman and Halkias, "Electronic Devices and Circuits", Tata McGraw– Hill, 2007.
2. Floyd, T.L, "Electronic Devices" 6th Edition, Pearson Education, 2003.
3. Millman and Halkias, "Integrated Electronics", McGraw-Hill, 2004.

REFERENCES:

1. Mottershead, A., "Electronic Devices and Circuits an Introduction", Prentice Hall of India, 2003.
2. Boylsted and Nashelsky, "Electronic Devices and Circuit Theory", Prentice Hall of India, 6th Edition, 1999.
3. Streetman, B. and Sanjay, B., "Solid State Electronic Devices", Prentice- Hall of India, 5th Edition, 2005.
4. Bell, D.A., "Electronic Devices and Circuits", Prentice Hall of India, 4th Edition, 1999.
5. Millman, J., Prakash Rao., M.S. and Taub, H., "Pulse Digital and Switching Wave Forms", McGraw-Hill, 2007.

AIM

To provide adequate knowledge in electrical measurements and instrumentation.

OBJECTIVES

To make the students to gain a clear knowledge of the basic laws governing the operation of electrical instruments and the measurement techniques.

- i. Emphasis is laid on the meters used to measure current & voltage.
- ii. To have an adequate knowledge in the measurement techniques for power and energy, power and energy meters are included.
- iii. Elaborate discussion about potentiometer & instrument transformers.
- iv. Detailed study of resistance measuring methods.
- v. Detailed study of inductance and capacitance measurement.

UNIT I MEASUREMENT OF VOLTAGE AND CURRENT**9**

Galvanometers – Ballistic, D'Arsonval galvanometer – Theory, calibration, application – Principle, construction, operation and comparison of moving coil, moving iron meters, dynamometer, induction type & thermal type meter, rectifier type – Extension of range and calibration of voltmeter and ammeter – Errors and compensation.

UNIT II MEASUREMENT OF POWER AND ENERGY**9**

Electrodynamometer type wattmeter – Theory & its errors – Methods of correction – LPF wattmeter – Phantom loading – Induction type kWh meter – Induction type energy meter – Calibration of wattmeter.

UNIT III POTENTIOMETERS & INSTRUMENT TRANSFORMERS**9**

DC potentiometer – Basic circuit, standardization – Laboratory type (Crompton's) – AC potentiometer – Drysdale (polar type) type – Gall-Tinsley (coordinate) type – Limitations & applications – C.T and P.T construction, theory, operation and characteristics.

UNIT IV RESISTANCE MEASUREMENT**9**

Measurement of low, medium & high resistance – Ammeter, voltmeter method – Wheatstone bridge – Kelvin double bridge – Series and shunt type ohmmeter – High resistance measurement – Megger – Direct deflection methods – Price's guard-wire method – Earth resistance measurement.

UNIT V IMPEDANCE MEASUREMENT**9**

A.C bridges – Measurement of inductance, capacitance – Q of coil – Maxwell Bridge – Wein's bridge – Schering bridge – Anderson bridge – Campbell bridge to measure mutual inductance – Errors in A.C. bridge methods and their compensation – Detectors – Excited field – A.C. galvanometer – Vibration galvanometer

L = 45 T = 15 Total = 60**TEXT BOOKS**

1. E.W. Golding & F.C.Widdis, 'Electrical Measurements & Measuring Instruments', A.H.Wheeler & Co, 2001
2. A.K. Sawhney, 'Electrical & Electronic Measurements and Instrumentation', Dhanpath Rai & Co (P) Ltd, 2004.

REFERENCES

1. J.B.Gupta, 'A Course in Electronic and Electrical Measurements and Instrumentation', S.K. Kataria & Sons, Delhi, 2003.
2. S.K.Singh, 'Industrial Instrumentation and control', Tata McGraw Hill, 2nd edn.,2002.
3. H.S.Kalsi, 'Electronic Instrumentation', Tata McGraw Hill, 2004.
4. Martin U. Reissland, 'Electrical Measurement – Fundamental Concepts and Applications', New Age International (P) Ltd., 2001.

EE2207

ELECTRON DEVICES AND CIRCUITS LABORATORY
(B.E. (EEE), B.E. (E&I) and B.E. (I & C)
(Revised)

L T P C
0 0 3 2

1. Characteristics of Semiconductor diode and Zener diode.
2. Characteristics of Transistor under common emitter, common collector and common base configurations.
3. Characteristic of FET.
4. Characteristic of UJT.
5. Characteristics of SCR, DIAC and TRIAC.
6. Photo diode, phototransistor Characteristics and study of light activated relay circuit.
7. Static characteristics of Thermistors.
8. Single phase half wave and full wave rectifiers with inductive and capacitive filters.
9. Differential amplifiers using FET.
10. Study of CRO.
11. Series and Parallel resonance circuits.
12. Realization of Passive filters.

P: 45 TOTAL : 45 PERIODS

REQUIREMENT FOR A BATCH OF 30 STUDENTS

S.No.	Description of Equipment	Quantity required	Quantity available	Deficiency %
1.	Regulated Power Supply	15		
2.	Dual Trace CRO (20 MHz)	15		
3.	Function Generator	15		
4.	3 ^{1/2} Digit digital multimeter	10		
5.	Bread Boards	40		

6.	Transistor	25 Nos.		
7.	JFET	10 Nos.		
8.	Diode	10 Nos.		
9.	Zener Diode	5 Nos.		
10.	UJT	5 Nos.		
11.	Photo Diode	5 Nos.		
12.	Photo Transistor	5 Nos.		
13.	Thermistors	5 Nos.		
14.	OP-amp	10 Nos.		
15.	Milli Ammeter (0-100mA)	15 Nos.		
16.	Micro Ammeter (0-50 μ A)	10 Nos.		
17.	Low range voltmeter (0-30V)	10 Nos.		
18.	Resistor of various ranges	50 Nos.		
19.	Capacitors of various ranges	50 Nos.		
20.	Connecting wires	Sufficient Nos		

EE2209

DATA STRUCTURES AND ALGORITHMS LABORATORY
(Common to EEE, EIE &ICE)

L T P C
0 0 3 2

Aim:

To develop skills in design and implementation of data structures and their applications.

1. Implement singly and doubly linked lists.
2. Represent a polynomial as a linked list and write functions for polynomial addition.
3. Implement stack and use it to convert infix to postfix expression
4. Implement array-based circular queue and use it to simulate a producer-consumer problem.
5. Implement an expression tree. Produce its pre-order, in-order, and post-order traversals.
6. Implement binary search tree.
7. Implement insertion in AVL trees.
8. Implement priority queue using heaps
9. Implement hashing techniques
10. Perform topological sort on a directed graph to decide if it is acyclic.
11. Implement Dijkstra's algorithm using priority queues
12. Implement Prim's and Kruskal's algorithms

13. Implement a backtracking algorithm for Knapsack problem
14. Implement a branch and bound algorithm for traveling salesperson problem
15. Implement any randomized algorithm.

P: 45 TOTAL : 45 PERIODS

REQUIREMENT FOR A BATCH OF 30 STUDENTS

S.No.	Description of Equipment	Quantity required	Quantity available	Deficiency %
1.	Computer(Pentium 4)	40 Nos with one server		
2.	Dot matrix printer	3 Nos		
3.	Laser Printer	2 Nos		
4.	UPS (5 KVA)	2		
5.	Turbo C	40 Nodes		

EI2208

**ELECTRICAL MACHINES LABORATORY
(Common to EIE & ICE)**

**L T P C
0 0 3 2**

1. Open circuit characteristic of DC Shunt Generator.
2. Load test on DC Shunt Generator.
3. Speed control of DC Shunt Motor.
4. Brake test on DC Shunt Motor.
5. Brake test on DC Series Motor.
6. Regulation characteristic of three - phase Alternator.
7. Open circuit and short circuit tests on Single - phase Transformer.
8. Load test on Single - phase Transformer
9. Load test on Three - phase Induction Motor.
10. Brake test on Single - phase Induction Motor.
11. 'V' curves of Synchronous Motor.
12. Power measurement in three - phase circuit using two - wattmeter method.

P: 45 TOTAL : 45 PERIODS

REQUIREMENT FOR A BATCH OF 30 STUDENTS

S.No.	Description of Equipment	Quantity required	Quantity available	Deficiency %
1.	D.C motor – Generator set D.C motor – Shunt Generator D.C motor – Compound Generator	2 set 2 set		
2.	D.C. Shunt Motor	2 Nos.		
3.	D.C. Series Motor	1 No.		
4.	D.C. Compound Motor	1 No.		
5.	Single phase transformers	7 Nos.		
6.	Three phase transformers	2 Nos.		
7.	D.C. Motor – Alternator set	4 sets		
8.	Three phase Induction Motor (Squirrel cage)	3 Nos.		
9.	Three phase slip ring Induction Motor	1 No.		
10.	Single phase Induction Motor	2 Nos.		
11.	Resistive load 3 phase – 2 , single phase - 3	5 Nos.		
12.	Inductive load	1 No.		
13.	Single phase Auto transformer	5 Nos.		
14.	Three phase Auto transformer	3 Nos.		
15.	Moving Coil Ammeter of different ranges	20 Nos.		
16.	Moving Coil Voltmeter of different ranges	20 Nos.		
17.	Moving Iron Ammeter of different ranges	20 Nos.		
18.	Moving Iron voltmeter of different ranges	20 Nos.		
19.	Wire wound Rheostats of different ratings	30 Nos.		
20.	Tachometers	10 Nos.		

21.	Single element wattmeters of different ranges UPF / LPF	20 Nos.		
22.	Double element wattmeters of different ranges	4 Nos.		
23.	Power factor meter	2 Nos.		
24.	Digital multimeter	5 Nos.		
25.	Three point starter, four point starter, DOL starter, manual star / delta starter, semi automatic and fully automatic star / delta starter	1 No each for study experiment		
26.	SCR based semi and fully controlled rectifier module	2 Nos.		
27.	SCR based chopper module	2 Nos.		
28.	SCR based inverter module	2 Nos.		
29.	SCR based AC voltage regulation module	2 Nos.		
30.	SCR, MOSFET, IGBT Trainer module	Each 2 Nos.		

EE2253

CONTROL SYSTEMS
(Common to EEE, EIE & ICE)

L T P C
3 1 0 4

AIM

To provide sound knowledge in the basic concepts of linear control theory and design of control system.

OBJECTIVES

- i To understand the methods of representation of systems and to derive their transfer function models.
- ii To provide adequate knowledge in the time response of systems and steady state error analysis.
- iii To accord basic knowledge in obtaining the open loop and closed-loop frequency responses of systems.
- iv To understand the concept of stability of control system and methods of stability analysis.
- v To study the three ways of designing compensation for a control system.

UNIT I SYSTEMS AND THEIR REPRESENTATION

9

Basic elements in control systems – Open and closed loop systems – Electrical analogy of mechanical and thermal systems – Transfer function – Synchros – AC and DC servomotors – Block diagram reduction techniques – Signal flow graphs.

UNIT II	TIME RESPONSE	9
Time response – Time domain specifications – Types of test input – I and II order system response – Error coefficients – Generalized error series – Steady state error – P, PI, PID modes of feed back control.		
UNIT III	FREQUENCY RESPONSE	9
Frequency response – Bode plot – Polar plot – Determination of closed loop response from open loop response – Correlation between frequency domain and time domain specifications.		
UNIT III	STABILITY OF CONTROL SYSTEM	9
Characteristics equation – Location of roots in S plane for stability – Routh Hurwitz criterion – Root locus construction – Effect of pole, zero addition – Gain margin and phase margin – Nyquist stability criterion.		
UNIT IV	COMPENSATOR DESIGN	9
Performance criteria – Lag, lead and lag-lead networks – Compensator design using bode plots.		

L = 45 T = 15 TOTAL :60 PERIODS

TEXT BOOKS

1. I.J. Nagrath and M. Gopal, 'Control Systems Engineering', New Age International Publishers, 2003.
2. Benjamin C. Kuo, Automatic Control systems, Pearson Education, New Delhi, 2003.

REFERENCES

1. K. Ogata, 'Modern Control Engineering', 4th edition, PHI, New Delhi, 2002.
2. Norman S. Nise, Control Systems Engineering, 4th Edition, John Wiley, New Delhi, 2007.
3. Samarajit Ghosh, Control systems, Pearson Education, New Delhi, 2004
4. M. Gopal, 'Control Systems, Principles and Design', Tata McGraw Hill, New Delhi, 2002.

EI2251

**INDUSTRIAL INSTRUMENTATION – I
(Common to EIE & ICE)**

**L T P C
3 0 0 3**

AIM

To equip the students with relevant knowledge to suit the industrial requirements.

OBJECTIVES

To provide sound knowledge about various techniques used for the measurement of industrial parameters.

Discussion of load cells, torque meter and various velocity pick-ups.

Exposure to various accelerometer pick-ups, vibrometers, density and viscosity pick-ups.

To have an adequate knowledge about pressure transducers.

To have an idea about the temperature standards, calibration and signal conditioning used in RTD's.

To have a sound knowledge about thermocouples and pyrometry techniques.

UNIT I	MEASUREMENT OF FORCE, TORQUE AND VELOCITY	9
Electric balance – Different types of load cells – Hydraulic, pneumatic strain gauge- Magneto elastic and Piezo electric load cell – Different methods of torque measurements: strain gauge-Relative angular twist-Speed measurement:-Capacitive tacho-Dragcup type tacho-D.C and A.C tachogenerators – Stroboscope.		
UNIT II	MEASUREMENT OF ACCELERATION, VIBRATION AND DENSITY	9
Accelerometers:- LVDT, Piezo-electric, Strain gauge and Variable reluctance type accelerometer – Mechanical type vibration instruments – Seismic instruments as an accelerometer – Vibrometers : Calibration of vibration pickups – Units of density and specific gravity – Baume scale, and API scale- Pressure head type densitometers- Float type densitometers – Ultrasonic densitometer- Bridge type gas densitometer.		
UNIT III	PRESSURE MEASUREMENT	9
Units of pressure-Manometers-Different types –Elastic type pressure gauges: Bourdon tube, bellows and diaphragms-Electrical methods: Elastic elements with LVDT and strain gauges – Capacitive type pressure gauge –Piezo-resistive pressure sensor-Resonator pressure sensor-Measurement of vacuum:-McLeod gauge-Thermal conductivity gauges-Ionization gauges:- Cold cathode type and hot cathode type-Testing and calibration of pressure gauges-Dead weight tester.		
UNIT IV	TEMPERATURE MEASUREMENT	9
Definitions and standards-Primary and secondary fixed points –Calibration of thermometers - Different types of filled in system thermometer-Sources of errors in filled in systems and their compensation-Bimetallic thermometers – Electrical methods of temperature measurement-Signal conditioning of industrial RTDs and their characteristics-3 lead and 4 lead RTDs - Thermistors.		
UNIT V	THERMOCOUPLES AND RADIATION PYROMETERS	9
Thermocouples-Laws of thermocouple –Fabrication of industrial thermocouples –Signal conditioning of thermocouple output-Isothermal block reference junctions – Commercial circuits for cold junction compensation-Response of thermocouple –Special techniques for measuring high temperature using thermocouples – Radiation fundamentals- Radiation methods of temperature measurement -- Total radiation pyrometers-Optical pyrometers-Two colour radiation pyrometers – Fiber optic temperature measurement.		
L = 45 TOTAL : 45 PERIODS		

TEXT BOOKS:

1. Doebelin, E.O., "Measurement systems Application and Design", International Student Edition, 5th Edition, McGraw Hill Book Company, 2004.
2. Jone's Instrument Technology, Vol.2, Butterworth-Heinemann, International Edition, 2003.
3. A.K. Sawhney, 'A course in Electrical & Electronic Measurements and Instrumentation', Dhanpath Rai & Co (P) Ltd, 2004.

REFERENCES:

1. Liptak, B.G., "Instrumentation Engineers Handbook (Measurement)", CRC Press, 2005
2. Patranabis,D., "Principles of Industrial Instrumentation", 2nd Edition, Tata McGraw Hill Publishing Company Ltd., New Delhi, 1999.
3. Holman,P., "Experimental methods for Engineers", 6th Edition, McGraw Hill Book Company, 2000.
4. Nakra, B.C., and Chaudry, K.K., "Instrumentation measurement and Analysis", TataMcGraw Hill publishing Company Limited, 2004.

AIM

To introduce the fundamentals of digital circuits, combinational and sequential circuit.

OBJECTIVES

- i. To study various number systems and to simplify the mathematical expressions using Boolean functions – simple problems.
- ii. To study implementation of combinational circuits
- iii. To study the design of various synchronous and asynchronous circuits.
- iv. To expose the students to various memory devices.

UNIT I NUMBER SYSTEMS AND BOOLEAN ALGEBRA 9

Review of number systems; types and conversion, codes. Boolean algebra: De-Morgan's theorem, switching functions and simplification using K-maps and Quine McCluskey method.

UNIT II COMBINATIONAL CIRCUITS 9

Design of Logic gates. Design of adder, subtractor, comparators, code converters, encoders, decoders, multiplexers and demultiplexers. Function realization using gates and multiplexers.

UNIT III SYNCHRONOUS SEQUENTIAL CIRCUITS 9

Flip flops - SR, D, JK and T. Analysis of synchronous sequential circuits; design of synchronous sequential circuits – Completely and incompletely specified sequential circuits - state diagram; state reduction; state assignment, Counters – synchronous, a synchronous, updown and Johnson counters; shiftregisters.

UNIT IV ASYNCHRONOUS SEQUENTIAL CIRCUITS 9

Analysis of asynchronous sequential machines, state assignment, asynchronous Design problem.

UNIT V MEMORY DEVICES, PROGRAMMABLE LOGIC DEVICES AND LOGIC FAMILIES 9

Memories: ROM, PROM, EPROM, PLA, PLD, FPGA, digital logic families: TTL, ECL, CMOS.

L = 45 T = 15 TOTAL = 60 PERIODS

TEXT BOOKS:

1. M. Morris Mano, 'Digital Logic and Computer Design', Prentice Hall of India, 2002.
2. John M.Yarbrough, 'Digital Logic, Application & Design', Thomson, 2002.

REFERENCES:

1. Charles H.Roth, 'Fundamentals Logic Design', Jaico Publishing, IV edition, 2002.
2. Floyd, 'Digital Fundamentals', 8th edition, Pearson Education, 2003.
3. John F.Wakerly, 'Digital Design Principles and Practice', 3rd edition, Pearson Education, 2002.

OBJECTIVES

- i. To expose the fundamentals of thermodynamics and to be able to use it in accounting for the bulk behaviour of the sample physical systems.
- ii. To integrate the basic concepts into various thermal applications like IC engines, gas turbines, steam boiler, steam turbine, compressors, refrigeration and air conditioning.
- iii. To enlighten the various modes of heat transfer and their engineering applications.
Use of standard steam tables, refrigeration tables and heat transfer data book are permitted)

UNIT I BASIC CONCEPTS AND LAWS OF THERMODYNAMICS 12

Classical approach: Thermodynamic systems – Control volume - System and surroundings – Universe – Properties - State-process – Cycle – Equilibrium - Work and heat transfer – Point and path functions - First law of thermodynamics for open and closed systems - First law applied to a control volume - SFEE equations [steady flow energy equation] - Second law of thermodynamics - Heat engines - Refrigerators and heat pumps - Carnot cycle - Carnot theorem.

UNIT II IC ENGINES 8

Air standard cycles: Otto, diesel and dual cycles and comparison of efficiency - Working Principle of four stroke and two stroke engines - Working principle of spark ignition and compression ignition engines - Application of IC engines.

UNIT III STEAM BOILERS AND TURBINES 8

Formation of steam - Properties of steam – Use of steam tables and charts – Steam power cycle (Rankine) - Modern features of high-pressure boilers – Mountings and accessories – Testing of boilers.

Steam turbines: Impulse and reaction principle – Velocity diagrams – Compounding and governing methods of steam turbines (qualitative treatment only) - Layout and working principle of a steam power plant.

UNIT IV COMPRESSORS, REFRIGERATION AND AIR CONDITIONING 8

Positive displacement compressors – Reciprocating compressors – Indicated power – Clearance volume – Various efficiencies – Clearance ratio - Volume rate - Conditions for perfect and imperfect intercooling - Multi stage with intercooling – Rotary positive displacement compressors – Construction and working principle of centrifugal and axial flow compressors.

Refrigeration - Various methods of producing refrigerating effects (RE) – Vapour compression cycle: P-H and T-S diagram - Saturation cycles - Effect of subcooling and super heating - (qualitative treatment only) - Airconditioning systems – Basic psychrometry - Simple psychrometric processes - Types of airconditioning systems - Selection criteria for a particular application (qualitative treatment only).

UNIT V HEAT TRANSFER 9

One-dimensional Heat Conduction: Plane wall – Cylinder – Sphere - Composite walls – Critical thickness of insulation – Heat transfer through extended surfaces (simple fins).

Convection: Free convection and forced convection - Internal and external flow – Simple Empirical relations.

Radiation: Black–Gray bodies - Radiation Shape Factor (RSF) - Cooling of electronic components - Thermoelectric cooling – Chip cooling.

L = 45 T = 15 TOTAL : 60 PERIODS

TEXT BOOKS:

1. R.S.Khurmi & J.K.Gupta, Thermal Engineering, S.Chand & Co. Ltd., 2006.
2. S.Domkundwar, C.P.Kothandaraman & A.V.Domkundwar, Thermal Engineering, Dhanpat Rai & Co. 2002.

REFERENCES

1. Rogers and Mayhew, 'Engineering Thermodynamics – Work and Heat Transfer', Pearson Education Pvt. Ltd. New Delhi, 2006.
2. Eastop and McConkey, 'Applied Thermodynamics', Pearson Education Pvt. Ltd. New Delhi, 2002.
3. P.K.Nag, 'Engineering Thermodynamics Tata McGraw Hill, New Delhi, 2003.
4. Rajput, B.K. Sankaar, Thermal Engineering, S.Chand & Co. Ltd., 2003.

EI2257

TRANSDUCERS AND MEASUREMENTS LABORATORY (Common to EIE & ICE)

L T P C
0 0 3 2

OBJECTIVES

The aim of this lab is to train the students in handling the different kinds of transducers like LVDT, Hall effect, Thermocouple etc., which he often meets in his study and also to impart the students an adequate knowledge and work experience of the different types of AC and DC bridges, electronic measurement methods for different electronic instruments.

1. Displacement versus output voltage characteristics of a potentiometric transducer.
2. Characteristics of Strain gauge and Load cell.
3. Characteristics of LVDT, Hall effect transducer and Photoelectric tachometer.
4. Characteristic of LDR, thermistor and thermocouple.
5. Step response characteristic of RTD and thermocouple and Study of smart transducers.
6. Wheatstone and Kelvin's bridge for measurement of resistance.
7. Schering Bridge for capacitance measurement and Anderson Bridge
8. for inductance measurement.
9. Calibration of Single-phase Energy meter and wattmeter.
10. Calibration of Ammeter and Voltmeter using Student type potentiometer.
11. Design, Construction and calibration of series and shunt type
12. ohmmeters.

P = 45 TOTAL = 45 PERIODS

DETAILED SYLLABUS

1. LOADING EFFECT ON POTENTIOMETER

AIM

To study the loading effect on potentiometer circuit.

OBJECTIVES

- i. To observe the output, input calibration curve using FET voltmeter has the output device.
- ii. To observe the output, input characteristic with an voltmeter whose input impedance is finite.
- iii. To observe the linearity which decreases with a decrease in the input impedance of the output meter.

EXERCISE

1. In the potentiometer circuit, displacement is given to the wiper arm and the corresponding output is observed with 2 meters (one is a FET voltmeter and the other is meter with a finite input impedance)
2. For various input displacements, output voltage from the two different meters are recorded and tabulated.
3. Plot the graph output Vs input displacement for both cases.

EQUIPMENT

- | | |
|---|--------|
| 1. Potentiometer – Linear displacement transducer kit | – 1 No |
| 2. Regulated power supply | – 1 No |
| 3. FET voltmeter, ordinary voltmeter | – 1 No |

2.CHARACTERISTICS OF STRAIN GAUGE AND LOAD CELL

AIM

To study the characteristics of strain gauge and load cell.

OBJECTIVES

1. To identify and study the characteristics of strain gauge and load cell.
2. To determine the sensitivity of strain gauge and load cell.
3. To determine the Young's modulus and hence the gauge factor of the given strain gauge.

EXERCISE

1. Load and Unload the load cell and strain gauge.
2. Measure the corresponding voltages during both loading and unloading and plot the calibration curve.
3. Find the Young's Modulus and gauge factor from the graph.

EQUIPMENT

- | | |
|------------------------------------|---------|
| 1. Strain gauge and Load cell kit. | – 1 No |
| 2. Variable power supply | – 1 No |
| 3. Loads for measurement | - A set |

3. CHARACTERISTICS OF LVDT, HALL EFFECT TRANSDUCER AND PHOTOELECTRIC TACHOMETER.

3.(A) CHARACTERISTICS OF LVDT

AIM

To study the operation and characteristics of LVDT

OBJECTIVES

1. To study the displacement of the core from its null position.
2. To study the variation of output voltage with change in displacement.

EXERCISE

1. Adjust the potentiometer knob present in the LVDT kit to bring the core to Null position (set the output voltage to be '0' volts)
2. Rotate the knob in the positive direction such that the LVDT scale moves in steps of 1cm and measure the corresponding output voltage.
3. Tabulate the readings.
4. Repeat the above procedure for negative displacement.
5. Plot the characteristic curve between displacement and output voltage.

EQUIPMENTS

1. LVDT trainer kit – 1 No
2. Power supply – 1 No

3.(b) HALL EFFECT TRANSDUCER

AIM

To study the characteristics of Hall effect transducer.

OBJECTIVE

1. To determine the positive hall voltage at the bottom of the transducer.
2. To determine the negative hall voltage.
3. To identify and study the characteristics of hall effect transducer.
4. To measure the displacement of a structural element .

EXERCISE

1. Study the internal configuration of Hall effect IC.
2. Patch the circuit diagram as per patching diagram.
3. Place the north pole of the magnet above the scale and take the reading air gap between hall IC and magnet to output voltage.
4. Place the south pole of the magnet above the scale and take the reading for different distances and plot the graph between air gap voltmeter readings.

EQUIPMENTS

1. Hall effect characteristics trainer – 1 No
2. Power supply – 1 No
3. Voltmeter – 1 No

3.(c) PHOTOELECTRIC TACHOMETER

AIM

To study the characteristics of photoelectric tachometer using the servo motor speed control trainer kit.

OBJECTIVES

1. To calculate the number of pulses generated in the photoelectric pick up.
2. To study the variation of speed with the variation of the input voltage.

EXERCISE

1. Connect the circuit as per instructions given in the manual.
2. Adjust the power supply.
3. Vary the speed of the motor by using rotary potentiometer and note down the readings.
4. Calculate number of pulses generated in the photoelectric pick up.
5. Draw the graph between voltage and speed.

EQUIPMENTS

1. Speed control trainer kit – 1 No
2. Power supply – 1 No
3. Wires - Some
4. Multimeter – 1 No

4.CHARACTERISTIC OF LDR, THERMISTOR AND THERMOCOUPLE.

(a) CHARACTERISTICS OF LDR

AIM

To determine the characteristics of LDR

OBJECTIVES

1. To determine the change in resistance for corresponding change in light intensity.
2. To determine the output voltage for corresponding change in voltage.

EXERCISE

1. The lamp for LDR is selected by using a select switch.
2. Initially the lamp is kept away from LDR.
3. Now the distance is decreased gradually and the corresponding values of voltages and resistances are taken.
4. Repeat the above steps for various positions of lamp.

EQUIPMENTS

- | | |
|------------------------------|--------|
| Photo conductive trainer kit | – 1 No |
| Multimeter | – 1 No |
| Connecting wires | – 1 No |

(b) CHARACTERISTICS OF THERMISTOR

AIM

To determine the characteristics of thermistor

OBJECTIVES

To measure the resistance value for the corresponding changes in temperature.

EXERCISE

1. Measure the initial temperature of water.
2. Take another vessel full of water and boil it to 100°C.
3. Note down the readings for every 5°C fall of temperature in thermistor, thermometer and output voltage readings.
4. Plot the Thermistor characteristics.

EQUIPMENTS

- | | |
|---------------------------|--------|
| 1. Thermistor Trainer kit | – 1 No |
| 2. Heater | – 1 No |
| 3. Thermistor | – 1 No |
| 4. Thermometer | – 1 No |
| 5. Voltmeter | – 1 No |

4(c) CHARACTERISTICS OF THERMOCOUPLE

AIM

To determine the characteristics of thermocouple.

OBJECTIVES

1. To determine the voltage for corresponding change in temperature.

EXERCISE

1. Measure the initial temperature and temperature of boiling water (100°C)
2. Calibrate the thermocouple in the hot water and measure the 5°C temperature fall in thermocouple.
3. The output voltage is noted for corresponding fall in temperature.

EQUIPMENT

1. Thermocouple trainer kit – 1 No

- | | |
|-----------------|--------|
| 2. Thermocouple | – 1 No |
| 3. Voltmeter | – 1 No |
| 4. Heater | – 1 No |

5. STEP RESPONSE CHARACTERISTIC OF RTD AND THERMOCOUPLE AND STUDY OF SMART TRANSDUCERS.

(a).STEP RESPONSE CHARACTERISTICS OF RTD AND THERMOCOUPLE

AIM

To study the step response characteristic of RTD and thermocouple.

OBJECTIVE

- a. To analyse the change in temperature due to change in emf in case of thermocouple.
- b. To analyse the change in temperature due to change in resistance in case of RTD.
- c. To observe the transients when step input [i.e sudden change in the input] is given.

EXERCISE

1. Calibrate the RTD and thermocouple at room temperature and 100⁰C alternatively.
2. Bring down the sensor to room temperature and provide a sudden change of input temperature to boiling point (i.e) 100⁰C.
3. Start the stop clock and tabulate the time taken for every 5⁰C rise of temperature.
4. Plot the step response for both the sensors.

EQUIPMENT

- | | |
|-------------------------------------|--------|
| 1. Thermocouple and RTD trainer kit | – 1 No |
| 2. Thermometer | – 1 No |
| 3. Heater | – 1 No |
| 4. Thermocouple and RTD sensors | – 1 No |
| 5. Voltmeters | – 1 No |
| I/P trainer kit | – 1 No |
| Pressure source | – 1 No |
| Control valve etc | – 1 No |

6. WHEATSTONE AND KELVIN'S BRIDGE FOR MEASUREMENT OF RESISTANCE.

(A) MEASUREMENT OF MEDIUM RESISTANCE USING WHEATSTONE'S BRIDGE

AIM

To measure the value of unknown resistance using Wheatstone's Bridge.

EXERCISE

Find the value of unknown resistance.

PROCEDURE

1. Connections are given as per the circuit diagram.
2. Supply is switched on.
3. When the unknown resistance s connected, the bridge becomes unbalanced.
4. The bridge is balanced by varying standard resistance.
5. The value of unknown resistance is calculated by the given formula.
6. The above steps are repeated for different value of unknown resistances.

EQUIPMENT

- | | |
|--------------|--------|
| 1. Resistors | – 1 No |
|--------------|--------|

- | | | |
|----|------------------------|--------|
| 2. | Galvanometer | – 1 No |
| 3. | Regulated Power supply | – 1 No |
| 4. | Bread board | – 1 No |
| 5. | Decade resistance box | – 1 No |
| 6. | Multimeter | – 1 No |

(b) KELVIN'S DOUBLE BRIDGE

AIM

To find the unknown value of low resistance using Kelvin's Double Bridge.

EXERCISE

Find the unknown value of low resistance.

PROCEDURE

1. Connections are given as per the circuit diagram.
2. Supply is switched on.
3. The bridge becomes unbalanced when unknown resistance R is connected.
4. The bridge is balanced by varying standard resistance.
5. Unknown resistance is calculated using balance equation.
6. The above steps are repeated for various values of unknown resistance.

EQUIPMENT

- | | | |
|----|-----------------------|--------|
| 1. | Power supply | – 1 No |
| 2. | Fixed resistance | – 1 No |
| 3. | Unknown resistors | – 1 No |
| 4. | Decade resistance box | – 1 No |
| 5. | Multimeter | – 1 No |
| 6. | Galvanometer | – 1 No |
| 7. | Bred board | - 1 No |

7. SCHERING BRIDGE FOR CAPACITANCE MEASUREMENT AND ANDERSON BRIDGE FOR INDUCTANCE MEASUREMENT.

(a) SCHERING'S BRIDGE

AIM

To measure the unknown value of capacitance using Schering's bridge

EXERCISE

Measure the unknown value of capacitance.

PROCEDURE

1. Connections are given as per the circuit.
2. Supply is witched on.
3. When unknown value of capacitance is connected, bridge becomes unbalanced.
4. The bridge is balanced by varying the standard.
5. The unknown value of capacitance is calculated using the balance equation.
6. The above steps are repeated for different values of unknown capacitances.

EQUIPMENT

- | | | |
|----|-----------|-------------|
| 1. | Resistors | - Some set. |
|----|-----------|-------------|

- | | | |
|----|------------------------|-------------|
| 2. | Capacitors | – Some set. |
| 3. | Decade Resistance box | – 1 No. |
| 4. | Decade Capacitance box | – 1 No. |
| 5. | CRO | – 1 No. |
| 6. | Function Generator | – 1 No. |

(b) ANDERSON'S BRIDGE

AIM

To measure the unknown value of inductance using Anderson's Bridge

EXERCISE

Measure the unknown value of inductance.

PROCEDURE

1. Connections are given as per the circuit diagram.
2. Supply is switched on.
3. When unknown value of inductance is connected the bridge becomes unbalanced.
4. The unknown value of inductance is calculated by using the balance equation.
5. The above step are repeated for different values of unknown inductance.

EQUIPMENT

- | | | |
|----|------------------------|---------|
| 1. | Resistors – Some set | |
| 2. | Decade Inductance box | – 1 No. |
| 3. | Decade Condenser box | – 1 No. |
| 4. | Regulated power supply | – 1 No. |
| 5. | CRO | – 1 No. |
| 6. | Bread board | - 1 No. |

8.CALIBRATION OF SINGLE-PHASE ENERGY METER AND WATTMETER.

(a) CALIBRATION OF SINGLE PHASE ENERGY METER

AIM

To calibrate the given energy meter using two substandard wattmeters and to obtain percentage error.

EXERCISE

Calibrate the given energy meter and draw % error Vs load graph.

PROCEDURE

1. Connections are given as per the circuit diagram.
2. The value of load current is adjusted to desire value.
3. When the red mark on the disk of the energy meter passes the observation point, the stopwatch is started and the number of revolution made by the disc is noted.
4. The load current is maintained by adjusting the load.
5. When the disc of the energy meter completes desired number of revolutions the stopwatch is stopped and the time taken is noted.
6. The procedure is repeated for different values of wattmeter reading and time taken, number of revolutions of the disc is noted down.
7. The graph is plotted between percentage error and load.

EQUIPMENT

- | | | |
|----|-----------|--------|
| 1. | Wattmeter | – 2 No |
|----|-----------|--------|

2. Voltmeter – 1 No
3. Ammeter – 1 No
4. Resistive load – 1 No

(b) CALIBRATION OF WATTMETER

AIM

To calibrate the given wattmeter using direct loading.

EXERCISE

Calibrate the given wattmeter and draw the graph between % error and load current.

PROCEDURE

1. Connections are given as per the circuit diagram.
2. Supply is given at no load condition.
3. Resistive load is applied in steps and the readings are tabulated.
4. Graph is drawn between % error and load current.

EQUIPMENT

1. Ammeter – 1 No
2. Voltmeter – 1 No
3. Wattmeter – 1 No
4. Load – 1 No

9. CALIBRATION OF AMMETER AND VOLTMETER USING STUDENT TYPE POTENTIOMETER.

(a) CALIBRATION OF AMMETER

AIM

To calibrate the given ammeter using standard ammeter

EXERCISE

Calibrate the given ammeter and draw the graph between % error and A_s .

PROCEDURE

1. Connections are given as per the circuit diagram.
2. The standard ammeter should be selected properly.
3. Supply is switched on.
4. At no load condition the readings of all the meters are noted.
5. By gradually increasing the load, the respective readings are taken from the meters.
6. The readings are tabulated and % error is calculated from the formula.
7. Graph is drawn between A_s and % error.
8. The procedure is repeated for both ac and dc supply.

EQUIPMENT

1. Standard ammeter – 1 No.
2. Ammeter – 1 No.
3. Variable resistive load – 1 No.
4. RPS – 1 No.

(b) CALIBRATION OF VOLTMETER

AIM

To calibrate the given voltmeter using standard voltmeter.

EXERCISE

Calibrate the given voltmeter and draw the graph between % error and

V_s .

PROCEDURE

1. Connections are given as per the circuit diagram.
2. The standard voltmeter should be selected properly.
3. Supply is switched on.
4. At no load condition the readings of all the meters are noted.
5. By gradually increasing the voltage, the respective readings are taken from the meters.
6. The readings are tabulated and % error is calculated from the formula.
7. Graph is drawn between V_s and % error.
8. The procedure is repeated for both ac and dc supply.

EQUIPMENT

- | | | |
|----|--------------------|---------|
| 1. | Standard voltmeter | – 1 No. |
| 2. | Voltmeter | – 1 No. |
| 3. | Auto transformer | – 1 No. |
| 4. | RPS | – 1 No. |

10. DESIGN AND CALIBRATION OF SERIES AND SHUNT TYPE OHMMETERS.

(a) SERIES TYPE OHMMETERS

AIM

To conduct a suitable experiment to measure an unknown medium resistance ($1\Omega - 0.1M\Omega$) with the series type ohmmeter.

OBJECTIVE

The instrument most commonly used to check the continuity (a complete circuit), or to measure the resistance of a circuit or circuit element, is the **OHMMETER**. The ohmmeter is widely used to measure resistance and check the continuity of electrical circuits and devices.

OHMMETER SAFETY PRECAUTIONS

The following safety precautions and operating procedures for ohmmeters are the **MINIMUM** necessary to prevent injury and damage.

- Be certain the circuit is deenergized and discharged before connecting an ohmmeter.
- Do not apply power to a circuit while measuring resistance.
- When you are finished using an ohmmeter, switch it to the OFF position if one is provided and remove the leads from the meter.
- Always adjust the ohmmeter for 0 (or in shunt ohmmeter) after you change ranges before making the resistance measurement.

EXERCISE

1. Place the resistance to be measured in series with the internal resistors and the meter movement of the ohmmeter.
2. Note down the reading of the meter and calculate the practical value.
3. Calculate the theoretical value

4. Find the difference and error between the theoretical and practical values.
5. Measure the Resistor using Ammeter – Voltmeter method and compare the result with the Ohmmeter method.
6. Calculate the difference and %error.
7. To implement the continuity test, consider any one electronic circuit and check the continuity

EQUIPMENT

1. Ohmmeter (Analog Multimeter) – 1No
2. Voltmeter - 1 No
3. Ammeter - 1 No
4. Resistor - 1 No
5. RPS - 1 No

(b) SHUNT TYPE OHMMETER

AIM

- i. To conduct a suitable experiment to measure an unknown medium resistance ($1\Omega - 0.1M\Omega$) with the series type ohmmeter.
- ii. To compare the result with the Ammeter – Voltmeter method

EXERCISE

1. Place the resistance to be measured in shunt (in parallel) with the meter movement of the ohmmeter.
2. Note down the reading of the meter and calculate the practical value.
3. Calculate the theoretical value
4. Find the difference and error between the theoretical and practical values.
5. Measure the Resistor using Ammeter – Voltmeter method and compare the result with the Ohmmeter method.
6. Calculate the difference and %error.
7. To implement the continuity test, consider any one electronic circuit and check the continuity

EQUIPMENT

1. Ohmmeter(Analog Multimeter) – 1No
2. Voltmeter - 1 No
3. Ammeter - 1 No
4. Resistor - 1 No
5. RPS - 1 No

EI2258

THERMODYNAMICS LABORATORY
(Common to EIE & ICE)

L T P C
0 0 3 2

THERMODYNAMICS LAB

1. Valve timing and port timing diagrams for IC Engines.
2. Performance test on a Petrol Engine.
3. Performance test on a Diesel Engine.
4. Heat Balance test on an IC Engine.

5. Boiler – performance and Heat Balance Test.
6. Performance test on a Refrigerator (Determination of COP)
7. Determination of heat transfer Coefficient (Free and forced convection)
8. Test to estimate frictional losses in pipe flow.
9. Test on reaction turbine for obtaining the characteristics curves and to design values of specific speed, discharge, output and efficiency.
10. Test on impulse turbine to obtain its characteristics curves and hydraulic design values.

LIST OF EQUIPMENTS

S.NO	APPARATUS	QUANTITY
1.	Engine – cut section models.	1 Set
2.	Single cylinder petrol engine with Mechanical dynamometer.	1 Set
3.	Multi cylinder petrol engine with hydraulic dynamometer.	1 Set
4.	Multi cylinder diesel engine with Electrical dynamometer.	1 Set
5.	Steam boilers with suitable mountings and accessories.	1 Set
6.	Refrigeration Test Rig.	1 No.
7.	Forced convection Heat transfer Test set up.	1 No.
8.	Free convection Heat transfer test set up.	1 No.
9.	Apparatus for measuring pipe friction	1 No.
10.	Francis turbine	1 No.
11.	Pelton wheel	1 No.
12.	Turgo impulse wheel	1 No.
13.	Stop watches	6 Nos.

AIM

- To study various digital & linear integrated circuits used in simple system configuration.
1. Study of Basic Digital IC's. (Verification of truth table for AND, OR, EXOR, NOT, NOR, NAND, JK FF, RS FF, D FF)
 2. Implementation of Boolean Functions, Adder/ Subtractor circuits.
 - 3a) Code converters, Parity generator and parity checking, Excess-3, 2s Complement, Binary to Gray code using suitable IC's .
 - 3(b) Encoders and Decoders: Decimal and Implementation of 4-bit shift registers in SISO, SIPO, PISO, PIPO modes using suitable IC's.
 4. Counters: Design and implementation of 4-bit modulo counters as synchronous and Asynchronous types using FF IC's and specific counter IC.
 5. Shift Registers:
Design and implementation of 4-bit shift registers in SISO, SIPO, PISO, PIPO modes using suitable IC's.
 6. Multiplex/ De-multiplex:
Study of 4:1; 8:1 multiplexer and Study of 1:4; 1:8 demultiplexer
 7. Timer IC application:
Study of NE/SE 555 timer in Astable, Monostable operation.
 8. Application of Op-Amp:
Slew rate verifications, inverting and non-inverting amplifier, Adder, comparator, Integrater and Differentiator.
 9. Study of Analog to Digital Converter and Digital to Analog Converter: Verification of A/D conversion using dedicated IC's.
 10. Study of VCO and PLL ICs:
 - i. Voltage to frequency characteristics of NE/ SE 566 IC.
 - ii. Frequency multiplication using NE/SE 565 PLL IC.

P = 45 TOTAL: 45 PERIODS**DETAILED SYLLABUS**

1. **STUDY OF BASIC DIGITAL IC'S.**
(Verification of truth table for AND, OR, EXOR, NOT, NOR, NAND, JK FF, RS FF, D FF)

AIM

To test of ICs by using verification of truth table of basic ICs.

EXERCISE

Breadboard connection of ICs with truth table verification using LED's.

2. **IMPLEMENTATION OF BOOLEAN FUNCTIONS, ADDER/ SUBTRACTOR CIRCUITS.**
[Minimizations using K-map and implementing the same in POS, SOP from using basic gates]

AIM

Minimization of functions using K-map implementation and combination Circuit.

EXERCISE

1. Realization of functions using SOP, POS, form.
2. Addition, Subtraction of atleast 3 bit binary number using basic gate IC' s.

- 3a) **CODE CONVERTERS, PARITY GENERTOR AND PARITY CHECKING, EXCESS 3, 2S COMPLEMENT, BINARY TO GREY CODE USING SUITABLE ICS .**

AIM

Realizing code conversion of numbers of different bar.

EXERCISE

- 1 Conversion Binary to Grey, Grey to Binary;
1's. 2's complement of numbers addition, subtraction,
2. Parity checking of numbers using Gates and with dedicated IC's

- 3b) **Encoders and Decoders: Decimal and Implementation of 4-bit shift registers in SISO, SIPO,PISO,PIPO modes using suitable ics.**

EXERCISE

1. Decimal to binary Conversion using dedicated ICs.
2. BCD – 7 Segment display decoder using dedicated decoder IC& display.

3. **COUNTERS: DESIGN AND IMPLEMENTATION OF 4-BIT MODULO COUNTERS AS SYNCHRONOUS AND ASYNCHRONOUS TYPES USING FF IC'S AND SPECIFIC COUNTER IC.**

AIM

Design and implementation of 4 bit modulo counters.

EXERCISE

1. Using flipflop for up-down count synchronous count.
2. Realization of counter function using dedicated ICs.

5. **SHIFT REGISTERS:**

Design and implementation of 4-bit shift registers in SISO, SIPO, PISO, PIPO modes using suitable IC's.

AIM

Design and implementation of shift register.

EXERCISE

1. Shift Register function realization of the above using dedicated IC's
For SISO, SIPO, PISO, PIPO, modes of atleast 3 bit binary word.
2. Realization of the above using dedicated IC's.

6. MULTIPLEX/ DE-MULTIPLEX.

Study of 4:1; 8:1 multiplexer and Study of 1:4; 1:8 demultiplexer

AIM

To demonstrate the addressing way of data channel selection for multiplex De-multiplex operation.

EXERCISE

1. Realization of mux-demux functions using direct IC's.
2. Realization of mux-demux using dedicated IC's for 4:1, 8:1, and vice versa.

7. TIMER IC APPLICATION. STUDY OF NE/SE 555 TIMER IN ASTABLE, MONOSTABLE OPERATION.

AIM

To design a multi vibrator circuit for square wave and pulse generation.

EXERCISE

1. Realization of Astable multivibrator & monostable multivibrator circuit using Timer IC.
2. Variation of R, C, to vary the frequency, duty cycle for signal generator.

8. APPLICATION OF OP-AMP-I

Slew rate verifications, inverting and non-inverting amplifier, Adder, comparator, Integrater and Differentiator.

AIM

Design and Realization of Op-Amp application.

EXERCISE

1. Verification of Op-Amp IC characteristics.
2. Op-Amp IC application for simple arithmetic circuit.
3. Op-Amp IC application for voltage comparator wave generator and wave shifting circuits.

9. STUDY OF ANALOG TO DIGITAL CONVERTER AND DIGITAL TO ANALOG CONVERTER: VERIFICATION OF A/D CONVERSION USING DEDICATED IC'S.

AIM

Realization of circuit for digital conversions.

EXERCISE

1. Design of circuit for analog to digital signal conversion using dedicated IC's.
2. Realization of circuit using dedicated IC for digital analog conversion.

10. STUDY OF VCO AND PLL ICS

- i) Voltage to frequency characteristics of NE/ SE 566 IC.
- ii) Frequency multiplication using NE/SE 565 PLL IC.

Aim

Demonstration of circuit for communication application

Exercise

1. To realize V/F conversion using dedicated IC's vary the frequency of the generated signal.
2. To realize PLL IC based circuit for frequency multiplier, divider.

REQUIREMENT FOR A BATCH OF 30 STUDENTS

S.NO.	DESCRIPTION OF EQUIPMENT	QUANTITY REQUIRED	QUANTITY AVAILABLE	DEFICIENCY %
1.	Interface such as, A/D, D/A converter, DMA, PIC Serial, Interface, Temperatures controller, Stepper motor, Key board	4 each		
2.	CRO and function generator	3 each		
3.	IC trainer Kit	15		
4.	Analog AC trainer kit	4		
5.	Components and bread boards	10 each		
6.	Chips IC – 7400	10		
7.	Chips IC – 7402	10		
8.	Chips IC – 7408	10		
9.	Chips IC – 7432	10		
10.	Chips IC – 7410	25		
11.	Chips IC – 555	10		
12.	Chips IC – 741	10		
13.	Chips IC – 74153	10		
14.	Chips IC – 7474	10		
15.	Chips IC – 7490	10		
16.	Chips IC – 7447	10		
17.	Chips IC – 7476	10		
18.	Chips IC – 7420	10		
19.	Chips IC – 7404	15		
20.	Chips LM – 317	10		

21.	Chips LM – 723	10		
22.	Chips MA – 7840	10		
23.	Chips LM – 380	10		
24.	Chips ICL - 8038	10		
25.	Traffic light control kit	2		
26.	VDU	2		
27.	7 segment Display	5		
28.	Interfacing card such as keyboard etc.	3 each		
29.	Work tables	15		

EC2312

MICROPROCESSORS AND MICRO CONTROLLER

**L T P C
3 0 0 3**

AIM

To introduce Microprocessor Intel 8085 and 8086 and the Micro Controller 8051

OBJECTIVES

- i. To study the Architecture of 8085 & 8086, 8051
- ii. To study the addressing modes & instruction set of 8085 & 8051.
- iii. To introduce the need & use of Interrupt structure 8085 & 8051.
- iv. To develop skill in simple program writing for 8051 & 8085 and applications
- v. To introduce commonly used peripheral / interfacing ICs

UNIT I 8085 and 8086 PROCESSOR

9

Hardware Architecture pinouts - Signals – Memory interfacing – I/O ports and data transfer concepts – Timing Diagram – Interrupt structure.

UNIT II PROGRAMMING OF 8085 PROCESSOR

9

Instruction format and addressing modes – Assembly language format – Data transfer, data manipulation & control instructions – Programming: Loop structure with counting & Indexing - Look up table - Subroutine instructions - stack.

- UNIT III PERIPHERAL INTERFACING FOR 8085** **9**
 Study of Architecture and programming of ICs: 8255 PPI, 8259 PIC, 8251 USART, 8279 Key board display controller and 8253 Timer/ Counter – Interfacing with 8085 - A/D and D/A converter interfacing.
- UNIT IV 8051 MICRO CONTROLLER** **9**
 Functional block diagram - Instruction format and addressing modes – Timing Diagram Interrupt structure – Timer –I/O ports – Serial communication.
- UNIT V MICRO CONTROLLER PROGRAMMING & APPLICATIONS** **9**
 Data Transfer, Manipulation, Control & I/O instructions – Simple programming exercises key board and display interface – Closed loop control of servo motor- stepper motor control - Washing Machine Control.

TOTAL : 45 PERIODS

TEXT BOOKS

1. "Microprocessor and Microcontrollers", Krishna Kant Eastern Company Edition, Prentice – Hall of India, New Delhi , 2007.
2. Muhammad Ali Mazidi & Janice Gilli Mazidi, R.D.Kinely 'The 8051 Micro Controller and Embedded Systems', PHI Pearson Education, 5th Indian reprint, 2003.

REFERENCES

1. R.S. Gaonkar, 'Microprocessor Architecture Programming and Application', Wiley Eastern Ltd., New Delhi.
2. The 8088 & 8086 Microprocessors , Walter A Tribal & Avtar Singh, Pearson, 2007, Fourth Edition.

EC2315

COMMUNICATION ENGINEERING

L T P C
3 0 0 3

AIM

1. To introduce the fundamental techniques of analog, digital and data communication.
2. To explain satellite and fiber optic communication and Networking systems.

OBJECTIVES

- i. To understand basic signals, analog modulation, demodulation and radio receivers.
- ii. To explain the characteristics and model of transmission medium.
- iii. To understand source digitization, digital multiplexing and modulation.
- iv. To understand data communication system and techniques.
- v. To learn the basics of satellite and optical fiber communication systems.

UNIT I INTRODUCTION **9**
 Transmission lines – Types, equivalent circuit, losses, standing waves, impedance matching, bandwidth; radio propagation – Ground wave and space wave propagation, critical frequency, maximum usable frequency, Path Loss, Gaussian white noise. Time and frequency domain representation of signals need for modulation

UNIT II ANALOG MODULATION SYSTEMS **9**
 Amplitude modulation and demodulation, frequency modulation and demodulation, super heterodyne radio receiver. Frequency division multiplexing. Time Division multiplexing.

UNIT V**10**

Inheritance – interfaces and inner classes - exception handling – threads - Streams and I/O

TOTAL : 45 PERIODS**TEXT BOOKS:**

1. B. Trivedi, "Programming with ANSI C++", Oxford University Press, 2007.
2. Cay S. Horstmann, Gary Cornell, "Core JAVA volume 1", Eighth Edition, Pearson Education, 2008.

REFERENCES:

1. ISRD Group, "Introduction to Object-oriented Programming and C++", Tata McGraw-Hill Publishing Company Ltd., 2007.
2. ISRD Group, "Introduction to Object-oriented programming through Java", Tata McGraw-Hill Publishing Company Ltd., 2007.
3. S. B. Lippman, Josee Lajoie, Barbara E. Moo, "C++ Premier", Fourth Edition, Pearson Education, 2005.
4. D. S. Malik, "C++ Programming: From Problem Analysis to Program Design", Third Edition, Thomson Course Technology, 2007.
5. K. Arnold and J. Gosling, "The JAVA programming language", Third edition, Pearson Education, 2000.
6. C. Thomas Wu, "An introduction to Object-oriented programming with Java", Fourth Edition, Tata McGraw-Hill Publishing Company Ltd., 2006.

EI2301**INDUSTRIAL ELECTRONICS****L T P C
3 0 0 3****AIM**

To introduce the application of electronic devices for conversion, control and conditioning of electric power.

OBJECTIVES

- i. To get an overview of different types of power semi-conductor devices and their switching characteristics.
- ii. To understand the operation, characteristics and performance parameters of controlled rectifiers.
- iii. To study the characteristics of DC and AC drives
- iv. To learn the different modulation techniques of pulse width modulated inverters and to understand the harmonic reduction methods.
- v. To know the practical application for power electronics converters in conditioning the power supply.

UNIT I POWER DEVICES**9**

Power diode – Power transistor – Power MOSFET – SCR – TRIAC – GTO – IGBT – MCT – Protection of power devices.

UNIT II	CONVERTERS	9
Introduction to half wave, full wave and bridge rectifiers – Single phase and three phase – Half controlled and fully controlled converters – Dual converters – Introduction to cyclo converters and ac controllers.		
UNIT III	INVERTER AND CHOPPER	9
Voltage, current and load commutation – Voltage Source Inverter (VSI) – Series and Parallel inverter – Bridge inverters – Single and three phase – Voltage control using PWM – Current Source Inverter (CSI) – Choppers – Step up and step down choppers – Chopper classification – Class A, B, C, D, E – AC choppers.		
UNIT IV	DC AND AC DRIVES	9
Steady state characteristic of dc motors – Control of DC motor using converters and choppers – Regenerative and dynamic braking – Closed loop control scheme – Speed-torque characteristic of induction motor – Static stator voltage control – V/f control – Static rotor resistance control – Slip power recovery scheme – Self control of synchronous motor.		
UNIT V	OTHER APPLICATIONS	9
Electronic timers – Digital counters – Voltage regulators – Online and offline ups – Switched mode power supply – Principle and application of induction and dielectric heating.		

TOTAL : 45 PERIODS

TEXT BOOK:

1. G. K. Mithal, "Industrial Electronics", Khanna Publishers, Delhi, 2000.

REFERENCES

1. M. H. Rashid, "power Electronics Circuits, Devices and Application", PHI, 3rd edition, 2004.
2. G. M. Chute and R. D. Chute, "Electronics in Industry", McGraw Hill Ltd, Tokyo, 1995.
3. F. D. Petruzulla, "Industrial Electronics", McGraw Hill, Singapore, 1996.

EI2302	ANALYTICAL INSTRUMENTS	LT P C
		3 0 0 3

AIM

The course is designed to equip the students with an adequate knowledge of a number of analytical tools which are useful for clinical analysis in hospitals, drugs and pharmaceutical laboratories and above all for environmental Pollution Monitoring.

OBJECTIVES

- i. To provide various techniques and methods of analysis which occur in the various regions of the spectrum. These are the powerful tools used in Clinical and Research laboratories.
- ii. To give unique methods of separation of closely similar materials, the most powerful being gas chromatography.
- iii. To study important methods of analysis of industrial gases. Awareness and control of pollution in the environment is of vital importance.
- iv. To bring out the latest ideas on ion-selective electrodes as well as biosensors which have potential applications in medical field, food and beverage industries.
- v. To provide the important electromagnetic resonance and microscopic methods of analysis. Further they are both sensitive and specific and often are characterized by good accuracy. NMR & ESR and microscopic techniques are useful in structure determination.

AIM

To equip the students with relevant knowledge to suit the industrial requirement.

OBJECTIVES

- i. To study about humidity and moisture measurements.
- ii. To study about mechanical flow meters and their installation.
- iii. To study about area flow meters, mass flow meters and calibration.
- iv. To know elaborately about non-content type flow meters.
- v. To know about various types of level measurements adopted in industry environment.

UNIT I VARIABLE HEAD TYPE FLOWMETERS 9

Variable head type flow meters: – Orifice plate – Venturi tube – Flow nozzle – Dall tube – Installation of head flow meters – Pitot tube.

UNIT II QUANTITY METERS, AREA FLOW METERS AND MASS FLOW METERS 9

Positive displacement flow meters: – Nutating disc, Reciprocating piston, Oval gear and Helix type flow meters – Inferential meter – Turbine flow meter – Area flow meter: – Rotameter – Theory and installation – Mass flow meter: – Angular momentum – Thermal, Coriolis type mass flow meters – Calibration of flow meters – Dynamic weighing methods.

UNIT III ELECTRICAL TYPE FLOW METER 9

Principle and constructional details of electromagnetic flow meter – Ultrasonic flow meters – Laser Doppler anemometer – Vortex shedding flow meter – Target flow meter – Guidelines for selection of flow meter – Open channel flow measurement – Solid flow rate measurement

UNIT IV LEVEL MEASUREMENT 9

Level measurement: – Float, Displacer type – Bubbler system – Electrical level gauge: – Resistance – Capacitance – Nuclear radiation and Ultrasonic type – Boiler drum level measurement: – Differential pressure method – Hydra step method.

UNIT V MEASUREMENT OF VISCOSITY, HUMIDITY AND MOISTURE 9

Viscosity: – Rotameter type viscometer – Consistency meters – Dry and wet bulb psychrometers – Hot wire electrode type hygrometer – Dew cell – Electrolysis type hygrometer – Commercial type dew point meter – Moisture measurement: – Different methods of Moisture measurement – Application of moisture measurement .

TOTAL : 45 PERIODS

TEXT BOOKS

1. Doebelin, E.O., "Measurement systems Application and Design", International Student Edition, 5th Edition, McGraw Hill Book Company, 2004
2. Liptak, B.G., "Instrumentation Engineers Handbook (Measurement)", CRC Press, 2005
3. A.K. Sawhney, 'A course in Electrical & Electronic Measurement and Instrumentation', Dhanpat Rai and Co (P) Ltd., 2004.

REFERENCES

1. Jain, R.K., "Mechanical and Industrial Measurements", Khanna Publishers, Delhi, 1999.
2. Eckman, D.P., "Industrial Instrumentation", Wiley Eastern Limited, 1990.

AIM

To understand programming using instruction sets of processors and microcontroller.

8-bit Microprocessor

1. Simple arithmetic operations:
 - Addition / subtraction / multiplication / division.
2. Programming with control instructions:
 - Increment / Decrement.
 - Ascending / Descending order.
 - Maximum / Minimum of numbers.
 - Rotate instructions.
 - Hex / ASCII / BCD code conversions.
3. Peripheral Interface Experiments:
 - Simple experiments using 8251, 8279, 8254, 8259, 8255.
4. Interface Experiments:
 - A/D Interfacing.
 - D/A Interfacing.
 - Traffic light controller.
5. Programming practice on assembler and simulator tools.

8-bit Micro controller

6. Demonstration of basic instructions with 8051 Micro controller execution, including:
 - Conditional jumps, looping
 - Calling subroutines.
 - Stack parameter testing
7. Parallel port programming with 8051 using port 1 facility:
 - Stepper motor
 - D / A converter.
8. Programming Exercise on
 - RAM direct addressing
 - Bit addressing
9. Programming practice using simulation tools and C - compiler
 - Initialize timer
 - Enable interrupts.
10. Study of micro controllers with flash memory.

DETAILED SYLLABUS:**8-bit Microprocessor****1. SIMPLE ARITHMETIC OPERATIONS**

- a. Addition / subtraction / multiplication / division.

AIM

To perform simple arithmetic operations using assembly language program.

EXERCISE

1. Write an assembly language program using 8085 instructions set to perform the following arithmetic operations
 1. Addition of two 8 bit numbers
 2. Subtraction of two 8 bit numbers
 3. Multiplication of two 8 bit numbers
 4. Division of two 8 bit numbers

2. PROGRAMMING WITH CONTROL INSTRUCTIONS

- a. Increment / Decrement.
- b. Ascending / Descending order.
- c. Maximum / Minimum of numbers.
- d. Rotate instructions.
- e. Hex / ASCII / BCD code conversions.

AIM

To write an assembly language program using the control instructions

EXERCISE

1. Using the control instructions of 8085 microprocessor write assembly language programs to perform the following
 1. Arrange the given array of data in ascending and descending order
 2. Find the maximum and minimum number in a group of data given.
 3. Conversion of the following
 1. ASCII to HEX code
 2. Conversion of HEX to ASCII code
 3. Conversion of BCD to HEX
 4. Conversion of HEX to BCD

3 PERIPHERAL INTERFACE EXPERIMENTS:

- a. Simple experiments using 8251, 8279, 8254, 8259, 8255.

4. INTERFACE EXPERIMENTS:

A/D Interfacing.
D/A Interfacing.
Traffic light controller.

Aim

To write an assembly language program to convert Analog input to Digital output and Digital input to Analog output.

EXERCISE

1. Write an assembly language program (using 8085) to convert Analog input to Digital output
2. Write an assembly language programs to convert digital input into analog signal of following type.
 1. Square wave
 2. Triangular wave
 3. Sawtooth wave

5. PROGRAMMING PRACTICE ON ASSEMBLER AND SIMULATOR TOOLS. 8-BIT MICRO CONTROLLER

6. DEMONSTRATION OF BASIC INSTRUCTIONS WITH 8051 MICRO CONTROLLER EXECUTION, INCLUDING:

- Conditional jumps, looping
- Calling subroutines.
- Stack parameter testing

AIM

To demonstrate use of control logic instructors.

EXERCISE

1. To write programs which can include instruction sets for jump, loop, call, return, stack.
2. To observe the change in status registers and various relevant registers.

7. PARALLEL PORT PROGRAMMING WITH 8051 USING PORT 1 FACILITY:

- Stepper motor
- D / A converter.

AIM

To demonstrate the access of parallel port.

EXERCISE

1. To develop command words on choice of port, addressing of port pins.
2. To vary timing cycle of speed of motor, direction of motor.
3. To demonstrate generation of sine wave saw tooth, triangular wave of various frequency, amplitude.

8. PROGRAMMING EXERCISE ON

- RAM direct addressing
- Bit addressing

AIM

To write the program to check the content of memory locations using READ / WRITE instructions using different addressing modes.

EXERCISE

To READ / WRITE the content of RAM registers, bits and the RAM from location 1 to N and check the display with say LEDs.

9. PROGRAMMING PRACTICE USING SIMULATION TOOLS AND C – COMPILER

- Initialize timer
- Enable interrupts.

AIM

To use the facility of popular Micro controller programming tools like KEIL or RIDE software.

EXERCISE

1. To study the initializing of timer interrupt with context saving like increasing or decreasing the counter count.
2. To demonstrate use of instruction like cjne, djnz, jb etc.

10. STUDY OF MICRO CONTROLLERS WITH FLASH MEMORY.

AIM

To familiarize of loading and executing on flash memory.

EXERCISE

1. To write the program to generate sine wave, square wave etc.
2. To vary the frequency, amplitude of the signal.

TOTAL : 45 PERIODS

REQUIREMENT FOR A BATCH OF 30 STUDENTS

S.No.	Description of Equipment	Quantity required
1.	8085 Microprocessor Trainer with Power supply	10
2.	8051 Micro controller Trainer Kit with power supply	10
3.	8255 Interface board	5
4.	8251 Interface board	5
5.	8259 Interface board	5
6.	8279 Keyboard/Display Interface Board	5
7.	8253 timer counter	5
8.	ADC and DAC card	5 each
9.	Stepper motor with Controller	1
10.	Traffic Light Control System	1
11.	Regulation power supply	1
12.	Universal ADD-ON modules	3
13.	8 Digit Multiplexed Display Card	2
14.	Function Generator	3
15.	Multimeter	3
16.	C Compilers	2
17.	KEIL or RIDE software	2 licenses

18.	8051 Microcontroller trainer kit with flash memory	2
19.	AT89C51 Microcontroller Kit	2

CS2312

OBJECT- ORIENTED PROGRAMMING LAB

L T P C
0 0 3 2

AIM

To develop object-oriented programming skills using C++ and Java

1. Function overloading, default arguments in C++
2. Simple class design in C++, namespaces, objects creations
3. Class design in C++ using dynamic memory allocation, destructor, copy constructor
4. Operator overloading, friend functions
5. Overloading assignment operator, type conversions
6. Inheritance, run-time polymorphism
7. Template design in C++
8. I/O, Throwing and Catching exceptions
9. Program development using STL
10. Simple class designs in Java with Javadoc
11. Designing Packages with Javadoc comments
12. Interfaces and Inheritance in Java
13. Exceptions handling in Java
14. Java I/O
15. Design of multi-threaded programs in Java

TOTAL : 45 PERIODS

REQUIREMENT FOR A BATCH OF 30 STUDENTS

S.No.	Description of Equipment	Quantity required
	Hardware Required	
1.	Computers (Pentium-4)	40 Nos with one server
2.	Dot matrix printer	3 Nos
3.	Laser Printer	2 Nos.
4.	UPS (5 KVA)	2
	Software Required	
5.	Turbo C++	40 Nodes
6.	(Java 2 SDK) JDK 5.0 update 6 (1.5.0 - Internal Version No.)	40 Nos.

Globalisation has brought in numerous opportunities for the teeming millions, with more focus on the students' overall capability apart from academic competence. Many students, particularly those from non-English medium schools, find that they are not preferred due to their inadequacy of communication skills and soft skills, despite possessing sound knowledge in their subject area along with technical capability. Keeping in view their pre-employment needs and career requirements, this course on Communication Skills Laboratory will prepare students to adapt themselves with ease to the industry environment, thus rendering them as prospective assets to industries. The course will equip the students with the necessary communication skills that would go a long way in helping them in their profession.

OBJECTIVES:

- To equip students of engineering and technology with effective speaking and listening skills in English.
- To help them develop their soft skills and interpersonal skills, which will make the transition from college to workplace smoother and help them excel in their job.
- To enhance the performance of students at Placement Interviews, Group Discussions and other recruitment exercises.

I. PC based session	(Weightage 40%)	24 periods
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A. English Language Lab (18 Periods)

1. LISTENING COMPREHENSION: (6)

Listening and typing – Listening and sequencing of sentences – Filling in the blanks -Listening and answering questions.

2. READING COMPREHENSION: (6)

Filling in the blanks - Close exercises – Vocabulary building - Reading and answering questions.

3. SPEAKING: (6)

Phonetics: Intonation – Ear training - Correct Pronunciation – Sound recognition exercises – Common Errors in English.

Conversations: Face to Face Conversation – Telephone conversation – Role play activities (Students take on roles and engage in conversation)

B. VIEWING AND DISCUSSING AUDIO-VISUAL MATERIALS (6 periods)

(Samples are available to learn and practice)

1. RESUME / REPORT PREPARATION / LETTER WRITING (1)

Structuring the resume / report - Letter writing / Email Communication - Samples.

2. PRESENTATION SKILLS: (1)

Elements of effective presentation – Structure of presentation - Presentation tools – Voice Modulation – Audience analysis - Body language – Video samples

3. SOFT SKILLS: (2)

Time management – Articulateness – Assertiveness – Psychometrics – Innovation and Creativity - Stress Management & Poise - Video Samples

4. **GROUP DISCUSSION:** (1)
Why is GD part of selection process ? - Structure of GD – Moderator – led and other GDs - Strategies in GD – Team work - Body Language - Mock GD -Video samples
5. **INTERVIEW SKILLS:** (1)
Kinds of interviews – Required Key Skills – Corporate culture – Mock interviews-Video samples.

II. Practice Session	(Weightage – 60%)	24 periods
1. Resume / Report Preparation / Letter writing:	Students prepare their own resume and report.	(2)
2. Presentation Skills:	Students make presentations on given topics.	(8)
3. Group Discussion:	Students participate in group discussions.	(6)
4. Interview Skills:	Students participate in Mock Interviews	(8)

REFERENCES:

1. Anderson, P.V, Technical Communication, Thomson Wadsworth , Sixth Edition, New Delhi, 2007.
2. Prakash, P, Verbal and Non-Verbal Reasoning, Macmillan India Ltd., Second Edition, New Delhi, 2004.
3. John Seely, The Oxford Guide to Writing and Speaking, Oxford University Press, New Delhi, 2004.
4. Evans, D, Decisionmaker, Cambridge University Press, 1997.
5. Thorpe, E, and Thorpe, S, Objective English, Pearson Education, Second Edition, New Delhi, 2007.
6. Turton, N.D and Heaton, J.B, Dictionary of Common Errors, Addison Wesley Longman Ltd., Indian reprint 1998.

LAB REQUIREMENTS:

1. Teacher console and systems for students.
2. English Language Lab Software
3. Career Lab Software

REQUIREMENT FOR A BATCH OF 60 STUDENTS

Sl.No.	Description of Equipment	Quantity required
1.	Server	1 No.
	o PIV system	
	o 1 GB RAM / 40 GB HDD	
	o OS: Win 2000 server	
	o Audio card with headphones (with mike)	
o JRE 1.3		
2.	Client Systems	60 No.
	o PIII or above	
	o 256 or 512 MB RAM / 40 GB HDD	
	o OS: Win 2000	

	<ul style="list-style-type: none"> ○ Audio card with headphones (with mike) ○ JRE 1.3 	
3.	Handicam Video Camera (with video lights and mic input)	1 No.
4.	Television - 29"	1 No.
5.	Collar mike	1 No.
6.	Cordless mikes	1 No.
7.	Audio Mixer	1 No.
8.	DVD Recorder / Player	1 No.
9.	LCD Projector with MP3 /CD /DVD provision for audio / video facility - Desirable	1 No.

EI2304

INDUSTRIAL INSTRUMENTATION LABORATORY

L T P C
0 0 3 2

OBJECTIVE

The training gained by the student in this area will be of immense help and ease for him in any industrial establishment.

1. Discharge coefficient of orifice plate
2. Calibration of pressure gauge
3. Torque measurement
4. Viscosity measurement
5. Vacuum pressure measurement
6. Level measurement using d/p transmitter
7. UV – Visible spectrophotometer
8. IR spectrophotometer
9. pH meter standardization and measurement of pH values of solutions
10. Measurements of conductivity of test solutions.

TOTAL : 45 PERIODS

DETAILED SYLLABUS:

1. DISCHARGE COEFFICIENT OF ORIFICE PLATE AIM

To find the discharge co-efficient of orifice plate.

EXERCISE

Find the discharge co-efficient C_d .

PROCEDURE

1. Open the outlet valve completely and switch on the motor.

2. Now open the inlet valve.
3. With a particular opening of the inlet valve note the reading on two times of manometer and compute the value of x .
4. Compute the actual discharge using the collecting tank and stop watch and the theoretical discharge.
5. Now change the opening of the inlet valves and note the reading of manometer and compare and discharge.
6. Calculate the value of C_d .

EQUIPMENT

1. Orifice meter – 1 No
2. Stopwatch – 1 No

2. CALIBRATION OF PRESSURE GAUGE

AIM

To calibrate the given pressure gauge using dead weight tester.

EXERCISE

Calibrate the pressure gauge and discuss the graphs (i) Actual pressure Vs true pressure (ii) Actual pressure Vs Error

PROCEDURE

1. A standard weight of 0.5 Kg/cm^2 is kept on the piston plate form.
2. Pressure is applied to the chamber containing oil by rotating the hand operated wheel in the anti clock wise direction.
3. This is continued until piston carrying weight shows a list.
4. In the movement the pressure acts equally on the piston as well as on the gauge.
5. The reading shown by the gauge is taken as actual reading.
6. The same procedure is repeated for increasing weights on the platform in steps of 0.5 Kg/cm^2 and actual reading shown by the gauge is noted down.
7. Graphs are drawn between
 - i. Actual pressure Vs true pressure.
 - ii. Actual pressure Vs Error.

EQUIPMENT

1. Dead weight tester - 1 No
2. Pressure gauge and standard weight - 1 No

3. TORQUE MEASUREMENT

AIM

To determine the due to dead weights using strain torsion meter and to determine the unknown weight.

EXERCISE

Find the % error of the torque measurement.

PROCEDURE

1. Connect the strain gauge torsion meter to the power supply.
2. Now change or hanger is fixed to the shaft, the torque is to subject.
3. Now keep the dead weights in the hanger gently.
4. Note the indicated torque value from the strain gauge torsion indicator.

5. Repeat the same for different weights (say 1Kg, 2Kg,) and tabulate the readings.
6. Now repeat the same procedure for the given unknown weight.
7. The unknown weight is interpreted from graph.

EQUIPMENT

1. Strain gauge torsion meter – 1 No
2. Dead weight – 1 No

4. MEASUREMENT OF VISCOSITY USING SAYBOLT VISCOMETER

AIM

To measure the viscosity using saybolt viscometer.

EXERCISE

Measure the viscosity using saybolt viscometer and draw the graph between voltage on x-axis and dynamo viscosity on y-axis.

PROEDURE

1. Viscosity determination shall be done in room free from dust rapid changes in temperature.
2. The oil in the cup and allow it to drain.
3. Pour oil in the cup and allow it to drain.
4. The cork stopper should be installed at the lower end of the tube.
5. The cork should be tight enough to prevent escape of oil.
6. Since the oil should be stirred well until a constant temperature is maintained both in the water and the oil.
7. After thermal equilibrium has been obtained.
8. Remove the thermometer from the oil bath.
9. 60ml of flask should be kept in position to collect oil from the tube.
10. Open the cork and start the stopwatch.
11. Record the time for the fall of 60mm of oil.
12. Vary the temperature of oil using temperature controller record the actual temperature.
13. Draw the graph between voltage on x-axis and dynamo viscosity on y-axis.

EQUIPMENT

1. Thermometer – 1 No
2. Stop watch – 1 No
3. 60ml flask – 1 No
4. Water – 1 No

5. VACUUM PRESSURE MEASUREMENT

AIM

To study the vacuum pressure gauge setup and measure the unknown vacuum pressure.

EXERCISE

- i. Maintain the vacuum pressure in the cylinder and switch on the vacuum pressure transmitter setup.
- ii. Measure the output voltage in Volts for the corresponding vacuum pressure in mbars.
- iii. Vary the vacuum pressure in cylinder and follow the step 2 for different values.
- iv. Draw the graph between output voltage Vs. vacuum pressure in mbars.

EQUIPMENT

Vacuum pressure setup
Vacuum pressure transmitter

Voltmeter

6. LEVEL MEASUREMENT USING DPT

AIM

To measure the level of liquid in the tank with the differential pressure transmitter and to calibrate the zero and span of the level interns of 4-20 mA.

EXERCISE

Measure the liquid level and calibrate it interms of 4-20 mA.

PROCEDURE

- a) Weight the empty container and calibrate the daters level to 4mA.
- b) Fill the container with the water and calibrate the full level to 20mA.
- c) Now perform the experiment in the ascending order in steps of 5cms.
- d) Repeat the same procedure for the descending order.
- e) Tabulate the readings.
- f) Draw the hastenis

EQUIPMENT

1. DPT - 1 No
2. Container - 1 No

7. UV-VISIBLE SPECTROPHOTOMETER

AIM

To find out the absorbance, % of transmittance and concentration for a given test solution, using UV spectrophotometer.

EXERCISE

Find out the absorbance, % of transmittance and concentration of the given Test solutions.

PROCEDURE

1. Switch on the UV-spectrophotometer.
2. Switch on the lamp by electing the names of rating disc.
3. Place the reference solution in the first column of rotating disc.
4. Use any other column to place the test solution.
5. Select the operating mode. There are 4 types of operating modes:
 - i. Single wavelength
 - ii. Multiple wavelength
 - iii. Scanning mode
 - iv. Time scan mode
6. Select the mode. The 3 parameters to be measures are absorbance, % of transmittance and concentration for a given test solution.
Note down the result from the 1st parameter.

EQUIPMENT

1. UV spectrophotometer – 1 No.
2. Currettes

8. IR – SPECTROPHOTOMETER

AIM

To measure and analyze the absorbance, percentage transmission concentration of the given samples using IR spectroscopy

EXERCISE

*wait for 30 minutes for IR source to be operated, then take the readings.

For IR wavelength is ABOVE 300nm :

Place reference sample in CELL No 2.

Place the sample to be analyzed in cell NO 1 or 3 or 4 or 5

Single wave length:

As the name suggests, this mode is used to take readings at one wave length. Depends on the absorbance mode, transmittance mode, concentration mode the data will be displayed on the monitor. Each subsequent data can be transferred just by pressing Key of 117. After completion of the data transfer, Press ESC key to stop the reception.

Multi wavelength analysis:

This mode is similar to single wave length except that it takes readings at more than one wavelength. With this mode, readings can be taken at minimum 2 discrete readings and maximum 8 discrete wavelength. Any 8 wavelength can be selected in the range 200nm to 1000nm. Note the maximum wavelength of absorption.

EQUIPMENT

1. IR spectrophotometer sl-117
2. cuvette
3. Solution
4. Printer

9. Ph – Meter Measurement of p^h - value of Test Solutions

AIM

To measure the P_H values of the test solutions using pH-meter.

EXERCISE

Find the pH values of the test solutions.

PROCEDURE

1. Switch on the P_H meter
2. Connect the glass electrode to the P_H -meter
3. Take distilled water in a beaker and insert electrode in the beaker
4. The P_H meter should show approximately test solutions. If Acidic than the P_H is < 7 and if alkaline than the $P_H > 7$

EQUIPMENT

1. pH meter – 1 No.
2. Test solutions – few types
3. Beaker – 2 Nos.
4. Stand – 1 No.

10. MEASUREMENTS OF CONDUCTIVITY OF TEST SOLUTIONS.

AIM

To measure the conductivity of the given solution.

EXERCISE

- (i) Solution under test is taken in a beaker.
- (ii) Electrode is immersed into the solution
- (iii) The electrode terminal is connected to display unit.
- (iv) Digital display shows the conductivity of the given solution in mho
- (v) Repeat the procedure for different samples.
- (vi) Switch on the supply.

EQUIPMENT

- (i) Solution under test.
- (ii) Conductivity electrode
- (iii) Conductivity meter setup with display.

EI2351

MODERN ELECTRONIC INSTRUMENTATION

**L T P C
3 0 0 3**

AIM

To provide adequate knowledge in digital instruments, display devices and virtual instrumentation.

OBJECTIVES

- i. To make the students to gain a clear knowledge of the basics of digital instruments and measurement techniques.
- ii. To have an adequate knowledge in various display and recording devices.
- iii. To have an elaborate study of communication standards
- iv. To have a detailed study of virtual instrumentation and its applications.

UNIT I DIGITAL INSTRUMENTS

9

Digital voltmeters and multimeters –Microprocessor based DMM with auto ranging and self diagnostic features – Digital IC tester –Frequency, period, time interval and pulse width measurement.

UNIT II DISPLAY AND RECORDING DEVICES

9

Cathode ray oscilloscope – General purpose and advanced types – Sampling and storage scopes – Wave analyzers – Signal and function generators – Distortion factor meter – Q meter – Seven segment and dot matrix display – X-Y recorders – Magnetic tape recorders – Digital recording and data loggers.

UNIT III RS 232 AND RS 485

9

Modern instrumentation and control systems – OSI model – EIA 232 Interface standard - EIA 485 Interface standard - EIA 422 Interface standard – 20 mA current loop – Serial Interface converters.

UNIT IV VIRTUAL INSTRUMENTATION

9

Virtual instrumentation – Definition, flexibility – Block diagram and architecture of virtual instruments – Virtual instruments versus traditional instruments – Review of software in virtual instrumentation - VI programming techniques – VI , sub VI, loops and charts ,arrays, clusters and graphs, case and sequence structures, formula nodes, string and file input / output.

UNIT V DATA ACQUISITION CARDS

9

DAQ cards for VI applications – Requirements – DAQ modules with serial communication – Design of digital voltmeters with transducer input – Design of ON/OFF controller for temperature control applications.

TOTAL : 45 PERIODS

TEXT BOOKS:

1. Chris Nadovich, 'Synthetic Instruments Concepts and Applications', Elsevier, 2005.
2. Rick Bitter, Taqi Mohiuddin and Matt Nawrocki, 'Labview Advanced Programming Techniques', CRC Press, Second Edition, 2007.
3. S. Gupta and J.P. Gupta, 'PC interfacing for data acquisition and process Control', second Edition, Instrument Society of America, 1994.
4. Kalsi H.S., "Electronic Instrumentation", Second Edition, Tata Mc Graw Hill Company, New Delhi, 2004.
5. Sawhney A.K., "A course in Electrical and Electronic Measurement and Instrumentation", Dhanpat Rai and sons, New Delhi, 2003.

REFERENCES:

1. Rahman Jamal and Herbert Picklik, LabVIEW – Applications and Solutions, National Instruments Release ISBN 0130964239.Rah
2. William Buchanan 'Computer Busses', CRC Press, 2000.
3. Rangan C.S., Sharma G.R., Mani V.S.V., "Instrumentation devices and Systems", Tata Mc Graw Hill Company, New Delhi,.2002.
4. Joseph J Carr, "Elements of Electronic Instrumentation and Measurement", Third Edition, Pearson Education, 2003.
5. David A. Bell, "Electronic Instrumentation and measurements", Second Edition, Prentice Hall of India, New Delhi, 2003.
6. Gupta J.B., "A course in Electrical and Electronic Measurement and Instrumentation", 12th Edition, Katson Publishing House, 2003.

EI2352

PROCESS CONTROL

L T P C
3 1 0 4

AIM

To provide basic knowledge of controllers, find control elements and the processes.

OBJECTIVES

- i. To study the basic characteristics of first order and higher order processes.
- ii. To get adequate knowledge about the characteristics of various controller modes and methods of tuning of controller.
- iii. To study about various complex control schemes.
- iv. To study about the construction, characteristics and application of control valves.

To study the five selected unit operations and a case study of distillation column control

UNIT I INTRODUCTION

9

Need for process control – mathematical model of first order level, pressure and thermal processes – higher order process – interacting and non-interacting systems – continuous and batch processes – self-regulation – servo and regulator operations.

UNIT II CONTROL ACTIONS AND CONTROLLERS

9

Basic control actions – characteristics of on-off, proportional, single-speed floating, integral and derivative control modes – P+I, P+D and P+I+D control modes – pneumatic and electronic controllers to realize various control actions.

- UNIT III OPTIMUM CONTROLLER SETTINGS 9**
 Evaluation criteria – IAE, ISE, ITAE and $\frac{1}{4}$ decay ratio – determination of optimum settings for mathematically described processes using time response and frequency response – Tuning – Process reaction curve method – Ziegler Nichols method – Damped oscillation method.
- UNIT IV MULTILoop CONTROL 9**
 Feed-forward control – ratio control- cascade control – inferential control – split-range control – introduction to multivariable control – examples from distillation column and boiler systems.
- UNIT V FINAL CONTROL ELEMENT 9**
 I/P converter – pneumatic and electric actuators – valve positioner – control valves – characteristics of control valves – inherent and installed characteristics – valve body – commercial valve bodies – control valve sizing – cavitation and flashing – selection criteria.

45 T = 15 TOTAL : 60 PERIODS

TEXT BOOKS

1. Stephanopoulos, G, Chemical Process Control, Prentice Hall of India, New Delhi, 1990.
2. Eckman. D.P., Automatic Process Control, Wiley Eastern Ltd., New Delhi, 1993.

REFERENCES

1. Pollard A.Process Control, Heinemann educational books, London, 1971.
2. Harriott. P., Process Control, Tata McGraw-Hill Publishing Co., New Delhi, 1991.

EI2353

DIGITAL SYSTEM DESIGN

**L T P C
3 0 0 3**

AIM

The course is designed to introduce the fundamental concepts and design of digital system.

OBJECTIVES

- To introduce the most common digital logic families.
- To provide introduction to programmable logic devices such as PLA, PAL, FPGA, CPLD etc.
- To provide introduction to Digital Memories. Such as ROM, RAM, SRAM, etc.
- To discuss case studies on Digital System design.

UNIT I DIGITAL LOGIC FAMILIES 9

TTL, CMOS, NMOS, Dynamic MOS , ECL, I²L, Operating conditions, Parameters, Interpreting data sheets. Power supply grounding considerations for digital ICs, TTL – to – CMOS Interface, CMOS – to – TTL interface.

UNIT II PROGRAMMABLE LOGIC DEVICES 9

Programmable logic Arrays, Programmable array logic, Realizing logic function using Multiplexers, Decoders, ROM, PLA, PAL. Design of sequential Networks using PAL, PLA – Programmable Gate arrays – FPGA – CPLD.

UNIT III DIGITAL MEMORIES 9

The role of Memory in a system – memory types and terminology – ROM – types of ROM – RAM – SRAM – DRAM – Expanding word size and capacity – Applications.

UNIT IV DIGITAL SYSTEM DESIGN CASE STUDIES 9

Multiplexing displays – Frequency counters – Time measurement – Digital voltmeter – PRBS generator – Interfacing with flash memory.

UNIT V DESIGN FOR TESTABILITY**9**

Testability – Ad hoc design for testing techniques – controllability and observability by means of scan registers – Generic scan based design – Board level and system level DFT approaches.

TOTAL: 45 PERIODS**TEXT BOOKS:**

1. Charles H.Roth, 'Fundamentals Logic Design', Jaico Publishing, IV edition, 2002.
2. Donald. P. Leach, Albert Paul Malvino, Goutam Suha, 'Digital Principles and Applications' Tata McGraw – Hill , Sixth edition .
3. Miron Abramonici, Melvin. A. Rrewer, Arthur.D. Friedman, Digital system testing and testable design, Jaico publishing house.

REFERENCES:

1. Theodore. F. Bogart, 'Introduction to Digital Circuits', McGraw – Hill International edn.1992
2. Ronald J.Tocci, Neal .S. Widmer, 'Digital System Principles and Applications', Pearson Education, 8th edition, Asia, 2002.
3. On demand public key management for wireless Ad Hoc networks.
4. Efficient hybrid security mechanisms for heterogeneous sensor networks.
5. Performance analysis of Handoff techniques based on Mobile Ip, TCP – migrate and SIP.

EC2361**DIGITAL SIGNAL PROCESSING****L T P C****3 1 0 4****AIM**

To introduce the concept of analyzing discrete time signals & systems in the time and frequency domain.

OBJECTIVES

- To classify signals and systems & their mathematical representation.
- To analyse the discrete time systems.
- To study various transformation techniques & their computation.
- To study about filters and their design for digital implementation.
- To study about a programmable digital signal processor & quantization effects.

UNIT I INTRODUCTION**9**

Classification of systems: Continuous, discrete, linear, causal, stable, dynamic, recursive, time variance; classification of signals: continuous and discrete, energy and power; mathematical representation of signals; spectral density; sampling techniques, quantization, quantization error, Nyquist rate, aliasing effect. Digital signal representation.

UNIT II DISCRETE TIME SYSTEM ANALYSIS**9**

Z-transform and its properties, inverse z-transforms; difference equation – Solution by z-transform, application to discrete systems - Stability analysis, frequency response – Convolution – Fourier transform of discrete sequence – Discrete Fourier series.

UNIT III DISCRETE FOURIER TRANSFORM & COMPUTATION**9**

DFT properties, magnitude and phase representation - Computation of DFT using FFT algorithm – DIT & DIF - FFT using radix 2 – Butterfly structure.

UNIT IV DESIGN OF DIGITAL FILTERS**9**

FIR & IIR filter realization – Parallel & cascade forms. FIR design: Windowing Techniques – Need and choice of windows – Linear phase characteristics. IIR design: Analog filter design - Butterworth and Chebyshev approximations; digital design using impulse invariant and bilinear transformation - Warping, prewarping - Frequency transformation.

UNIT V DIGITAL SIGNAL PROCESSORS**9**

Introduction – Architecture – Features – Addressing Formats – Functional modes - Introduction to Commercial Processors

L = 45 T = 15 TOTAL : 60 PERIODS**TEXT BOOKS**

1. J.G. Proakis and D.G. Manolakis, 'Digital Signal Processing Principles, Algorithms and Applications', Pearson Education, New Delhi, 2003 / PHI.
2. S.K. Mitra, 'Digital Signal Processing – A Computer Based Approach', Tata McGraw Hill, New Delhi, 2001.

REFERENCES

1. Alan V. Oppenheim, Ronald W. Schafer and John R. Buck, 'Discrete – Time Signal Processing', Pearson Education, New Delhi, 2003.
2. Emmanuel C Ifeachor and Barrie W Jervis , "Digital Signal Processing – A Practical approach" Pearson Education, Second edition, 2002.
3. Steven W. Smith, "The Scientist and Engineer's Guide to Digital Signal Processing", Second Edition, California Technical Publishing San Diego, California.(www.DSPguide.com)
4. B. Venkataramani, M. Bhaskar, 'Digital Signal Processors, Architecture, Programming and Applications', Tata McGraw Hill, New Delhi, 2003.

CS2364**EMBEDDED SYSTEM****L T P C
3 0 0 3****AIM**

To understand the basic concepts of embedded system design and its applications to various fields.

OBJECTIVES

To provide a clear understanding of

- Embedded system terminologies and its devices.
- Various Embedded software Tools
- Design and architecture of Memories.
- Architecture of processor and memory organizations.
- Input/output interfacing
- Various processor scheduling algorithms.
- Basics of Real time operating systems.
- Introduction to PIC and its applications

UNIT I INTRODUCTION TO EMBEDDED SYSTEMS**9**

Introduction to embedded real time systems – The build process for embedded systems – Embedded system design process-Embedded computory applications-Types of memory – Memory management methods.

OBJECTIVE

To understand the concepts of various modulation techniques and to have an indepth knowledge of various signal processing techniques.

1. Generation and Detection of Amplitude Modulation
2. Generation of Frequency Modulation and its Detection
3. Generation and Detection of PAM
4. Generation of BFSK and its Detection
5. Generation of standard inputs using simulation package
6. Analysis of Linear Systems [with Convolution and Deconvolution Operation]
7. FIR Filter Design (any one Technique)
8. IIR Filter Design (any one Technique)
9. Implementation of FFT algorithm
- 10 Implementation of Interpolation and Decimation function

TOTAL : 45 PERIODS

DETAILED SYLLABUS**1. GENERATION AND DETECTION OF AMPLITUDE MODULATION****AIM:**

To study the working concept of Amplitude Modulation and detection

OBJECTIVE:

1. To study the modulation of message signal using high frequency carrier
2. To study the detection of message signal from the modulated signal

EXERCISE**MODULATION**

1. Construct a modulation circuit using discrete components.
2. Using signal generators, give message signal and high frequency carrier.
3. Using CRO, observe E_{max} and E_{min} of modulated wave and find out modulation index

DETECTION

1. Construct demodulated circuit
2. Give modulated wave as input
3. Measure the amplitude and frequency of modulating signal

EQUIPMENTS REQUIRED

1. Signal Generators
2. Power supply
3. CRO

2. GENERATION OF FREQUENCY MODULATION AND ITS DETECTION**AIM:**

To understand the working concept of Frequency Modulation and Detection

OBJECTIVE:

1. To study Frequency Modulation for the given modulated signal
2. To study Frequency Detection from the Frequency Modulated signal

EXERCISE

MODULATION

1. Construct a Frequency Modulation Circuit
2. Give Modulating Signal and Carrier using Signal Generators
3. Using CRO, Observe Frequency Deviation and calculate Modulation Index

DETECTION

1. Give Frequency Modulated Signal as input to the detector circuit
2. Using CRO, Observe Frequency and Amplitude of modulating signal.

EQUIPMENTS REQUIRED

1. Signal Generators
2. CRO
3. Power Supply

3. GENERATION AND DETECTION OF PAM

AIM:

To study the working concept of PAM and its detection

OBJECTIVE

1. To study PAM for the given Message Signal using Pulse train
2. To study the detection of message signal from the PAM Signal

EXERCISE

MODULATION

1. Construct a circuit using discrete components
2. Give analog message signal, pulse train carrier using signal generators
3. Using CRO, Observe the amplitude of the pulses of PAM Signal

DETECTION

1. Give PAM Signal to the detection circuit
2. Observe the Amplitude and Frequency of Message signal

EQUIPMENTS REQUIRED

1. CRO
2. Signal Generators
3. Power Supply

4. GENERATION AND DETECTION OF BFSK

AIM:

To study the concept of BFSK Generation and its Detection

OBJECTIVE

1. To study BFSK Concept for the given analog modulating Signal.
2. To Study the retrieved Modulating Signal from the BFSK Signal.

EXERCISE

MODULATION

1. Construct BFSK Circuit using discrete components

2. Using Signal Generators, Give Message input Pulse Carrier to the circuit
3. Observe the amplitude and difference frequencies of output pulse train

DETECTION

1. Construct a Detection Circuit
2. Give BFSK Signal to the circuit and observe the amplitude and frequency of output

EQUIPMENTS REQUIRED

1. CRO
2. Signal Generators
3. Power supply

5. GENERATION OF STANDARD TEST INPUTS USING SIMULATE PACKAGE

AIM:

To generate different signals (Signals and Sequences) Using MATLAB

OBJECTIVE:

1. To Generate Signals (Sine Wave, Exponential Wave, Sawtooth Wave)
2. To generate sequences (Impulse sequence, Step Sequence)

EXERCISE

1. Generate Signals using Sine, Exponential, Step Functions
2. Generate Sequences such as Impulse Sequences, Step Sequences using Expression Ex. $\cos(2\pi t)$

SOFTWARE REQUIRED:

MATLAB

6. ANALYSIS OF LINEAR SYSTEM [WITH CONVOLUTION AND DECONVOLUTION OPERATION]

AIM:

To Study Linear Convolution of two sequences

OBJECTIVE:

1. To analyze Convolution and deconvolution of two sequences using CONV, DECONV Functions

EXERCISE:

1. Generate Convolved Sequences using CONV function
2. Generate Convolved output using FFT.

7. FIR FILTER DESIGN

AIM:

To Design FIR Low Pass, High Pass, Band Pass filters using MATLAB

OBJECTIVE:

1. To design FIR Low Pass, High Pass, Band Pass filters using Windowing Technique (Rectangular Window)

EXERCISE

1. Get the Passband and Stopband ripples
2. Get the Passband and Stopband edge frequencies
3. Get the sampling Frequency
4. Calculate the order of the filter
5. Find the window coefficients

6. Draw the magnitude and phase responses

MATLAB FUNCTIONS

fir1, freqz

8. IIR Filter Design (any one technique)

AIM:

To design a IIR Filter

OBJECTIVE

To design Butterworth IIR Filter using MATLAB

EXERCISE:

1. Get the Passband and Stopband ripples
2. Get the Passband and Stopband edge Frequencies
3. Get the sampling Frequency
4. Calculate the order of the filter
5. Find the filter coefficients
6. Draw the magnitude and phase responses

MATLAB FUNCTIONS

buttord, butter, freqz

9. IMPLEMENTATION OF FFT ALGORITHM

AIM:

To find out FFT of the given sequence

OBJECTIVE

1. To find out FFT of the given Sequence using FFT function

EXERCISE

1. Find out FFT of the sequence using FFT function

MATLAB FUNCTION

FFT

10 IMPLEMENTATION OF INTERPOLATION AND DECIMATION FUNCTION

AIM:

To implementation of interpolate and decimate the given Signal

OBJECTIVE

To interpolate and decimate the given signal

EXERCISE

Find out interpolation and Decimation of given signal using interpolate and decimate function

MATLAB FUNCTION

Interpolate, Decimate

OBJECTIVE

To experimentally verify the process control concepts on the selected process control loops.

1. Operation of interacting and non-interacting systems
2. Responses of different order processes with and without transportation lag
3. Response of on-off controller
4. Response of P+I+D controller
5. Characteristics of control valve with and without positioner
6. Operation of on-off controlled thermal process
7. Closed loop response of flow control loop
8. Closed loop response of level control loop
9. Closed loop response of temperature control loop
10. Closed loop response of pressure control loop
11. Tuning of controllers
12. Study of complex control system (ratio / cascade / feed forward)

TOTAL = 45 PERIODS

1. STUDY OF INTERACTING AND NON- INTERACTING SYSTEMS**AIM**

To study the operation of interacting and non- interacting systems

EXERCISE

1. Connect the two tank system (Level process) in series (as non- interacting system)
2. Check whether level in tank is affected due to changes made in the second tank.
3. Connect the two tank system in series (as interfacing as system).
4. Check whether level in tank 1 is affected due to changes made in the second tank.
5. Determine the transfer function of individual and overall system.

EQUIPMENT

1. Two tank system with provision for making them as interfacing and non- interfacing.
– 1 No
2. Level transmitters – 1 No
3. Recorder – 1 No

2. RESPONSE OF DIFFERENT ORDER PROCESSES WITH AND WITHOUT TRANSPORTATION DELAY

AIM

To determine the transient response of a first order process with and without transportation delay and second order process with and without transportation delay to step change in input.

EXERCISE

1. Record the transient response to a step change of first order process and second order process (Level or thermal (or) any process) with and without transportation lag.
2. Calculate the process gain, time constant and dead time of the process from the step response.

EQUIPMENT

1. Two tank system with provision for transportation delay (Non – interacting process)
2. Level transmitter – 1 No
3. Recorder – 1 No

3. RESPONSE OF P+I+D CONTROLLER**AIM**

To investigate the operation of an electronic controllers with P, P+I and P+I+D action.

EXERCISE

1. Plot the response of P, P+I, P+D and P+I+D controllers to step and ramp inputs.
2. Determine
3. the calibration of the proportional, Integral and derivative adjustments.

EQUIPMENT

1. Electronic PID controller – 1 No
2. Source for generating step and ramp inputs – 1 No
3. Recorder – 1 No
4. Digital Multimeter – 1 No

4. CHARACTERISTICS OF CONTROL VALVE WITH AND WITHOUT VALVE POSITIONER**AIM**

To determine the flow – lift characteristics (Internet / Installed) of a control valve equipped with and without valve positioner.

EXERCISE

1. Plot the flow – lift characteristics of the given valve without positioner keeping
 - (i) Constant ΔP
 - (ii) Variable ΔP
2. Compute the valve gain at different operating points.
3. Plot the flow – lift characteristics of the given with positioner keeping.
 - i. Constant ΔP
 - ii. Variable ΔP
4. Compute the valve gain at different operating points.

EQUIPMENT

1. Control valve trainer (with position for varying ΔP across the valve) - 1 No
2. Flowmeter - 1No

5. CLOSED LOOP RESPONSE OF FLOW CONTROL LOOP

AIM

To obtain the closed loop response of flow control loop for servo and regulator Operation.

EXERCISE

1. Closed – loop connection is made in the flow process station.
2. The flow controller (P+I) is tuned using any one of the tuning techniques.
3. The response of the control loop is obtained for changes in the set point.
4. The response of the control loop is obtained for changes in the load variable.
5. The step 3 and 4 are repeated for different controller modes and settings.

EQUIPMENT

1. Flow process station with all accessories - 1 No
2. Analog / Digital PID controller - 1 No
3. Recorder - 1 No

6. CLOSED LOOP RESPONSE OF LEVEL CONTROL LOOP

AIM

To obtain the closed loop response of level control loop for servo and regulator operation.

EXERCISE

1. Closed loop connection is made in the level process station.
2. The level controller (P+I) is tuned using any one of the tuning techniques.
3. The response of the control loop is obtained for changes in the set point.
4. The response of the control loop is obtained for changes in the load variable.
5. The step 3 and step 4 are repeated for different controller modes and settings.

EQUIPMENT

1. Level process station with all accessories - 1 No
2. Analog / Digital PID controller - 1 No
4. Recorder - 1 No

7. CLOSED LOOP RESPONSE OF TEMPERATURE CONTROL LOOP

AIM

To obtain the closed loop response of temperature control loop for servo and regulator operation.

EXERCISE

1. Closed-loop connection is made in the temperature process station.
2. The temperature controller (P+I+D) is tuned using any one of the tuning techniques.
3. The response of the control loop is obtained for changes in the set point.
4. The response of the control loop is obtained for changes in the load variable.
5. The step 3 and 4 are repeated for different controller modes and settings.

EQUIPMENT

1. Temperature process station with all accessories - 1 No
2. Analog / Digital PID controller - 1 No
3. Recorder - 1 No

8. CLOSED LOOP RESPONSE OF PRESSURE CONTROL LOOP

AIM

To obtain the closed loop response of pressure control loop for servo and regulator operation.

EXERCISE

1. Closed – loop connection is made in the pressure process station.
2. The pressure controller (P+I) is tuned using any one of the tuning techniques.
3. The response of the control loop is obtained for changes in the set point.
4. The response of the control loop is obtained for changes in the load variable.
5. The step 3 and 4 are repeated for different controller modes and settings.
- 6.

EQUIPMENT

1. Pressure process station with all accessories - 1 No
2. Analog / Digital PID controller - 1 No
3. Recorder - 1 No

9. TUNING OF PID CONTROLLER**AIM**

To determine the controller settings of a given process using two popular tuning techniques.

EXERCISE

1. Plot the process reaction curve for the given process (higher order process)
2. From the reaction curve, calculate the process gain, time constant and dead time using the above process parameters calculate the K_c , T_i , T_d values using the appropriate thumb rules.
3. Conduct the closed loop test as per Z-N method [continuous cycling method] and determine the ultimate gain (K_u) and ultimate period (P_u), calculate the controller parameters (K_c , T_i , T_d) using Ziegler Nichol's closed loop tuning approach.

EQUIPMENT

1. Process control trainer / real time process (level / thermal process) - 1 No
2. Recorder - 1 No
3. PID controller - 1 No

10. RESPONSE OF CASCADE CONTROL SYSTEM**AIM**

To determine the closed loop performance of a cascade control system and compare it with that of conventional control system.

EXERCISE

1. The secondary and primary controllers are tuned using any one of the tuning techniques.
2. Obtain the closed loop response of cascade control system with the load variable entering the inner loop.
3. Obtain the closed loop regulating response with conventional control system.
4. Compare the performance of conventional control system and cascade control system internal of peak overshoot, setting time, I&E etc

EQUIPMENT

1. Cascade control system with flow as inner variable and liquid level as outer variable with following accessories.

2. Level transmitter - 1 No
3. Flow transmitter - 1 No
4. Control valve - 1 No
5. Analog / Digital PID controller - 1 No
6. Recorder - 1 No

EI2357

VIRTUAL INSTRUMENTATION LAB

**L T P C
0 0 3 2**

1. Creating Virtual Instrumentation for simple applications
2. Programming exercises for loops and charts
3. Programming exercises for clusters and graphs.
4. Programming exercises on case and sequence structures, file Input / Output.
5. Data acquisition through Virtual Instrumentation.
6. Developing voltmeter using DAQ cards.
7. Developing signal generator using DAQ cards.
8. Simulating reactor control using Virtual Instrumentation.
9. Real time temperature control using Virtual Instrumentation.
10. Real time sequential control of any batch process.

LABORATORY REQUIREMENTS FOR BATCH OF 30 STUDENTS

SL.NO.	SPECIFICATIONS	QTY
1.	Laboratory Virtual Instrumentation Engineering Software Package	30 users License
2.	PCI /USB DAQ Boards	2 Nos.
3.	Temperature Control Test Rig using Laboratory Virtual Instrumentation Engineering Software Package and Hardware Models	1 No.
4.	Sequential Control using Laboratory Virtual Instrumentation Engineering Software Package and Hardware Models.	1 No.

EI2401

INDUSTRIAL DATA NETWORKS

**L T P C
3 0 0 3**

AIM

To learn more about the industrial data communication protocols.

OBJECTIVES:

- i. To understand basic data networks
- ii. To learn the basic of inter networking
- iii. To have adequate knowledge in various communication protocol.
- iv. To study industrial data communication.

UNIT I	DATA NETWORK FUNDAMENTALS	9
Network hierarchy and switching – Open System Interconnection model of ISO– Data link control protocol: - HDLC – Media access protocol – Command/response – Token passing – CSMA/CD, TCP/IP		
UNIT II	INTER NETWORKING	9
Bridges – Routers – Gateways –Standard ETHERNET and ARCNET configuration- special requirement for networks used for control.		
UNIT III	HART AND FIELDBUS	9
Introduction- Evolution of signal standard – HART communication protocol – Communication modes – HART networks – HART commands – HART applications. Fieldbus: – Introduction – General Fieldbus architecture – Basic requirements of Field bus standard – Fieldbus topology – Interoperability – Interchangeability – Introduction to OLE for process control (OPC).		
UNIT IV	MODBUS AND PROFIBUS PA/DP/FMS AND FF	9
MODBUS protocol structure – function codes – troubleshooting Profibus: Introduction – profibus protocol stack – profibus communication model – communication objects – system operation – troubleshooting – review of foundation field bus.		
UNIT V	INDUSTRIAL ETHERNET AND WIRELESS COMMUNICATION	9
Industrial Ethernet : Introduction – 10Mbps Ethernet, 100Mbps Ethernet. Radio and wireless communication : Introduction – components of radio link – the radio spectrum and frequency allocation – radio modems.		

TOTAL : 45 PERIODS

TEXT BOOKS

1. Steve Mackay, Edwin Wrijut, Deon Reynders, John Park, 'Practical Industrial Data networks Design, Installation and Troubleshooting', Newnes publication, Elsevier First edition, 2004.
2. William Buchanan 'Computer Busses', CRC Press, 2000.

REFERENCES

1. Andrew S. Tanenbaum, Modern Operating Systems, Prentice Hall of India Pvt. LTD, 2003
2. Theodore S. Rappaport, 'Wireless communication: Principles & Practice', 2nd Edition, 2001 Prentice Hall of India
3. Willam Stallings, ' Wireless Communcion & Networks' 2nd Edition, 2005, Prentice Hall of India

EI2402	LOGIC AND DISTRIBUTED CONTROL SYSTEM	L T P C
		3 0 0 3

AIM

To illustrate the concept of programmable logic controllers and distributed control system.

OBJECTIVES

- To give an introductory knowledge about PLC and the programming languages.
- To give adequate knowledge about of application of PLC.

- iii. To give basic knowledge in the architecture and local control unit of distributed control system.
- iv. To give adequate information in the interfaces used in DCS.
- v. To give basic knowledge about Computer Controlled Systems.

UNIT I PROGRAMMABLE LOGIC CONTROLLER 9

Evolution of PLC's – Components of PLC – Advantages over relay logic – Architecture of PLC– Programming devices - Discrete and Analog I/O modules – Programming languages — Ladder diagram – Programming timers and counters – Design of PLC.

UNIT II APPLICATIONS OF PLC 9

Instructions in PLC – Program control instructions, math instructions, sequencer instructions – Use of PC as PLC – Application of PLC – Case study of bottle filling system.

UNIT III COMPUTER CONTROLLED SYSTEMS 9

Basic building blocks of Computer controlled systems – SCADA – data Acquisition System – supervisory Control – Direct digital Control .

UNIT IV DISTRIBUTED CONTROL SYSTEM 9

DCS - Architectures – Comparison – Local control unit – Process interfacing issues – Communication facilities.

UNIT V INTERFACES IN DCS 9

Operator interfaces - Low level and high level operator interfaces – Operator displays - Engineering interfaces – Low level and high level engineering interfaces – General purpose computers in DCS.

TOTAL : 45 PERIODS

TEXT BOOKS

1. Petruzella, 'Industrial Electronics', McGraw Hill, Second edition, 1997.
2. Michael P. Lukas, 'Distributed Control System', Van Nostrand Reinhold Co., Canada, 1986.
3. John. W. Webb Ronald A Reis - Programmable Logic Controllers - Principles and Applications, Fourth edition, Prentice Hall Inc., New Jersey, 1998.

REFERENCES

1. T. Hughes, 'Programmable Logic Controllers', ISA press,2007.
2. Krishna Kant – Computer based Industrial Control, Prentice Hall, New Delhi, 1997.

EI2403

VLSI DESIGN

**L T P C
3 0 0 3**

AIM

To introduce the technology and concepts of VLSI.

OBJECTIVES

- To introduce MOS theory / Manufacturing Technology.
- To study inverter / counter logic / stick / machine diagram / sequential circuits.
- To study address / memory / arithmetic circuits.
- To introduce FPGA architecture / principles / system design.
- To get familiarised with VHDL programming behavioural/Structural/concurrent/ process.

UNIT I	BASIC MOS TRANSISTOR	9
Enhancement mode and Depletion mode – Fabrication (NMOS, PMOS, CMOS, BiCMOS) Technology – NMOS transistor current equation – Second order effects – MOS Transistor Model.		
UNIT II	NMOS AND CMOS INVERTER AND GATES	9
NMOS and CMOS inverter – Determination of pull up / pull down ratios – Stick diagram – lambda based rules – Super buffers – BiCMOS & steering logic.		
UNIT III	SUB-SYSTEM DESIGN AND LAYOUT	9
Structured design of combinational circuits – Dynamic CMOS & clocking – Tally circuits – (NAND-NAND, NOR-NOR and AOI logic) – EXOR structure – Multiplexer structures – Barrel shifter, high speed adder and multiplier circuits.		
UNIT IV	DESIGN OF COMBINATIONAL ELEMENTS AND REGULAR ARRAY LOGIC	9
NMOS PLA – Programmable Logic Devices - Finite State Machine PLA – Introduction to FPGA.		
UNIT V	VHDL PROGRAMMING	9
RTL Design – simulation and synthesis - Combinational logic – Types – Operators – Packages – Sequential circuit – Sub-programs – Test benches. (Examples: adders, counters, flipflops, FSM, Multiplexers / Demultiplexers).		

TOTAL : 45 PERIODS

TEXT BOOKS:

1. D.A.Pucknell, K.Eshraghian, 'Basic VLSI Design', 3rd Edition, Prentice Hall of India, New Delhi, 2003.
2. Rabey, J.M., Digital Integrated Circuits: A Design Perspective, Prentice Hall, 1955
3. Bhasker, J., VHDL Primer, Prentice Hall 1995

REFERENCES:

1. Eugene D.Fabricius, 'Introduction to VLSI Design', Tata McGraw Hill, 1990.
2. N.H.Weste, 'Principles of CMOS VLSI Design', Pearson Education, India, 2002.
3. Zainalatsedin Navabi, 'VHDL Analysis and Modelling of Digital Systems', 2nd Edition, Tata McGraw Hill, 1998.
4. Douglas Perry, 'VHDL Programming by example', Tata McGraw Hill, 3rd Edition, 2003

EI2404

FIBRE OPTICS AND LASER INSTRUMENTS

**L T P C
3 0 0 3**

AIM:

To contribute to the knowledge of Fibre optics and Laser Instrumentation and its Industrial and Medical Application.

OBJECTIVES:

- i. To expose the students to the basic concepts of optical fibres and their properties.
- ii. To provide adequate knowledge about the Industrial applications of optical fibres.
- iii. To expose the students to the Laser fundamentals.
- iv. To provide adequate knowledge about Industrial application of lasers.
- v. To provide adequate knowledge about holography and Medical applications of Lasers.

- UNIT I OPTICAL FIBRES AND THEIR PROPERTIES 9**
Principles of light propagation through a fibre - Different types of fibres and their properties, fibre characteristics – Absorption losses – Scattering losses – Dispersion – Connectors and splicers – Fibre termination – Optical sources – Optical detectors.
- UNIT II INDUSTRIAL APPLICATION OF OPTICAL FIBRES 9**
Fibre optic sensors – Fibre optic instrumentation system – Different types of modulators – Interferometric method of measurement of length – Moire fringes – Measurement of pressure, temperature, current, voltage, liquid level and strain.
- UNIT III LASER FUNDAMENTALS 9**
Fundamental characteristics of lasers – Three level and four level lasers – Properties of laser – Laser modes – Resonator configuration – Q-switching and mode locking – Cavity damping – Types of lasers – Gas lasers, solid lasers, liquid lasers, semiconductor lasers.
- UNIT IV INDUSTRIAL APPLICATION OF LASERS 9**
Laser for measurement of distance, length, velocity, acceleration, current, voltage and Atmospheric effect – Material processing – Laser heating, welding, melting and trimming of material – Removal and vaporization.
- UNIT V HOLOGRAM AND MEDICAL APPLICATIONS 9**
Holography – Basic principle - Methods – Holographic interferometry and application, Holography for non-destructive testing – Holographic components – Medical applications of lasers, laser and tissue interactive – Laser instruments for surgery, removal of tumors of vocal cards, brain surgery, plastic surgery, gynaecology and oncology.

TOTAL : 45 PERIODS

TEXT BOOKS:

1. J.M. Senior, 'Optical Fibre Communication – Principles and Practice', Prentice Hall of India, 1985.
2. J. Wilson and J.F.B. Hawkes, 'Introduction to Opto Electronics', Prentice Hall of India, 2001.

REFERENCES:

1. G. Keiser, 'Optical Fibre Communication', McGraw Hill, 1995.
2. M. Arumugam, 'Optical Fibre Communication and Sensors', Anuradha Agencies, 2002.
3. John F. Read, 'Industrial Applications of Lasers', Academic Press, 1978.
4. Monte Ross, 'Laser Applications', McGraw Hill, 1968

EI2405

VLSI LABORATORY

**L T P C
0 0 3 2**

OBJECTIVE

To study synthesis, simulation and schematic of various digital combinational circuits using FPGA on Xilinx simulator.

1. Study of Synthesis tools
 - Half and full adder.
 - Decoder – 2 x 4, 3 x 8
 - Priority encoder.
 - Ripple adder.

- 4 – Bit ripple counter.
- Code conversion.

All the above synthesis in three modeling styles - data flow, structural and behavioral

2. Study of Simulation using tools

- Half adder.
- Multiplexer – 2 x 1, 4 x 1
- Demultiplexer – 1 x 2, 1 x 4

All the above synthesis in three modeling styles - data flow, structural and behavioral

3. Study of Simulation using tools

- Flipflop – D, T
- Priority encoder.
- Ripple adder.
- 4 – Bit ripple counter.

All the above synthesis in three modeling styles - data flow, structural and behavioral

4. Study of development tool for FPGAs for schematic entry and verilog

- Full adder, half adder.
- Demultiplexer – 1 x 2, 1 x 4.

5. Design and simulation of pipelined serial and parallel adder to add/ subtract 8 number of size, 12 bits each in 2's complement.

6. Place and Root and Back annotation for FPGAs

7. Design and simulation of back annotated verilog files for multiplying two signed, 8 bit numbers in 2's complement.

8. Study of FPGA board and testing on board LEDs and switches using verilog code.

9. Design a Realtime Clock (2 digits, 7 segments LED displays each for HRS., MTS, and SECS.) and demonstrate its working on the FPGA board.

- to display binary number on the FPGA.

10. Design of traffic light controller using verilog tools .

- Movement of vehicles in any direction or pedestrian in any direction.

DETAILED SYLLABUS

1. STUDY OF SYNTHESIS TOOLS

Adder/encoder/decoder/counter/converter.

AIM

To study the synthesis of various combinational circuits on Xilinx(ISE9.1) tool using Hardware Descriptive Language(HDL).

EXERCISE

Write a HDL program in three modeling styles - data flow, structural and behavioral. using verilog and perform the synthesis of following combinational circuits

- Half and full adder.
- Decoder – 2 x 4, 3 x 8
- Priority encoder.
- Ripple adder.

- 4 – Bit ripple counter.
- Code conversion.

2. STUDY OF SIMULATION USING TOOLS

Adder/encoder/decoder/counter/multiplexer.

AIM

To study the simulation of various combinational circuits on Xilinx(ISE9.1) tool using Hardware Descriptive Language(HDL).

EXERCISE

Write a HDL program in three modeling styles - data flow, structural and behavioral. using verilog and simulate the following combinational circuits

- Half adder.
- Multiplexer – 2 x 1, 4 x 1
- Demultiplexer – 1 x 2, 1 x 4

3. STUDY OF SIMULATION USING TOOLS

Adder/encoder/decoder/counter/multiplexer.

AIM

To study the simulation of various combinational circuits on Xilinx(ISE9.1) tool using Hardware Descriptive Language(HDL).

EXERCISE

Write a HDL program in three modeling styles - data flow, structural and behavioral.using verilog and simulate the following combinational circuits

- Flipflop – D, T
- Priority encoder.
- Ripple adder.
- 4– Bit ripple counter

4. STUDY OF DEVELOPMENT TOOL FOR FPGAS FOR SCHEMATIC ENTRY AND VERILOG.

Adder /demultiplexer.

AIM

To draw the schematic and generate the verilog code.

EXERCISE

Draw the schematic using Xilinx(ISE9.1) tool and generate the verilog code for the following digital circuits and observe the simulated output

- Full adder, half adder.
- Demultiplexer – 1 x 2, 1 x 4.

5. DESIGN AND SIMULATION OF PIPELINED SERIAL AND PARALLEL ADDER TO ADD/SUBTRACT 8 NUMBER OF SIZE, 12 BITS EACH IN 2'S COMPLEMENT.

AIM

To design and simulate the pipelined 12 bit serial/parallel adder/subtractor.

EXERCISE

1. Write a verilog code to add/subtract 8 numbers of 12 bits each and verify the result after addition of every 2 numbers
2. Verify the result after adding all 8 numbers and simulate the output.

6. PLACE AND ROOT AND BACK ANNOTATION FOR FPGAS

AIM

To calculate the propagation delay produced by back annotated circuit from the already software designed circuit..

EXERCISE

Write the verilog code and develop the following combinational circuit using s/w in the design layout that is produced. Make the required changes in the routing as per the required propagation delay.

1. Multiplexer/Demultiplexer
2. Encoder/Decoder.

7. DESIGN AND SIMULATION OF BACK ANNOTATED VERILOG FILES FOR MULTIPLYING TWO SIGNED, 8 BIT NUMBERS IN 2'S COMPLEMENT. DESIGN MUST BE PIPELINED AND COMPLETELY RTL COMPLIANT

AIM

To design and simulate back annotated circuit using verilog code.

EXERCISE

Write the verilog code and develop the following combinational circuit using s/w in the design layout that is produced.

1. Pipelined structure of multiplication of two signed 8 bit numbers.
2. View RTL model

8. STUDY OF FPGA BOARD AND TESTING ON BOARD LEDS AND SWITCHES USING VERILOG CODE.

AIM

To study the FPGA board and testing on-board LED's and switches using ISE9.1 simulator.

EXERCISE

- Write the verilog code in three modeling styles - data flow, structural and behavioral to accept the input from switches and display the output in LEDs.
- Synthesis the program and view RTL model.
- Download the program into FPGA for testing on board LED's and switches.

9. Design a realtime clock (2 digits, 7 segments led displays each for hrs., mts, and secs.) and demonstrate its working on the fpga board. an expansion card is required for the displays.
- c. to display binary number on the FPGA.

AIM

To design and test the real time clock on FPGA board.

EXERCISE

- Write the verilog code to display hrs, mts and secs.
- Synthesis, simulate and download the program on the RTC board.

10. Design of traffic light controller using verilog tools .
- d. Movement of vehicles in any direction or pedestrian in any direction.

AIM

To design and test the traffic light controller using FPGA board.

EXERCISE

- Write the verilog code to implement different sequences of traffic light.
- Synthesis, simulate and download the program on the traffic light controller board.

REQUIREMENT FOR A BATCH OF 30 STUDENTS

S.No.	Name of the Equipments	Quantity
1.	Software – Simulation, Synthesis, back annotation, place & route Xlinx ISE (latest version)	5 User License (minimum)
2.	Spartan 2E boards	5 Nos.
3.	Spartan 3 AN boards	1 No.
4.	Add – on boards 1. Real - time clock 2. Traffic light control 3. LED displays with switches 4. I/O boards	1 No. 1 No. 1 No. 1 No.
5.	Multimeter	2 Nos.

OBJECTIVE

To have adequate knowledge in design of various signal conditioning circuits.

1. Design of Instrumentation amplifier.
2. Design of active filters.
3. Design of regulated power supply and design of V/I and I/V converters.
4. Design of linearizing circuits and cold – junction compensation circuit for thermocouples.
5. Design of signal conditioning circuit for strain gauge and RTD.
6. Design of orifice plate and rotameter.
7. Control valve sizing.
8. Design of PID controller (using operational amplifier and microprocessor)
9. Piping and Instrumentation Diagram – case study.
10. Preparation of documentation of instrumentation project and project scheduling (process flow sheet, instrument index sheet and instrument specifications sheet, job scheduling, installation procedures and safety regulations).

TOTAL : 45 PERIODS

DETAILED SYLLABUS**DESIGN OF INSTRUMENTATION AMPLIFIER****AIM**

To design an instrumentation amplifier based on the three operational amplifier configuration with a differential gain of 100.

EXERCISE

1. Develop the instrumentation amplifier with differential gain of 100 and draw the input Vs output characteristics of the three operational amplifier based instrumentation amplifier and make a comment on the response.
2. Compare the performance characteristics of Instrumentation amplifiers with commercial Monolithic Instrumentation amplifier.

EQUIPMENT

1. Dual power supply – 1 No
2. Digital Multimeters – 1 No
3. Resistors – 10 No
4. Operational Amplifiers – 4 No
5. Any commercial Monolithic Instrumentation amplifier - 2 No

1. DESIGN OF ACTIVE FILTERS**AIM**

To design an active first order / second order Butterworth type Low – Pass / High Pass / Band-pass filter with the following specifications.

Low pass filter : Cut – off frequency : 1 KHz

High pass filter : Cut – off frequency : 1 KHz

Band pass filter : Cut off frequency : $1 \text{ KHz} < f_c < 5 \text{ KHz}$

EXERCISE

1. Develop an active Butterworth first order (or) second order low pass and / or high – pass, band pass filter and determine experimentally the frequency response.

EQUIPMENT

1. Dual power supply - 1 No
2. Operational amplifiers - 2 Nos
3. Resistors - 10 Nos
4. Capacitors - 10 Nos
5. Signal generator - 1 No
6. C.R.O - No

3. DESIGN OF REGULATED POWER SUPPLY AND DESIGN OF V/I AND I/V CONVERTERS

(a) AIM:-

To Design a Regulated Power Supply.

EQUIPMENT

1. Diodes IN4007
2. 100 μ F, 10 μ F
3. IC 7805
4. Potentiometer
5. Ammeter and Voltmeter

EXERCISE

LINE REGULATION

1. Varying the Input Voltage (0 -15)V.
2. Note down the output voltage

LOAD REGULATION

1. Connect a variable Potentiometer across the output of the RPS.
2. Vary the potentiometer and note down the corresponding output current and voltage.

(b) AIM:-

To design a voltage to current converter and a current to voltage converter and verify the characteristics experimentally.

OBJECTIVES

1. To design a voltage to current converter (grounded load) with the following specification
Input voltage range : (0 – 5) V
Output current range : (4-20) mA (should be independent of load)
2. To design a current to voltage converter with the following specification
Input current range : (4-20) mA
Output voltage range : (0-5) V

EXERCISE

1. Determine experimentally the characteristics of voltage and current converter and plot output current versus input voltage and comment on the response.
2. Determine experimentally the characteristics of current to voltage converter and plot output voltage versus input current and comment on the response.

EQUIPMENT

- | | | | |
|----|------------------------|------|----|
| 1. | Resistors | - 10 | No |
| 2. | Operational amplifiers | - 5 | No |
| 3. | Transistor (NPN / PNP) | - 2 | No |
| 4. | Dual power supply | - 1 | No |
| 5. | Digital Multimeters | - 2 | No |
| 6. | Loop analyzer | - 1 | No |

3. DESIGN OF LINEARIZING CIRCUIT AND COLD-JUNCTION COMPENSATION CIRCUIT FOR THERMOCOUPLE

AIM

To design a cold – junction compensation circuit for thermocouple.

OBJECTIVE

To design an automatic reference correction circuit for thermocouple. (A solid – state temperature sensor or RTD can be used for the cold junction measurement)

EXERCISE

1. Develop the circuit for reference junction compensation.
2. Keep the hot junction temperature at say 400°C.
3. Vary the cold – junction temperature from 30 – 90°C and observe the output voltage for with and without cold-junction compensation.
4. Plot the output voltage versus cold-junction temperature and comment on the response.

EQUIPMENT

- | | | | |
|----|-----------------------|------|-----|
| 1. | Thermocouple | - 1 | Nos |
| 2. | Operational amplifier | - 3 | Nos |
| 4. | AD – 590 or RTD | - 1 | Nos |
| 5. | Resistors | - 10 | Nos |
| 6. | Dual power supply | - 1 | No |
| 7. | Multimeters | - 1 | No |

5. DESIGN OF SIGNAL CONDITIONING CIRCUIT FOR STRAIN GAUGES AND RTD

(a) Aim:

To design Signal Conditioning Circuit for Strain Gauge.

Specification as follows

1. Input Range 0 to 1 Kg
2. Output Voltage 0 to 5 V
3. Device -Bourdon Strain Gauge (350 Ohm)

Equipment

1. Bonded Strain Gauge

2. Loads (100 gm to 1 Kg)
3. Operational Amplifier
4. RPS
5. Resistors

Exercise:

Develop Signal Conditioning Circuits for different loads and plot output voltage versus Load.
Comment on Linearity

(b) Aim

To design a signal conditioning circuit to RTD. The specification are as follows

Temperature Range : 30⁰ C – 100⁰C (Approximately)

Output voltage : 0 – 5 V DC

Sensor : RTD (Pt 100)

Current through RTD : Not to exceed 10mA

Equipment

- | | | |
|----|------------------------|---------|
| 1. | RTD (Pt 100) | - 1 No |
| 2. | Resistors | - ? |
| 3. | Operational amplifiers | - 4 Nos |
| 3. | Dual power supply | - 1 No |
| 4. | Temperature bath | - 1 No |
| 5. | Multimeter | - 1 No |
| 6. | Trim Pot | - 3 Nos |

Exercise

1. Develop the signal conditioning circuit and plot the output voltage versus temperature and comment on the linearity.

7. DESIGN OF ORIFICE PLATE AND ROTAMETER

Design of Orifice Plate

Aim:

To Design an Orifice Plate for the given Specification.

Equipment

1. Pump and Reservoir
2. Pipeline with Orifice plate
3. Collecting Tank

Exercise:

1. Convert Electrical Signal to Differential Pressure
2. Determine the interval data
3. Calculate D/d
4. Calculate sizing factor

Design of Rotameter

Aim:

To Design a Rotameter for given Specification

Equipment

1. Pump and Reservoir
2. Pipeline with Orifice plate
3. Collecting Tank

Exercise

1. Switch On the Motor
2. Adjust the Rotameter to read the required flow rate.
3. Start the Timer
4. After 5 Min Note the Head in the tank.

5. Drain the tank.
6. Repeat the Procedure and Calculate Cd in each case

8. Control Valve Sizing

Aim:

To design a Control Valve and Study the flow lift Characteristics

Equipment:

1. Linear Control Valve
2. On/OFF Control Valve
3. Air Regulator
4. Rotameter
5. Pump

Exercise

1. By varying the inlet pressure note down the stem moment value and the flow rate.
2. Draw the Graph for pressure Vs Flow rate, Stem Moment Vs Flow rate

9. Design of PID Controller

Design of PID Controller using Op-Amp

Aim:

1. To study the response of P, PI, PD, PID Controllers using Op-Amp

Equipment

1. Signal Generator
2. IC 741
3. Resistors and Capacitors
4. CRO
5. Bread Board

Exercise

1. Design an Analog PID Controller for various values of K_p , K_i , K_d
2. Apply the error signal from signal generator (Square, Sine)
3. Note down the response from the CRO.

Design of PID Controller using Microprocessor

Aim:

To study the response of P, PI, PD, PID Controllers using Microprocessor.

Equipment

1. Signal Generator
2. Microprocessor based kit with ADC and DAC Section
3. CRO

Exercise:

1. Enter the PID Algorithm in Microprocessor
2. Give the Error signal to ADC Section of Microprocessor Kit.
3. Execute the Microprocessor Program
4. Note down the output response of PID Controller in the DAC Section Microprocessor Kit

AIM:

To encourage the students to comprehend the knowledge acquired from the first Semester to Sixth Semester of B.E Degree Course through periodic exercise.

UNIT I OVERVIEW OF MANAGEMENT 9

Organization - Management - Role of managers - Evolution of Management thought - Organization and the environmental factors - Managing globally - Strategies for International Business.

UNIT II PLANNING 9

Nature and purpose of planning - Planning process - Types of plans – Objectives - Managing by objective (MBO) Strategies - Types of strategies - Policies - Decision Making - Types of decision - Decision Making Process - Rational Decision Making Process - Decision Making under different conditions.

UNIT III ORGANIZING 9

Nature and purpose of organizing - Organization structure - Formal and informal groups / organization - Line and Staff authority - Departmentation - Span of control - Centralization and Decentralization - Delegation of authority - Staffing - Selection and Recruitment - Orientation - Career Development - Career stages – Training - Performance Appraisal.

UNIT IV DIRECTING 9

Creativity and Innovation - Motivation and Satisfaction - Motivation Theories Leadership - Leadership theories - Communication - Hurdles to effective communication - Organization Culture - Elements and types of culture - Managing cultural diversity.

UNIT V CONTROLLING 9

Process of controlling - Types of control - Budgetary and non-budgetary control techniques - Managing Productivity - Cost Control - Purchase Control - Maintenance Control - Quality Control - Planning operations.

TOTAL : 45 PERIODS

TEXT BOOKS:

1. Stephen P. Robbins and Mary Coulter, 'Management', Prentice Hall of India, 8th edition.
2. Charles W L Hill, Steven L McShane, 'Principles of Management', Mcgraw Hill Education, Special Indian Edition, 2007.

REFERENCES:

1. Hellriegel, Slocum & Jackson, ' Management - A Competency Based Approach', Thomson South Western, 10th edition, 2007.
2. Harold Koontz, Heinz Wehrich and Mark V Cannice, 'Management – A global & Entrepreneurial Perspective', Tata Mcgraw Hill, 12th edition, 2007.
3. Andrew J. Dubrin, 'Essentials of Management', Thomson Southwestern, 7th edition, 2007.

CS2351	ARTIFICIAL INTELLIGENCE	L T P C
		3 0 0 3

UNIT I	PROBLEM SOLVING	9
Introduction – Agents – Problem formulation – uninformed search strategies – heuristics – informed search strategies – constraint satisfaction		

UNIT II	KNOWLEDGE AND REASON	9
Logical agents – propositional logic – inferences – first-order logic – inference in first-order logic – forward chaining – backward chaining – resolution		

UNIT III	PLANNING	9
Planning with state-space search – partial-order planning – planning graphs – planning and acting in the real world		

UNIT IV	UNCERTAIN KNOWLEDGE AND REASONING	9
Uncertainty – review of probability - probabilistic Reasoning – Bayesian networks – inferences in Bayesian networks – Temporal models – Hidden Markov models		

UNIT V	LEARNING	9
Learning from observation - Inductive learning – Decision trees – Explanation based learning – Statistical Learning methods - Reinforcement Learning		

TOTAL : 45 PERIODS

TEXT BOOKS:

1. S. Russel and P. Norvig, "Artificial Intelligence – A Modern Approach", Second Edition, Pearson Education, 2003.

REFERENCES:

1. David Poole, Alan Mackworth, Randy Goebel, "Computational Intelligence : a logical approach", Oxford University Press, 1998.
2. G. Luger, "Artificial Intelligence: Structures and Strategies for complex problem solving", Fourth Edition, Pearson Education, 2002.
3. J. Nilsson, "Artificial Intelligence: A new Synthesis", Elsevier Publishers, 1998.

CS2071	COMPUTER ARCHITECTURE	L T P C
		3 0 0 3

UNIT I	INSTRUCTION SET ARCHITECTURE	9
Introduction to computer architecture - Review of digital design – Instructions and addressing – procedures and data – assembly language programs – instruction set variations		

UNIT II	ARITHMETIC/LOGIC UNIT	9
Number representation – design of adders – design of simple ALUs – design of Multipliers and dividers – design of floating point arithmetic unit		

UNIT III	DATA PATH AND CONTROL	9
Instruction execution steps – control unit synthesis – microprogramming – pipelining – pipeline performance		

UNIT IV MEMORY SYSTEM 9
Main Memory concepts – types of memory – cache memory organization – secondary storage – virtual memory – paging

UNIT V I/O AND INTERFACES 9
I/O devices – I/O programming – polling – interrupts – DMA – buses – links – interfacing – context switching – threads and multithreading

TOTAL : 45 PERIODS

TEXT BOOKS

1. B. Parhami, "Computer Architecture", Oxford University Press, 2005.
2. Carl Hamacher, Zvonko Vranesic and Safwat Zaky, "Computer Organization", Fifth Edition, Tata McGraw Hill, 2002

REFERENCES

1. David A. Patterson and John L. Hennessy, "Computer Organization and Design: The Hardware/Software interface", Third Edition, Elsevier, 2004.
2. William Stallings, "Computer Organization and Architecture – Designing for Performance", Seventh Edition, Pearson Education, 2006.
3. Miles Murdocca "Computers Architecture and Organization An Integrated approach", Wiley India pvt Ltd, 2007
4. John D. Carpinelli, "Computer systems organization and Architecture", Pearson Education, 2001.

CS2411

OPERATING SYSTEMS

**L T P C
3 0 0 3**

AIM

To learn the various aspects of operating systems such as process management, memory management, file systems, and I/O management

UNIT I PROCESSES AND THREADS 9

Introduction to operating systems – review of computer organization – operating system structures – system calls – system programs – system structure – virtual machines. Processes: Process concept – Process scheduling – Operations on processes – Cooperating processes – Interprocess communication – Communication in client-server systems. Case study: IPC in Linux. Threads: Multi-threading models – Threading issues. Case Study: Pthreads library

UNIT II PROCESS SCHEDULING AND SYNCHRONIZATION 9

CPU Scheduling: Scheduling criteria – Scheduling algorithms – Multiple-processor scheduling – Real time scheduling – Algorithm Evaluation. Case study: Process scheduling in Linux. Process Synchronization: The critical-section problem – Synchronization hardware – Semaphores – Classic problems of synchronization – critical regions – Monitors. Deadlock: System model – Deadlock characterization – Methods for handling deadlocks – Deadlock prevention – Deadlock avoidance – Deadlock detection – Recovery from deadlock.

UNIT III STORAGE MANAGEMENT 9

Memory Management: Background – Swapping – Contiguous memory allocation –Paging – Segmentation – Segmentation with paging. Virtual Memory: Background – Demand paging – Process creation – Page replacement – Allocation of frames – Thrashing. Case Study: Memory management in Linux

UNIT IV FILE SYSTEMS 9

File-System Interface: File concept – Access methods – Directory structure – File -system mounting – Protection. File-System Implementation : Directory implementation – Allocation methods – Free-space management – efficiency and performance – recovery – log-structured file systems. Case studies: File system in Linux – file system in Windows XP

UNIT V I/O SYSTEMS 9

I/O Systems – I/O Hardware – Application I/O interface – kernel I/O subsystem – Streams – performance. Mass-Storage Structure: Disk scheduling – Disk management – Swap-space management – RAID – disk attachment – stable storage – tertiary storage. Case study: I/O in Linux

TOTAL : 45 PERIODS

TEXT BOOKS

1. Silberschatz, Galvin, and Gagne, "Operating System Concepts", Sixth Edition, Wiley India Pvt Ltd, 2003.
2. D. M. Dhamdhare, "Operating Systems: A concepts based approach", Second Edition, Tata McGraw-Hill Publishing Company Ltd., 2006.

REFERENCES

1. Andrew S. Tanenbaum, "Modern Operating Systems", Second Edition, Pearson Education/PHI, 2001.
2. Harvey M. Deital, "Operating Systems", Third Edition, Pearson Education, 2004.

**CS2070 VISUAL LANGUAGES AND APPLICATIONS L T P C
3 0 0 3**

AIM

To study the principles and techniques of windows programming using MFC, procedures, resources, controls and database programming through the visual languages, Visual C++ and Visual Basic.

OBJECTIVES

- i. To study about the concepts of windows programming models, MFC applications, drawing with the GDI, getting inputs from Mouse and the Keyboard.
- ii. To study the concepts of Menu basics, menu magic and classic controls of the windows programming using VC++.
- iii. To study the concept of Document/View Architecture with single & multiple document interface, toolbars, status bars and File I/O Serialization.
- iv. To study about the integrated development programming event driven programming, variables, constants, procedures and basic ActiveX controls in visual basic.
- v. To understand the database and the database management system, visual data manager, data bound controls and ADO controls in VB.

TEXT BOOKS:

1. Jeff Prosise, 'Programming Windows With MFC', Second Edition, WP Publishers & Distributors [P] Ltd, Reprinted 2002.
2. Evangelos Petroustos, 'Mastering Visual Basic 6.0', BPB Publications, 2002.

REFERENCES:

1. Herbert Schildt, 'MFC Programming From the Ground Up', Second Edition, Tata McGraw Hill, reprinted 2002.
2. John Paul Muller, 'Visual C++ 6 From the Ground Up Second Edition', Tata McGraw Hill, Reprinted 2002.
3. Curtis Smith & Micheal Amundsen, 'Teach Yourself Database Programming with Visual Basic 6 in 21 days', Techmedia Pub, 1999.

EI2021**POWER PLANT INSTRUMENTATION****L T P C
3 0 0 3****AIM**

The course is designed to familiarize the student with the functions and instrumentation available in a modern power generation plant.

OBJECTIVES

- i. To provide an overview of different methods of power generation with a particular stress on thermal power generation.
- ii. To bring out the various measurements involved in power generation plants.
- iii. To provide knowledge about the different types of devices used for analysis.
- iv. To impart knowledge about the different types of controls and control loops.
- v. To familiarize the student with the methods of monitoring different parameters like speed, vibration of turbines and their control

UNIT I OVERVIEW OF POWER GENERATION**9**

Brief survey of methods of power generation – hydro, thermal, nuclear, solar and wind power – importance of instrumentation in power generation – thermal power plants – building blocks – details of boiler processes UP&I diagram of boiler – cogeneration.

UNIT II MEASUREMENTS IN POWER PLANTS**9**

Electrical measurements – current, voltage, power, frequency, power – factor etc. – non electrical parameters – flow of feed water, fuel, air and steam with correction factor for temperature – steam pressure and steam temperature – drum level measurement – radiation detector – smoke density measurement – dust monitor.

UNIT III ANALYZERS IN POWER PLANTS**9**

Flue gas oxygen analyser – analysis of impurities in feed water and steam – dissolved oxygen analyser – chromatography – PH meter – fuel analyser – pollution monitoring instruments.

UNIT IV CONTROL LOOPS IN BOILER**9**

Combustion control – air/fuel ratio control – furnace draft control – drum level control – main stem and reheat steam temperature control – superheater control – attemperator – deaerator control – distributed control system in power plants – interlocks in boiler operation.

UNIT V TURBINE – MONITORING AND CONTROL**9**

Speed, vibration, shell temperature monitoring and control – steam pressure control – lubricant oil temperature control – cooling system

TOTAL : 45 PERIODS**TEXT BOOKS:**

1. Sam G. Dukelow, The control of Boilers, instrument Society of America, 1991.
2. Modern Power Station Practice, Vol.6, Instrumentation, Controls and Testing, Pergamon Press, Oxford, 1971.

REFERENCES:

1. Elonka, S.M. and Kohal A.L. Standard Boiler Operations, McGraw-Hill, New Delhi, 1994.
2. R.K. Jain, Mechanical and industrial Measurements, Khanna Publishers, New Delhi, 1995.

EI2022**INSTRUMENTATION IN PETROCHEMICAL INDUSTRIES****L T P C
3 0 0 3****AIM**

To expose the students to the Instrumentation applied in petrochemical industries.

OBJECTIVES

- i. To expose the students to the basic processing in petroleum industry.
- ii. To provide adequate knowledge about the unit operations.
- iii. To impart knowledge pertaining to the petroleum products and the chemicals obtained from them.
- iv. To provide adequate knowledge about the measurement of various parameters in petrochemical industry.
- v. To expose the students to the various control loops in Petrochemical Industry.

UNIT I PETROLEUM PROCESSING**9**

Petroleum exploration – Recovery techniques – Oil – Gas separation - Processing wet gases – Refining of crude oil.

UNIT II OPERATIONS IN PETROLEUM INDUSTRY**9**

Thermal cracking – Catalytic cracking – Catalytic reforming – Polymerisation – Alkylation – Isomerization – Production of ethylene, acetylene and propylene from petroleum.

UNIT III CHEMICALS FROM PETROLEUM PRODUCTS**9**

Chemicals from petroleum – Methane derivatives – Acetylene derivatives – Ethylene derivatives – Propylene derivatives – Other products.

UNIT III MEASUREMENTS IN PETROCHEMICAL INDUSTRY**9**

Parameters to be measured in refinery and petrochemical industry – Selection and maintenance of measuring instruments – Intrinsic safety of Instruments.

UNIT IV CONTROL LOOPS IN PETROCHEMICAL INDUSTRY**9**

Process control in refinery and petrochemical industry – Control of distillation column – Control of catalytic crackers and pyrolysis unit – Automatic control of polyethylene production – Control of vinyl chloride and PVC production.

TOTAL : 45 PERIODS**TEXT BOOKS**

1. A.L. Waddams, 'Chemicals from Petroleum', Butter and Janner Ltd., 1968.
2. J.G. Balchan. and K.I. Mumme, 'Process Control Structures and Applications', Van Nostrand Reinhold Company, New York, 1988.

REFERENCES

1. Austin G.T. Shreeves, 'Chemical Process Industries', McGraw Hill International Student edition, Singapore, 1985.
2. B.G Liptak, 'Instrumentation in Process Industries', Chilton Book Company, 1994.

EI2023**MICRO ELECTRO MECHANICAL SYSTEMS****L T P C
3 0 0 3****AIM**

The course is designed to familiarize the student with the functions and applications of MEMS.

OBJECTIVES

- (i) To study about MEMS and parts of MEMS
- (ii) To study the design methodology of MEMS for various mechanics.
- (iii) To study about actuators in MEMS.
- (iv) To study about MEMS based circuits.
- (v) To study about optical and RF based MEMS.

UNIT I INTRODUCTION TO MEMS**9**

MEMS and Microsystems, Miniaturization, Typical products, Micro Sensors, Micro actuation, MEMS with micro actuators, Microaccelerometers and Micro fluidics, MEMS materials, Micro Fabrication

UNIT II MECHANICS FOR MEMS DESIGN**9**

Elasticity, Stress, strain and material properties, Bending of thin plates, Spring configurations, torsional deflection, Mechanical vibration, Resonance, Thermo mechanics – actuators, force and response time, Fracture and thin film mechanics, material, physical vapor deposition (PVD), chemical mechanical polishing (CMP)

UNIT III ELECTRO STATIC DESIGN 9

Electrostatics: basic theory, electro static instability, Surface tension, gap and finger pull up, Electro static actuators, Comb generators, gap closers, rotary motors, inch worms, Electromagnetic actuators, bistable actuators.

UNIT IV CIRCUIT AND SYSTEM ISSUES 9

Electronic interfaces, Feed back systems, Noise, Circuit and system issues, Case studies –Capacitive accelerometer, Peizo electric pressure sensor, Thermal sensors, radiation sensors, mechanical sensors, bio-chemical sensors Modeling of MEMS systems, CAD for MEMS.

UNIT V INTRODUCTION TO OPTICAL AND RF MEMS 9

Optical MEMS, system design basics – Gaussian optics, matrix operations, Resolution, Case studies, MEMS scanners and retinal scanning, display, Digital Micro mirror devices, RF Memes – design basics, case study – Capacitive RF MEMS switch, Performance issues.

TOTAL : 45 PERIODS

TEXT BOOK:

1. Stephen Santeria, "Microsystems Design ", Kluwer publishers, 2000.

REFERENCES

1. Nadim Maluf, " An introduction to Micro electro mechanical system design", Artech House, 2000.
2. Mohamed Gad-el-Hak, editor, " The MEMS Handbook", CRC press Baco Raton, 2000
3. Tai Ran Hsu, "MEMS & Micro systems Design and Manufacture" Tata McGraw Hill, New Delhi, 2002.
4. Julian w. Gardner, Vijay k. varadan, Osama O.Awadelkarim,micro sensors mems and smart devices, John Wiley & son LTD,2002
5. James J.Allen, micro electro mechanical system design, CRC Press published in 2005

**GE2023 FUNDAMENTALS OF NANOSCIENCE L T P C
3 0 0 3**

UNIT I INTRODUCTION 10

Nanoscale Science and Technology- Implications for Physics, Chemistry, Biology and Engineering-Classifications of nanostructured materials- nano particles- quantum dots, nanowires-ultra-thinfilms-multilayered materials. Length Scales involved and effect on properties: Mechanical, Electronic, Optical, Magnetic and Thermal properties. Introduction to properties and motivation for study (qualitative only).

UNIT II PREPARATION METHODS 10

Bottom-up Synthesis-Top-down Approach: Precipitation, Mechanical Milling, Colloidal routes, Self-assembly, Vapour phase deposition, MOCVD, Sputtering, Evaporation, Molecular Beam Epitaxy, Atomic Layer Epitaxy, MOMBE.

UNIT III PATTERNING AND LITHOGRAPHY FOR NANOSCALE DEVICES 5

Introduction to optical/UV electron beam and X-ray Lithography systems and processes, Wet etching, dry (Plasma /reactive ion) etching, Etch resists-dip pen lithography

UNIT III	IMAGE ENHANCEMENT AND RESTORATION	9
Histogram modification and specification techniques, Noise distributions, Spatial averaging, Directional Smoothing, Median and filters, Homomorphic filtering, Color image enhancement. Image Restoration – degradation model, Inverse filtering – removal of blur, Wiener filtering, Geometric transformations – spatial transformations, Gray-Level interpolation,		
UNIT IV	IMAGE SEGMENTATION AND RECOGNITION	9
Edge detection. Image segmentation by region growing, region splitting and merging, edge linking.. Image Recognition – Patterns and pattern classes, Matching by minimum distance classifier, Matching by correlation, Image classification using neural network.		
UNIT V	IMAGE COMPRESSION	9
Need for data compression, Huffman,. Run Length Encoding, Shift codes, Arithmetic coding, Vector Quantization, Block Truncation Coding. Transform Coding – DCT and Wavelet. JPEG, MPEG. Standards, principles of Context based Compression.		
		TOTAL : 45 PERIODS

TEXT BOOKS:

1. William K.Pratt, ‘ Digital Image Processing’, John Wiley, NewYork,2002.
2. Anil K. Jain, ‘Fundamentals of Digital Image Processing’, Prentice Hall of India, 2002.

REFERENCES:

1. David Salomon : Data Compression – The Complete Reference, Springer Verlag New York Inc., 2nd Edition, 2001
2. Rafael C. Gonzalez, Richard E.Woods, Steven Eddins, ‘ Digital Image Processing using MATLAB’, Pearson Education, Inc., 2004.
3. Rafael C. Gonzalez, Richard E.Woods, ‘Digital Image Processing’, Pearson Education, Inc., Second Edition, 2004
4. Milman Sonka, Vaclav Hlavac, Roger Boyle, ‘Image Processing, Analysis, and Machine Vision’, Brooks/Cole, Vikas Publishing House, II ed., 1999.
5. Sid Ahmed, M.A., ‘Image Processing Theory, Algorithms and Architectures’, McGraw-Hill, 1995.

EC2056	ADVANCED COMMUNICATION ENGINEERING	L T P C
		3 0 0 3

AIM

To understand the advanced communication engineering concepts.

OBJECTIVES

- i. To have a detailed knowledge of various spread spectrum techniques.
- ii. To understand the basic principles of mobile communication and bluetooth technology.
- iii. To have an exposure towards the high performance communication networks – ATM and ISDN
- iv. To understand the operation of Radar and Navigationational aids.

UNIT I	SPREAD SPECTRUM COMMUNICATION	9
Spread spectrum techniques-spreading techniques-PN sequences-DSSS, RHSS-use of spread spectrum with CDMA		

UNIT II	MOBILE COMMUNICATION	9
Basic cellular system-performance criteria-operation of cellular system-cell splitting-interference GSM, GPRS, Blue tooth-the link controller, the link manager, the host controller interface, LLCAP, WLL, Multiple access techniques		
UNIT III	ATM	9
ATM's position in OSL model-B-ISDN protocol reference model-ATM functions and layers-ATM signaling principles, TM operation and maintenance-ATM protocol stack: lower layers, fibre based networks and its advantages-ATM physical layer media		
UNIT IV	ISDN	9
ISDN standards, ISDN interface and functions-UNI-ISDN proctol architecture, ISDN physical layer, ISDN dataline layer-Network interface		
UNIT V	RADAR AND NAVIGATIONAL AIDS	9
Radar block diagram and operation-Radar range equation-Prediction of range performance-Minimum detectable signal-Pulse repetition frequency and range ambiguities-CW and FM CW radar-Synthetic aperature and air surveillance radar-ECCM and bistatic radar		

TOTAL : 45 PERIODS

TEXT BOOKS

1. M.I. SKOLNIK, Introduction to Radar-Mc Graw Hill and Edition
2. William C.Y.Lee, Mobile Cellular Telecommunications system-Mc Graw Hill International Ed. 1990
3. Simon Haykin, 'Digital communications', John Wiley and Sons, 1988
4. William Stallings,'ISDN and B-ISDN' Macmillan Publishers, 1995

REFERENCES

1. Jennifer Bray and Charles F. Sturman, 'Bluetooth', Pearson Education Asia, 2001.

EC2057

ADVANCED DIGITAL SIGNAL PROCESSING

**L T P C
3 0 0 3**

[Review of discrete-time signals and systems- DFT and FFT, Z-Transform, Digital Filters is recommended]

AIM

To provide adequate knowledge in Random signal processing.

OBJECTIVES

- i. Detail study of time averaging , ensamble averaging & study of power spectral density.
- ii. Detail study of parametric & non – parametric estimation
- iii. Detail study of adaptive filters & its applications
- iv. Introduction study of multivariable digital signal processing.

- UNIT I DISCRETE RANDOM SIGNAL PROCESSING 9**
Discrete Random Processes- Ensemble averages, stationary processes, Autocorrelation and Auto covariance matrices. Parseval's Theorem, Wiener-Khintchine Relation- Power Spectral Density- Periodogram Spectral Factorization, Filtering random processes. Low Pass Filtering of White Noise. Parameter estimation: Bias and consistency.
- UNIT II SPECTRUM ESTIMATION 9**
Estimation of spectra from finite duration signals, Non-Parametric Methods-Correlation Method , Periodogram Estimator, Performance Analysis of Estimators -Unbiased, Consistent Estimators- Modified periodogram, Bartlett and Welch methods, Blackman –Tukey method. Parametric Methods - AR, MA, ARMA model based spectral estimation. Parameter Estimation -Yule-Walker equations, solutions using Durbin's algorithm
- UNIT III LINEAR ESTIMATION AND PREDICTION 9**
Linear prediction- Forward and backward predictions, Solutions of the Normal equations- Levinson-Durbin algorithms. Least mean squared error criterion -Wiener filter for filtering and prediction , FIR Wiener filter and Wiener IIR filters.
- UNIT IV ADAPTIVE FILTERS 9**
FIR adaptive filters -adaptive filter based on steepest descent method-Widrow-Hoff LMS adaptive algorithm, Normalized LMS. Adaptive channel equalization-Adaptive echo cancellation-Adaptive noise cancellation- Adaptive recursive filters (IIR).
- UNIT V MULTIRATE DIGITAL SIGNAL PROCESSING 9**
Mathematical description of change of sampling rate - Interpolation and Decimation , Decimation by an integer factor - Interpolation by an integer factor, Sampling rate conversion by a rational factor, Filter implementation for sampling rate conversion- direct form FIR structures, Polyphase filter structures, time-variant structures. Multistage implementation of multirate system.

L = 45 T = 15 TOTAL: 60 PERIODS

TEXT BOOKS:

1. Monson H.Hayes, Statistical Digital Signal Processing and Modeling, John Wiley and Sons, Inc., Singapore, 2002.
2. John G. Proakis, Dimitris G.Manolakis, Digital Signal Processing Pearson Education, 2002.

REFERENCES:

1. John G. Proakis et.al.'Algorithms for Statistical Signal Processing', Pearson Education, 2002.
2. Dimitris G.Manolakis et.al.' Statistical and adaptive signal Processing', McGraw Hill, New York, 2000.
3. Rafael C. Gonzalez, Richard E.Woods, 'Digital Image Processing', Pearson Education, Inc., Second Edition, 2004.(For Wavelet Transform Topic)

EE 2023

ROBOTICS AND AUTOMATION

**L T P C
3 0 0 3**

AIM:

To provide comprehensive knowledge of robotics in the design, analysis and control point of view.

GE2022	TOTAL QUALITY MANAGEMENT	L T P C
		3 0 0 3
UNIT I	INTRODUCTION	9
Introduction - Need for quality - Evolution of quality - Definition of quality - Dimensions of manufacturing and service quality - Basic concepts of TQM - Definition of TQM – TQM Framework - Contributions of Deming, Juran and Crosby – Barriers to TQM.		
UNIT II	TQM PRINCIPLES	9
Leadership – Strategic quality planning, Quality statements - Customer focus – Customer orientation, Customer satisfaction, Customer complaints, Customer retention - Employee involvement – Motivation, Empowerment, Team and Teamwork, Recognition and Reward, Performance appraisal - Continuous process improvement – PDSA cycle, 5s, Kaizen - Supplier partnership – Partnering, Supplier selection, Supplier Rating.		
UNIT III	TQM TOOLS & TECHNIQUES I	9
The seven traditional tools of quality – New management tools – Six-sigma: Concepts, methodology, applications to manufacturing, service sector including IT – Bench marking – Reason to bench mark, Bench marking process – FMEA – Stages, Types.		
UNIT IV	TQM TOOLS & TECHNIQUES II	9
Quality circles – Quality Function Deployment (QFD) – Taguchi quality loss function – TPM – Concepts, improvement needs – Cost of Quality – Performance measures.		
UNIT V	QUALITY SYSTEMS	9
Need for ISO 9000- ISO 9000-2000 Quality System – Elements, Documentation, Quality auditing- QS 9000 – ISO 14000 – Concepts, Requirements and Benefits – Case studies of TQM implementation in manufacturing and service sectors including IT.		

TOTAL : 45 PERIODS

TEXT BOOK

1. Dale H.Besterfiled, et at., “Total Quality Management”, Pearson Education Asia, Third Edition, Indian Reprint (2006).

REFERENCES

1. James R. Evans and William M. Lindsay, “The Management and Control of Quality”, (6th Edition), South-Western (Thomson Learning), 2005.
2. Oakland, J.S. “TQM – Text with Cases”, Butterworth – Heinemann Ltd., Oxford, Third Edition (2003).
3. Suganthi,L and Anand Samuel, “Total Quality Management”, Prentice Hall (India) Pvt. Ltd. (2006)
4. Janakiraman,B and Gopal, R.K, “Total Quality Management – Text and Cases”, Prentice Hall (India) Pvt. Ltd. (2006)

GE2025	PROFESSIONAL ETHICS IN ENGINEERING	L T P C
	(For Affiliated Colleges under R-2008)	3 0 0 3

UNIT I	ENGINEERING ETHICS	9
Senses of ‘Engineering Ethics’ – Variety of moral issues – Types of inquiry – Moral dilemmas – Moral Autonomy – Kohlberg’s theory – Gilligan’s theory – Consensus and Controversy – Professions and Professionalism – Professional Ideals and Virtues – Uses of Ethical Theories		
UNIT II	ENGINEERING AS SOCIAL EXPERIMENTATION	9
Engineering as Experimentation – Engineers as responsible Experimenters – Research Ethics - Codes of Ethics – Industrial Standards - A Balanced Outlook on Law – The Challenger Case Study		

UNIT III ENGINEER'S RESPONSIBILITY FOR SAFETY 9

Safety and Risk – Assessment of Safety and Risk – Risk Benefit Analysis – Reducing Risk – The Government Regulator's Approach to Risk - Chernobyl Case Studies and Bhopal

UNIT IV RESPONSIBILITIES AND RIGHTS 9

Collegiality and Loyalty – Respect for Authority – Collective Bargaining – Confidentiality – Conflicts of Interest – Occupational Crime – Professional Rights – Employee Rights – Intellectual Property Rights (IPR) - Discrimination

UNIT V GLOBAL ISSUES 9

Multinational Corporations – Business Ethics - Environmental Ethics – Computer Ethics - Role in Technological Development – Weapons Development – Engineers as Managers – Consulting Engineers – Engineers as Expert Witnesses and Advisors – Honesty – Moral Leadership – Sample Code of Conduct

TOTAL : 45 PERIODS

TEXT BOOKS :

1. Mike Martin and Roland Schinzinger, "Ethics in Engineering", McGraw Hill, New York (2005).
2. Charles E Harris, Michael S Pritchard and Michael J Rabins, "Engineering Ethics – Concepts and Cases", Thompson Learning, (2000).

REFERENCES :

1. Charles D Fleddermann, "Engineering Ethics", Prentice Hall, New Mexico, (1999).
2. John R Boatright, "Ethics and the Conduct of Business", Pearson Education, (2003)
3. Edmund G Seebauer and Robert L Barry, "Fundamentals of Ethics for Scientists and Engineers", Oxford University Press, (2001)
4. Prof. (Col) P S Bajaj and Dr. Raj Agrawal, "Business Ethics – An Indian Perspective", Biztantra, New Delhi, (2004)
5. David Ermann and Michele S Shauf, "Computers, Ethics and Society", Oxford University Press, (2003)

IC2401

DIGITAL CONTROL SYSTEM

**L T P C
3 0 0 3**

AIM

To provide sound knowledge on the principles of discrete data control system

OBJECTIVES

- i. To study the importance of sample data control system.
- ii. To give adequate knowledge about signal processing in digital control.
- iii. To study the importance of modeling of discrete systems and stability analysis of discrete data system.
- iv. To study the importance of state space representation for discrete data system.
- v. To introduce the design concept for digital controllers.

UNIT I	ANN - INTRODUCTION	9
Introduction – Biological neuron – Artificial neuron – Neuron modeling – Learning rules – Single layer – Multi layer feed forward network – Back propagation – Learning factors.		
UNIT II	ANN - ARCHITECTURE AND APPLICATIONS	9
Feedback networks – Discrete time Hopfield networks – Transient response of continuous time networks – Process modeling using ANN- Neuro controller for inverted pendulum.		
UNIT III	FUZZY SYSTEMS	9
Classical sets – Fuzzy sets – Fuzzy relations – Fuzzification - Membership functions – Defuzzification – Methods of defuzzification – Fuzzy rules.		
UNIT IV	FUZZY LOGIC CONTROL	9
Membership function – Knowledge base – Decision-making logic – Optimisation of membership function using neural networks – Adaptive fuzzy system.- FLC for inverted pendulum- Home heating system- Introduction to Neuro-fuzzy systems.		
UNIT V	OPTIMIZATION TECHNIQUES	9
Gradient Search – Non-gradient search – Genetic Algorithms: Operators, search algorithm, penalty – Evolutionary Programming: Operators, Search Algorithms		

TOTAL: 45 PERIODS

TEXT BOOKS:

1. Laurance Fausett, 'Fundamentals of Neural Networks', Pearson Education, 2004.
2. Timothy J. Ross, 'Fuzzy Logic with Engineering Applications', McGraw Hill, 1997.
3. David Goldberg, "Genetic Algorithms in Search, Optimization and Machine Learning", Pearson Education, 2007.

REFERENCES:

1. J.S.R.Jang, C.T.Sun and E.Mizutani, ' Neuro- Fuzzy and Soft Computing' Pearson Education, New Delhi, 2004
2. Jacek M. Zurada, 'Introduction to Artificial Neural Systems', Jaico Publishing home, 2002.
3. John Yen and Reza Langari, 'Fuzzy Logic – Intelligence, Control and Information', Pearson Education, New Delhi, 2003.
4. Robert J.Schalkoff, ' Artificial Neural Networks', McGraw Hill, 1997.