FACULTY OF INFORMATION AND COMMUNICATION ENGINEERING

M.E. VLSI DESIGN (R 2021)

SEMESTER – I

Course Code: VL4111	**Course Title:** FPGA Laboratory	
Sl. No.	**Description of Equipment**	**Required numbers (for batch of 25 students)**
1.	FPGA simulator tool	14 users (licensed/open source)
2.	FPGA Trainer Board	14 Kits
3.	PCs	25 Nos.

FACULTY OF INFORMATION AND COMMUNICATION ENGINEERING

M.E. VLSI DESIGN (R 2021)

SEMESTER – I

Course Code: VL4112	Course Title: Analog IC Design Laboratory	
Sl. No.	**Description of Equipment**	**Required numbers (for batch of 25 students)**
1.	Spice Simulator tool	14 users (licensed/open source)
2.	PCs	14 Nos.