UNIT I  

UNIT II  

UNIT III  
Multicore programming Model – Shared memory model, message passing model, transaction model – OpenMP and MPI Programming.

UNIT IV  

UNIT V  
Cell Broad band engine architecture, PPE (Power Processor Element), SPE (Synergistic processing element), Cell Software Development Kit, Programming for Multicore architecture.

TOTAL: 45

TEXT BOOK:

3. IBM Journals for Power 5, Power 6 and Cell Broadband engine architecture.

REFERENCES: