

**National Level workshop on
FPGA Design Flow using Xilinx
Vivado**

11th and 12th October 2019

**Under the patronage of UGC-UPE project,
Anna University**



Coordinators

**Dr.S. Nirmala Devi, Professor,
Dept.ECE.**

**Dr. T.Jayasree, Asst. Professor,
Dept.ECE.**

In association with



Organized by

**University with potential of Excellence
in Biomedical Engineering
Instrumentation (UPE-BMEI)
Anna University
Chennai.**

ABOUT UPE-BMEI

In 2016 University Grants commission awarded “University with potential for Excellence in Biomedical Engineering and Instrumentation (UPE-BMEI) to Anna University with handsome funding owing to the proven track record of its multidisciplinary teams to design, develop and commercialize biomedical devices. This platform helps in bringing together the scientists, engineers, clinicians and industrialists. The high end infra structure designed under this UPE-BMEI programme for the versatile design and development of cost effective indigenous commercial prototypes, validation and manufacturing in limited quantities for multicentre clinical validation and initial batch productions is a rich source for practical learning and medical device development applications.

ABOUT DEPARTMENT of ECE

The Department of Electronics and Communications Engineering (DECE), which became a part of this celebrated institution in the year 1945. The department, headed by professors and lecturers who are experts in their own disciplines, aims at educating and training students with sound knowledge and awareness in the latest trends in electronics, communication and information technology. Keeping in line with fast changing technology, the department has a well designed, constantly reviewed syllabus to incorporate all advancements in existing and emerging technologies. The state-of-the-art laboratories complement the high standards set by the competitive syllabus and nurture the inclination of the students towards research and development, besides giving them the necessary and sufficient backing of practical knowledge that they need.

ABOUT CoreEL TECHNOLOGIES

CoreEL Technologies is a Customer Application Specific Product & Solutions (CASPS) company offering INNOVATIVE solutions from its diverse portfolio of expertise that includes Intellectual Property (IP) cores, System Design, Manufacturing, Sustenance and OEM solutions in the form of EDA tools, CAE tools, COTS products and Technology training. CoreEL’s strength lies in its ability to blend deep domain knowledge with the right ingredients across its portfolio of offerings.

OBJECTIVE

The objective of this workshop is to provide opportunity for faculty members and research scholars to enrich their knowledge in the area of Vivado design flow using Xilinx platform. This workshop will afford a vibrant opening for researchers in the Simulation and design procedures using the XSIM HDL simulator and attribute in Synthesizing a RTL Design. The workshop is intended for the exchange of ideas among Engineers, Scientists, Research organizations and Industries to promote and accelerate the ideas involved in FPGA design flow. The technical experts will bring out the current and future capabilities in Xilinx design constrains.

RESOURCE PERSONS

Experts from CoreEL Technologies

TARGET AREAS

- **Vivado Design Flow**
- **Synthesizing a RTL Design**
- **Xilinx Design Constraints**
- **Hardware Debugging**
- **Embedded Design flow on Zynq & Pynq framework using PYNQ Z2**

CHIEF PATRON:

Dr. M. K. Surappa
Vice-Chancellor,
Anna University,
Chennai.

PATRON:

Dr. L. Karunamoorthy
Registrar,
Anna University,
Chennai.

CHAIR:

Dr.T.V.Geetha
Dean of CEG.

CO-CHAIR:

Dr.S.Muttan
Coordinator, UPE

CO-ORDINATORS:

Dr.S.Nirmala Devi, Prof
Dr.T.Jayasree Asst. Prof

VENUE:

Mini Auditorium,
Department of ECE.
College of Engineering Guindy,
Anna University,
Chennai-600 025.

REGISTRATION FEE

There is no registration fee for the workshop. The participants will be selected on "First come- First served" basis. The completed registration form can be submitted to the coordinator not later than 07.10.2019. The scanned copy of the form can be mailed to the specified e-mail ID. The selection intimation will be sent by e-mail on 09.10.2019.

IMPORTANT DATES:

Submission of Application: 07.10.2019
Intimation of Selection: 08.10.2019
Confirmation by participants: 09.10.2019

ADDRESS FOR COMMUNICATION:

Workshop on "FPGA Design Flow using Xilinx Vivado"
Department of ECE,
College of Engineering Guindy,
Anna University,
Chennai- 600025.

E-mail: upeworkshop@gmail.com

NO ACCOMMODTION WILL BE PROVIDED

REGISTRATION FORM

Name:

Designation:

Specialization:

Areas of research:

Department:

College/organization:

Address:

Phone No. :

E-mail:

DECLARTION

The above information is true to the best of my knowledge. I agree to abide by the rules and regulations governing the Workshop of the Anna University. If selected, I shall attend the programme for the entire duration. I also undertake the responsibility to inform the coordinator in advance in case I am unable to attend the workshop.

Date:

Signature of applicant

Place:

Signature of the HOD / Principal with seal