REGISTRATION FORM
FACULTY DEVELOPMENT TRAINING PROGRAMME ON
DIGITAL SIGNAL PROCESSING
11th -17th, JUNE 2014

1. Name :
2. Designation :
3. Qualification:
4. Age and Date of Birth:
5. Experience:
6. Email ID & Mobile No.:
7. Institution Address:
8. Institute Affiliated to Anna University: Yes/No
9. Address for Communication:
10. Accommodation Required: Yes/No

Date :
Place :
Signature of the Applicant

SPONSORSHIP CERTIFICATE
Certified that Dr./Prof./Mr./Ms. ________________
______________ is a bonafide faculty member of our
Engineering College. He/She is identified to teach the
subject “Digital Signal Processing”. He/She is
sponsored to attend this FDP programme.

Date :
Place :
Signature of the Principal with Seal

ORGANIZING COMMITTEE

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Prof. Dr. M. Rajaram
Vice Chancellor
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Chennai-600 025

PATRON
Dr. S. Ganesan
Registrar
Anna University
Chennai-600 025

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Associate Professor / Dept. of ECE
Anna University Regional Centre, Coimbatore
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Asst. Professor/Dept. of ECE
Anna University Regional Centre, Coimbatore

CONTACT ADDRESS
For any queries and to send completed registration
forms:
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SEVEN DAYS FDTP ON
DIGITAL SIGNAL PROCESSING
11th -17th, JUNE 2014

SPONSORED BY
CENTRE FOR FACULTY DEVELOPMENT
ANNA UNIVERSITY, CHENNAI

CO-ORDINATORS
Dr. V. R. Vijaykumar
Mr. S. Saravanakumar

ORGANISED BY
Department of Electronics and Communication Engineering

Anna University
Regional Centre, Coimbatore
Coimbatore – 641 047
ABOUT THE COURSE

The objective of this Faculty Development Programme is to give a better exposure to the faculty members on the subject “DIGITAL SIGNAL PROCESSING”. The course covers detailed theory, tutorials and laboratory practices.

UNIT I DISCRETE FOURIER TRANSFORM
DFT and its properties, Relation between DTFT and DFT, FFT computations using Decimation in time and Decimation in frequency algorithms, Overlap-add and save methods.

UNIT II INFINITE IMPULSE RESPONSE DIGITAL FILTERS

UNIT III FINITE IMPULSE RESPONSE DIGITAL FILTERS

UNIT IV FINITE WORD LENGTH EFFECTS
Fixed point and floating point number representations – Comparison – Truncation and Rounding errors - Quantization noise – derivation for quantization noise power – coefficient quantization error – Product quantization error - Overflow error – Roundoff noise power - limit cycle oscillations due to product roundoff and overflow errors - signal scaling.

UNIT V MULTIRATE SIGNAL PROCESSING
Introduction to Multirate signal processing-Decimation-Interpolation-Poly phase implementation of FIR filters for interpolator and decimator - Multistage implementation of sampling rate conversion- Design of narrow band filters - Applications of Multirate signal processing.

ABOUT THE DEPARTMENT

The Coimbatore Regional Centre of Anna University has nine PG departments and it offers twenty one PG Programmes. The Department offers M.E degree courses in Applied Electronics, Communication Systems, and VLSI Design both full time and part time mode. The Department is equipped with excellent infrastructure and state-of-the-art equipment to explore the technological challenges in the areas VLSI Design, Signal Processing, Image Processing, RF Design and Digital System Design.

SCOPE OF THE FDTP

This Faculty Development Training Programme has emphasis to provide a platform for interaction and to discuss about the recent advances, new paradigms, technological trends and challenges in the field of Digital Signal Processing. The main objectives of this FDTP are but not limited to:

- Introduce the concept of DFT and to understand how signals are analyzed using DFT and FFT
- Design of FIR and IIR filters
- Study the various types of finite word length effects
- Introduce multirate signal processing and its applications
- Implement signal processing algorithms in MATLAB

RESOURCE PERSONS

Dr.D.Ebenezer,
Professor, Department of ECE
Eswari Engineering College, Chennai.

Dr.Y.V.Ramanarao
Professor, Dept. of ECE
CEG, Guindy, Anna University Chennai

Dr.N.Malmarugan
Director, SRIET, Coimbatore

Dr.P.Palanichamy
Associate Professor, Department of ECE
National Institute of Technology, Trichy

Dr.V.R.Vijaykumar
Associate Professor, Department of ECE
Anna University Coimbatore

ELIGIBILITY

The course is open to teachers from Affiliated Engineering Colleges in Tamil Nadu.

COURSE FEE

No course fee for the first 25 registrants. Applicants from affiliated colleges of Anna University will be given preference. Additional participants may be considered provided each additional participant pays Rs.2000/- as course fee in the form of Demand Draft in favour of “IEEE STUDENT BRANCH-AUCBE”, Department of ECE, Anna University, Regional Centre, Coimbatore.

Venue:
MBA Seminar Hall,
Anna University Regional Centre, Coimbatore
Coimbatore - 641 047

IMPORTANT DATES

Last Date of Registration: 05-06-2014
Intimation of Acceptance: 06-06-2014
(Through E-mail only)