REGISTRATION FORM

Name: 

Department: 

College/organization: 

Address: 

Phone No.: 

E-mail: 

DD Details: 

Amount: 

DD No & date: 

Bank: 

Date: 

Signature of the applicant

Signature of the HOD / Principal with seal

Address for Communication:
The Coordinators,
Workshop on “Digital Custom IC Design Using Cadence Systems Custom Design Flow”
Department of ECE,
College of Engineering, Guindy,
Anna University, Chennai – 600025
E-mail: nirmal@annauniv.edu

WORKSHOP ON
“DIGITAL CUSTOM IC DESIGN USING
CADENCE SYSTEMS CUSTOM DESIGN FLOW”
(March 12-14, 2015)

Coordinators
Dr. P.Nirmal Kumar, Associate Professor
Dr. P.Sakthivel, Associate Professor
Dr. N.Ramadass, Associate Professor

Organized by

Department of Electronics and Communication Engineering
CEG Campus
Anna University
Chennai 600025

in collaboration with

Entuple Technologies Private Limited
Bangalore
OBJECTIVE
The workshop has been planned for three days to provide a platform where the students and the research scholar can get knowledge on Digital Custom IC Design using Cadence Systems and current research topics in the area of digital IC design.

OUTCOME OF THE WORKSHOP
At the end of the workshop, each participant attending this workshop is expected to be a recipient of the knowledge on
- Digital custom IC design
- Applications of custom IC in electronics industry
- Research challenges in the development of digital custom IC
- Programming digital custom IC for telecommunication components realization
- Architectural design and realization of communication components

RESOURCE PERSONS
The experts from nationally reputed institutes, engineers from Entuple Technologies Pvt. Ltd., Bangalore and the faculty of Department of ECE, Anna University..

TARGET AUDIENCE
UG/PG Students and Research Scholars (of Department of ECE, Anna University, Chennai) pursuing research in digital VLSI custom design, Research scholars and Academicians of other Engineering Colleges and R&D personnel from industries.

REGISTRATION
The completed registration form along with Demand Draft should be submitted to ‘The Coordinator, Workshop on Digital Custom IC Design Using Cadence Systems Custom Design Flow”, not later than 28.02.2015. Selection will be intimated via email. There are only limited entries. The participants will be selected on first-come-first-served basis. Applications without registration fee will not be entertained.

Registration Fee:
UG/PG Students/Research Scholars of Dept. of ECE, CEG Campus : Free of cost
Research Scholars/Faculty Members from other Colleges/Institutions : Rs. 500/-
Industry Participants : Rs.1000/-

The payment should be made in the form of Demand Draft in favor of “Organised Programmes, Anna University, Chennai-25” payable at Chennai.

IMPORTANT DATES
Last date for submission of Registration form : February 28, 2015
Selection intimation to the participants by email: March 04, 2015
Workshop Dates : March 12-14, 2015

Note: The participants are advised to arrange their own accommodation.