

# CALL FOR PAPERS

## PAPER SUBMISSION:

Authors are instructed to submit their Camera Ready paper(s) not exceeding 6 pages in a single column format and to be submitted using easy Chair link ([easychair.org/cfp/nceece2022](http://easychair.org/cfp/nceece2022)) or mail your papers to [eee@aubit.edu.in](mailto:eee@aubit.edu.in)

## REGISTRATION FEES:

It is mandatory that at least one author of each accepted paper should register for the conference.

Author's Registration (Faculty/ Industry)	: Rs.600/-
Author's Registration (Scholar/ UG & PG Students)	: Rs.350/-
Participation Registration (Co-Author / Non-Author)	: Rs.250/-

Registration fee includes conference kit, refreshment and lunch.

(Note: Each registration covers maximum of one paper)

Authors can pay the registration fees by

1.The payment can be made by DD drawn in favour of "The Head, Department of EEE, Anna University, BIT Campus, Tiruchirappalli" payable at Tiruchirappalli on or before 1st November 2022.

(Or)

2. Through online :

Account name : The Head, Dept of EEE  
Account no : 1602115000000240  
IFSC code : KVBL0001602  
Bank name : Karur Vysya Bank  
Branch : Suriyur Branch

Send the soft copy of the payment details (either online or offline payment) via email to [eeeconference@aubit.edu.in](mailto:eeeconference@aubit.edu.in)

## TOPICS:

- Power Systems
- Power Quality
- Reliability and continuity of supply
- Energy efficiency in industry
- High Voltage Engineering
- Electronic Materials
- Power System Planning and Scheduling
- Power System Protection, Operation and Control
- Transmission & Distribution Systems and Apparatus HVDC and FACTS
- Electromagnetic Transients Programs (EMTP)
- Wind, Solar and Renewable Energy
- Control Theory and Applications
- Electrical Machines
- Electric Drives and Applications
- Electrical Traction Systems and Control
- Microprocessor and Microcontroller
- Very Large Scale Integration (VLSI)
- Power Electronics and Drive Systems
- Embedded System
- Embedded Networking
- Embedded System Security
- Intelligent Control
- Instrumentation
- Electric Power Generation
- Electronic Materials
- Electromagnetic Transients Programs

## IMPORTANT DATES:

Paper Submission	:October 28, 2022
Acceptance Notification	:October 29, 2022
Camera Ready	:November 01, 2022
Conference Dates	:November 04 & 05, 2022

## CONTACT US

- ☎ +91 94441 50585
- ☎ +91 96774 77905
- ☎ +91 99441 61284
- ✉ [eeeconference@aubit.edu.in](mailto:eeeconference@aubit.edu.in)
- 🌐 [eee-aubit.com](http://eee-aubit.com),



## National Conference on Emerging Trends in Electrical, Electronics and Communication Engineering (NCEECE-2022)

On  
4th & 5th November 2022



Organized by

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING,  
UCE-BIT CAMPUS,  
ANNA UNIVERSITY, TIRUCHIRAPPALLI - 620 024

# ABOUT US

## OUR INSTITUTION

The Anna University, BIT Campus, Tiruchirappalli, has been established by the Government of Tamil Nadu as a "Technical Institution" at Tiruchirappalli, known as Rock Fort City, with a rich cultural heritage and home to many reputed educational institutions. The BIT institution is one of the constituent colleges of Anna University, Chennai. The Institution was established with a view to imparting quality student-centered technical education so as to produce proven technocrats and to strive hard for continuous development and improvement in learning, research and developmental activities. Teaching, research, and extension services are carried through the departments and centres of the institution. The Institution also works towards fostering partnerships with academic and industrial experts to provide quality education and opportunities for entrepreneurial development by providing hands-on training. BIT has carved a niche for itself in providing technical education in the disciplines that are rare of their kind in our country.

## OUR DEPARTMENT

The Department of Electronics and Electronics Engineering (EEE) has been a pioneer in the field of Electrical and Electronics Engineering at Bharathidasan Institute of Technology since 2007. It offers UG and PG courses, with core research and development in power apparatus and systems for many decades. The research specializations of the department are Power Systems, High Voltage, Power Electronics and Drives, Control and Instrumentation, Embedded System Technology, and Power Engineering and Management.

# ABOUT CONFERENCE

## OBJECTIVE

The aim of the conference is to bring out the calibre in young researcher in the field of Electric power and computational engineering, sets a stage for them to share their ideas, innovation and knowledge, that will help them to reshape their work and modify according to the feedback from the excellence resource persons and co-researchers. The major objectives of the conference are

- To create the technical forum for young scientist and PhD Scholars.
- To develop new ideas for interdisciplinary works.
- To collaborate with various research laboratory and institutions
- To enhance the knowledge to meet industry needs and standards
- To enrich the research standards

Young researchers are invited to join the conference on a concession fee.

## PROGRAM OUTLINE

This conference is to provide opportunities for the different area delegates to exchange new ideas and application experiences face to face, to establish business or research relations and to find global partners for future collaboration. We hope that the conference results constituted significant contribution to the knowledge in these up to date scientific field.

# ORGANIZING COMMITTEE

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