REGISTRATION FORM

8th NATIONAL CONFERENCE ON
SIGNAL PROCESSING COMMUNICATION
& VLSI DESIGN
NCSCV’ 16

29th & 30th April 2016

Name:
Designation:
Institution:
Address for Communication:
Contact No:
E-Mail id:

CATEGORY
Student* ☐
Research Scholar ☐
Faculty ☐
delegate from Industry ☐

PAYMENT DETAILS
Amount:
DD Number:
Bank:
Date:

Signature:

*Note: Bonafide Certificate with College seal to be attached

ORGANIZED BY

8th NATIONAL CONFERENCE ON
SIGNAL PROCESSING COMMUNICATION
& VLSI DESIGN
NCSCV’ 16

29th & 30th April 2016

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Vice Chancellor
Anna University, Chennai

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Anna University, Regional Campus, Coimbatore

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Anna University, Regional Campus, Coimbatore.
Anna University was established on 4th September 1978 as a unitary type of University. It offers higher education in Engineering, Technology and allied Sciences relevant to the current and projected needs of the society. Besides promoting research and disseminating knowledge gained there from, it fosters cooperation between the academic and industrial communities. Anna University is of the affiliated type and is a member of the Association of Indian Universities, the Association of Commonwealth Universities and Partner of UNESCO International Center for Engineering Education (UICEE). With proven capabilities both in academic and research areas, Anna University was able to impart quality and excellence in Technical Education. Anna University has four regional centres at Coimbatore, Madurai, Tiruchirapalli and Tirunelveli to sphere head the academic activities throughout the state.

The aim of NCSCV’16 is to provide an opportunity for students, research scholars, academicians and industry professionals for sharing and exploring new areas of research and development and to discuss emerging issues and novel methods. It will provide a gateway to enhance their knowledge in the area of Signal Processing, VLSI design and other related fields. It will be a great chance for the people from various disciplines to share new ideas in the below mentioned areas.

Original Research papers are invited in the following areas but not limited to:
- Ad Hoc / Sensor Networks
- Antenna & Wave Propagation
- ASIC Design
- Biomedical Imaging
- CAD VLSI
- Data Mining
- Embedded/RTOS Applications
- Image/Video Processing
- Low Power VLSI/VLSI Testing
- Mobile/ Satellite Communications
- Nano Electronics
- Network Security and Management
- Pattern Recognition
- RF Design & Microwave Communication
- Soft Computing Techniques
- VLSI/Reconfigurable Architectures
- Wavelet Transform & Applications
- Wireless Communications
- Internet of Things (IoT)

Full Paper must be submitted in IEEE double column format (soft copy in MS-Word as E-mail attachment) must not exceed 5 pages. After acceptance, 2 hard copies of the paper along with Registration Fee in the form of Demand Draft should be submitted.

Best Papers will be forwarded to ICTACT Journal of IMAGE & VIDEO Processing and Soft Computing DD should be drawn in favour of “The Organizing Secretary-NCSCV” Payable at Coimbatore, Tamil Nadu, India. Registration fee doesn’t include accommodation charges. No TA/DA will be paid to the participants

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