

ABOUT THE INSTITUTE

Madras Institute of Technology is one of the premier technical institutions started in the year 1949 by Shri. C. Rajam, an eminent industrialist. The institute was the result of a bold experiment in technical education as it introduced for the first time, totally unconventional Engineering courses such as Aeronautical Engineering, Automobile Engineering, Electronics Engineering and Instrumentation Engineering. It was merged with Anna University in the year 1978.

ABOUT THE DEPARTMENT

The Department of Electronics Engineering established in the year 1949, has its core strength in the leading areas of Electronics & Communication technology. The academic programmes offered in the Department are B.E (Electronics and Communication Engineering), M.E (Communication and Networking, VLSI Design and Embedded Systems & Wireless Technologies). The cutting edge research areas include Image Processing & Pattern Recognition, Communication Technologies, Network Security, Sensor Networks, Optical Communication, Signal Processing, and VLSI. The Department has collaborative partners from academia and industry both within India and worldwide.

LOCATION

Madras Institute of Technology is located in Chrompet, Chennai, Tamil Nadu, India and the campus is very close to Chrompet railway station.

ABOUT THE RO-FDTP

The RO-FDTP is intended to provide opportunity for faculty members and scholars registered for Ph.D in Anna University to enhance their knowledge in the area of VLSI and Communications and will help in identifying the research problem.

COURSE COVERAGE

- The following topics will be covered
- Research Problem Identification and Solving methods
- Research Methodologies in Advanced Communications
- Challenges in Communication, Signal Processing and Biomedical Signal Processing
- Research Issues in VLSI Technologies & Image Processing
- Development towards Wireless Technologies
- Research Data Analytics
- Art of writing research papers

ABOUT REGISTRATION

- Number of seats is restricted to 30.
- Selection on first come first serve basis.
- Faculty members and scholars registered for PhD programme in Anna University alone are eligible to attend this programme
- Selected candidates will be intimated by e-mail. Confirmation of participation is to be made by email.
- No TA/DA will be paid.
- No Registration Fee.
- No accommodation will be provided.

Send the duly filled in Registration forms to the Coordinator(s) by post.

TWO DAY RESEARCH ORIENTED FACULTY DEVELOPMENT TRAINING PROGRAMME (UGC-XII Plan)

on

Research Challenges in VLSI and Communications 6th & 7th February 2020

Co-ordinators

Dr. M.Kannan, Professor

Dr. K.Mariammal, Assistant Professor



Sponsored by
**Centre for Faculty Development,
ANNA UNIVERSITY
Chennai**

Organized by

**Department of Electronics Engineering
Madras Institute of Technology Campus,
Anna University,
Chennai – 600 044.**

**TWO DAY RO-FDTP
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**Research Challenges in VLSI and
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6th & 7th February 2020

**CERTIFICATE FROM
HEAD OF THE INSTITUTION**

Dr./Mr./Mrs. _____ is a
faculty member/ research Scholar of our
institution. He/She will be permitted to
attend the programme if selected.

Signature of the Principal with seal

For further details, contact:

The Co-ordinator
RO-FDTP on Research Challenges in VLSI
and Communications

Dept. of Electronics Engineering,

MIT Campus,

Anna University Chennai

Chennai – 600 044

Phone : 044-22516236/6238

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ORGANIZING COMMITTEE

CHIEF PATRON:

Dr.M.K. Surappa
Vice-Chancellor
Anna University
Chennai

PATRON:

Dr.L.Karunamoorthy
Registrar
Anna University
Chennai

Dr.T.Thyagarajan
Dean, MIT Campus
Anna University

CHAIR:

Dr.D.Sridharan
Director, CFD

Dr.I.Arul Aram
Addl.Director, CFD

CO-CHAIR: Dr.M.Ganesh Madhan
Electronics Engineering

Co-ordinators:

Dr.M.Kannan
Dr. K. Mariammal

Venue:

Seminar Hall
Electronics Engineering,
MIT Campus, Chromepet.

IMPORTANT DATES:

Submission of Application: 27.01.2020

Intimation of Selection: 28.01.2020

Confirmation by Participants: 29.01.2020

1. Name :
(in capital)

2. Designation :

3. Qualification :

4. Age & D.O.B:

5. Professional experience(in years)
Teaching :
Industrial :

6.Community:

7. Address of the College/institution :

8. Research interest :

9. Phone (Office) :
(Mobile) :

10. E-mail :

I hereby declare that all the details furnished
by me are true to the best of my knowledge.

Signature of the Applicant