SPONSORSHIP CERTIFICATE

Certified that Dr./Prof./Mr./Ms. ______________ is a bonafide faculty member of our Engineering College. He /She is identified to teach the subject “VLSI DESIGN”. He/She is sponsored to attend this FDP programme.

Date :

Place :

Signature & Seal of the Principal

HOW TO REGISTER?
Registration form given in the brochure or copy of this form can be used for registration.

IMPORTANT DATES

Last Date for Registration : 05.12.2012

Intimation of Acceptance : 06.12.2012
(Through E-mail only)

CONTACT ADDRESS
For any queries and to send completed registration forms:

Dr. V.R.VIJAYKUMAR
Coordinator
FDP on VLSI Design
Department of ECE
Anna University
Regional Centre, Coimbatore
Coimbatore – 641 047
Mobile Nos.: 09442014139, 09865285501,
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Web : www.annauniv.edu

FACULTY DEVELOPMENT PROGRAMME ON

VLSI DESIGN
12th -18th, DECEMBER 2012

SPONSORED BY

CENTRE FOR FACULTY DEVELOPMENT
ANNA UNIVERSITY, CHENNAI

CO-ORDINATORS

Dr. V.R.Vijaykumar
Mr. S.Saravanakumar

ORGANISED BY

Department of Electronics and Communication Engineering

Anna University
Regional Centre, Coimbatore
Coimbatore – 641 047
ABOUT THE COURSE

The objective of this Faculty Development Programme is to give a better exposure to the faculty members on the subject “VLSI DESIGN”. The course covers detailed theory, tutorials and laboratory practices.

INTRODUCTION TO CMOS TECHNOLOGY

A brief History-MOS transistor, Ideal I-V characteristics, C-V characteristics, Non ideal I-V effects, DC transfer characteristics - CMOS technologies, Layout design Rules, CMOS process enhancements, Technology related CAD issues, Manufacturing issues.

CIRCUIT CHARACTERIZATION AND SIMULATION

Delay estimation, Logical effort and Transistor sizing, Power dissipation, Interconnect, Design margin, Reliability, Scaling- SPICE tutorial, Device models, Device characterization, Circuit characterization, Interconnect simulation.

COMBINATIONAL AND SEQUENTIAL CIRCUIT DESIGN

Circuit families –Low power logic design – comparison of circuit families – Sequencing static circuits, circuit design of latches and flip flops, Static sequencing element methodology- sequencing dynamic circuits – synchronizers.

CMOS TESTING

Need for testing- Testers, Text fixtures and test programs- Logic verification- Silicon debug principles- Manufacturing test – Design for testability – Boundary scan.

SPECIFICATION USING VERILOG HDL

Basic concepts- identifiers- Data flow and RTL, structural gate level switch level modeling, Design hierarchies, Behavioral and RTL modeling, Test benches, Structural gate level description of decoder, equality detector, comparator, priority encoder, half adder, full adder, Ripple carry adder, D latch and D flip flop.

ABOUT THE DEPARTMENT

The Coimbatore Regional Centre of Anna University has nine PG departments and it offers twenty one PG Programmes. The Department offers M.E degree courses in Applied Electronics, Communication Systems, Embedded System Technologies and VLSI Design both full time and part time mode. The Department is equipped with excellent infrastructure and state-of-the-art equipment to explore the technological challenges in the areas VLSI Design, Signal Processing, Image Processing, RF Design and Digital System Design.

RESOURCE PERSONS

Subject experts from AU, PSG Tech, GCT, AMRITA Coimbatore and Industries.

ELIGIBILITY

The course is open to teachers from Affiliated Engineering Colleges in Tamil Nadu.

COURSE FEE

No course fee for the first 25 registrants. Applicants from affiliated colleges of Anna University will be given preference. Additional participants may be considered provided each additional participant pays Rs.1500/- as course fee in the form of Demand Draft in favour of “IEEE STUDENT BRANCH-AUCBE”, Department of ECE, Anna University, Regional Centre, Coimbatore.

VENUE:

MBA Seminar Hall ,
Anna University
Regional Centre, Coimbatore
Coimbatore - 641 047.

FACULTY DEVELOPMENT PROGRAMME ON VLSI DESIGN

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REGISTRATION FORM

1. Name :

2. Designation :

3. Qualification:

4. Age and Date of Birth:

5. Experience:

6. Email ID &Mobile No.:

7. Institution Address:

8. Institute Affiliated to Anna University: Yes/No

9. Address for Communication:

10. Accommodation Required: Yes/No

Date :
Place :

Signature of the Applicant