

Seven Days FDTP on
“VLSI DESIGN – EC6601”
20th – 26th November 2017
Registration Form

Name (In Capitals) :
Designation :
Qualification :
Department :
Institution :
Address :

Teaching :
Experience :
(in years)
E-Mail :
Mobile :

Declaration

The information provided is true to the best of my knowledge. I agree to abide by the rules and regulations governing the FDTP. If selected, I shall attend the program for entire duration.

Date:
Place:

Signature of the Applicant

Signature of Head of the Dept. / Institution
(with seal)

Target Audience

Faculty Members from Engineering Colleges affiliated to Anna University.

Resource Persons

Experts from Academia and Industry.

About the Registration

- Number of seats is restricted to 25.
- Selection on “First Come First Serve” basis.
- Preference will be given to Young faculty.
- Selected applicants will be intimated only by e-mail.
- No TA/DA will be paid.
- No Registration Fee.
- No accommodation will be provided.

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Send the duly filled in Registration form to the Co-ordinator(s) by E-mail or post to,

**The Co-ordinator,**  
Seven Days FDTP on  
**“VLSI Design – EC6601”**  
Department of Electronics Engineering,  
MIT Campus, Anna University,  
Chromepet, Chennai – 600044.

For further details:  
E-Mail: vlsielex17@gmail.com  
Mobile: 8015823900

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Seven Days FDTP
on
“VLSI DESIGN”
(EC 6601)

20th – 26th Nov. 2017

Co-ordinator
Dr. M. Kannan



Sponsored by
Anna University
Chennai

Organized by
Department of Electronics Engineering,
Madras Institute of Technology Campus,
Anna University, Chennai – 600044.

About Anna University Chennai

Anna University was established on 4th September 1978 as unitary type of University. Its constituent colleges are College of Engineering, Madras Institute of Technology, Alagappa College of Technology and School of Architecture and Planning. It offers higher education in Engineering, Technology and allied Sciences that are relevant to the present needs of the society. Anna University has a strong vision to impart excellence in education, research and societal service through highly dedicated and competent teachers who are also expert researchers in various emerging and cutting edge technologies. Besides promoting research and disseminating knowledge gained there from, it fosters cooperation between the academic and industrial communities.

About Madras Institute of Technology

Madras Institute of Technology is a pioneer institute established by the great visionary and industrialist Late Shri C. Rajam on 18th of July 1949 and it merged with Anna University in the year 1978. The educational practice of this institution is application oriented, thereby helping the students make a smooth transition from college to the industrial world. This is reflected in the profile of vast number of MIT alumni occupying key positions in various MNCs and Research Organizations across the globe. Shri Dr. APJ Abdul Kalam, our former president of India, is an alumnus of MIT.

About Electronics Engineering

The Department of Electronics Engineering established in the year 1949 has its core strength in the area of Electronics & Communication technology. The cutting edge research areas include Communication Technologies Wireless Communication, Signal Processing, Image Processing & Pattern Recognition, Network Security, Sensor Networks, Optical Communication, and VLSI. The department is supported by DST - FIST & UGC –DRS-SAP Phase II program.

About the FDTP

The FDTP is proposed to provide opportunity for young faculty members to enhance their knowledge in the area of VLSI Design.

This FDTP is an endeavor to support Anna University affiliated colleges and faculty members to handle VLSI Design and also to offer hands-on training in VLSI domain.

The contents of the course includes,

- Fundamentals of VLSI design
- MOS transistor principles
- Stick and layout diagrams
- Combinational circuits
- Sequential circuits
- Memory architectures
- Arithmetic building blocks
- ASIC design
- FPGA routing procedures
- Altera based system design

Organizing Committee

Chief Patron

Vice Chancellor

Anna University, Chennai

Patrons

Dr. S. Ganesan

Registrar, Anna University, Chennai

Dr. A. Rajadurai

Dean – MITC, Anna University, Chennai

Chair

Dr. K. Shanthy

Director, CFD

Dr. D. Sridharan

Addl. Director, CFD

Co-Chair

Dr. M. Kannan

Associate Professor & Head,
Dept. of Electronics Engineering,
MIT Campus, Anna University

Co-ordinator

Dr. M. Kannan

Associate Professor & Head,
Dept. of Electronics Engineering,
MIT Campus, Anna University

Venue

Seminar Hall

Dept. of Electronics Engineering,
MIT Campus, Anna University

Important Dates

Submission of Application : 17.11.2017
Intimation of Application : 17.11.2017
Confirmation by Participants : 18.11.2017