About the Institute

Madras Institute of Technology is one of the premier technical institutions started in the year 1949 by Shri. C. Rajam, an eminent industrialist. The institute was the result of a bold experiment in technical education as it introduced for the first time, totally unconventional Engineering courses such as Aeronautical Engineering, Automobile Engineering, Electronics Engineering and Instrumentation Engineering. It was merged with Anna University in the year 1978.

About the Department

The Department of Electronics Engineering established in the year 1949, has its core strength in the leading areas of Electronics & Communication technology. The academic programmes offered in the Department are B.E (Electronics and Communication Engineering), M.E (Communication and Networking). The cutting edge research areas include VLSI, Image Processing & Pattern Recognition, Communication Technologies, Network Security, Sensor Networks, Optical Communication and Signal Processing.

Location

Madras Institute of Technology is located in Chrompet, Chennai and close to Chrompet railway station.

About the seminar

The two-day seminar is intended to provide opportunity for faculty members and research scholars to enrich their knowledge in the area of analog design.

This seminar will focus on recent advances in analog IC design. The seminar will provide a vibrant opportunity for researchers and faculty members. The following topics will be covered on this seminar.

- Challenges in Analog Integrated Circuit Design
- Integrated Circuit Design
- Design Issues of Op-amp
- Design of SMPS
- Concepts of ADCs.
- IC Design using cadence tool.

Resource Persons

- Academics and Industry Experts

REGISTRATION

1. Name (in BLOCK letters) :

2. Designation, Department and Address of the College/institution :

3. Academic Qualification :

4. Research interest :

5. Communication Details :

   Office Phone :

   Phone/Mobile :

   E-mail :

   I hereby declare that all the details furnished by me are true to the best of my knowledge.

   Signature
CERTIFICATE FROM HEAD OF THE INSTITUTION

Dr./Mr./Mrs. _________________________

is an employee / research scholar of our institution. He/She will be permitted to attend the programme if selected.

Signature of Head of the institution with seal

Registration

NO REGISTRATION FEE.

Registration will be done on first come first serve basis. As limited seats are available, early registration is recommended.

No accommodation will be provided.

Send the duly filled in Registration forms to the Co-ordinator.

Targeted Participants

- Faculty from Engineering Colleges
- Research Scholars

Important Dates

Last date for the receipt of application 07.09.2012

Intimation of selection will be sent only through e-mail on or before 10.09.2012.

UGC Sponsored
TWO DAYS SEMINAR
On
“ANALOG IC DESIGN”
14th & 15th September, 2012

Co-ordinators
Dr. D. Meganathan
Dr. S.P. Joy Vasantha Rani

For further details, contact:
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Dept. of Electronics Engineering, MIT Campus, Anna University, Chennai – 600 044

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E-mail: analogdesign2012@gmail.com

Organized by
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