Recruitment of Project Associates for Full Spectrum Simulator Project under NaMPET

NaMPET project intends to develop a FULL SPECTRUM SIMULATOR (FSS), which will serve as a platform for the Real-time design and simulation of circuits and systems in Power Electronics/Power systems and enable Hardware-in-loop testing.

This project is a joint proposal by combining the knowledge of academic institutes viz, IISc, IIT-Bombay, IIT-Kanpur, IIT-Kharagpur, Bengal Engineering College, Anna University besides C-DAC and CPRI.

Applications are invited for the post of project associates as per the details given below.

<table>
<thead>
<tr>
<th>Sl No.</th>
<th>Post Name</th>
<th>Qualification</th>
<th>Experience</th>
<th>Period of the Project</th>
<th>Salary</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>Project Associate (A)</td>
<td>M.E. * (with First class)</td>
<td>Programming and Hardware experience will be given preference</td>
<td>Six Months **</td>
<td>Rs 8000 ** (Consolidated)</td>
</tr>
<tr>
<td>2.</td>
<td>Project Associate (B)</td>
<td>B.E. * or Diploma in EEE</td>
<td>Hardware experience will be given preference</td>
<td>Six Months **</td>
<td>Rs 5000 ** (Consolidated)</td>
</tr>
</tbody>
</table>

* with B.E. degree in Electrical and Electronics Engineering. ** Period may be subsequently extended for one more year based on the performance of the candidate. *** Deserving candidates will be considered for a higher starting salary.

Candidates are requested to submit their Bio-data (by post or thro’ e-mail) to the principal investigator of the project on or before November 20, 2006.

S.Hosimin Thilagar (Principal Investigator for FSS/NaMPET )

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